

Pin	Name	Direction	PIN Description
1	NPOR	I Pull up	Power on Reset
2	SDT_A6	O	SDRAM/SRAM addr[6]
3	SDT_A5	O	SDRAM/SRAM addr[5]
4	SDT_A4	O	SDRAM/SRAM addr[4]
5	SDT_A3	O	SDRAM/SRAM addr[3]
6	SDT_A2	O	SDRAM/SRAM data[2]
7	SDT_A1	O	SDRAM/SRAM data[1]
8	SDT_A0	O	SDRAM/SRAM data[0]
9	LCD_D0	O	lcd data[0]
10	LCD_D1	O	lcd data[1]
11	LCD_D2	O	lcd data[2]
12	LCD_D3	O	lcd data[3]
13	VDDIO	P	IO POWER SUPPLY 3.3V
14	VSS	P	GROUND
15	XIN24M	I OSC	Crystal 24M input
16	XOUT24M	O OSC	Crystal 24M output
17	VDD	P	CORE POWER SUPPLY 1.2V
18	LCD_D4	O	lcd data[4]
19	LCD_D5	O	lcd data[5]
20	LCD_D6	O	lcd data[6]
21	LCD_D7	O	lcd data[7]
22	PE0/LCD_D8	IO Pull up/O	GPIO E0/LCD data[8]
23	PE1/LCD_D9	IO Pull up/O	GPIO E1/LCD data[9]
24	PE2/LCD_D10	IO Pull up/O	GPIO E2/LCD data[10]
25	PE3/LCD_D11	IO Pull up/O	GPIO E3/LCD data[11]
26	PE4/LCD_D12	IO Pull up/O	GPIO E4/LCD data[12]
27	PE5/LCD_D13	IO Pull up/O	GPIO E5/LCD data[13]
28	PE6/LCD_D14	IO Pull up/O	GPIO E6/LCD data[14]
29	PE7/LCD_D15	IO Pull up/O	GPIO E7/LCD data[15]
30	PA0/LCD_D16/RXD0	IO Pull up/O/O	GPIO A1/LCD data[16]/UART0 rxd
31	PA1/LCD_D17/TXD0	IO Pull up/O/O	GPIO A1/LCD data[17]/UART0 txd
32	LCD_CLK/LCD_RS	O	RGB dot clock / MUC panel
33	LCD_HSYNC	O	RGB HSYNC/MCU_WR
34	PA7/LCD_VSYNC	IO Pull up/O	GPIO A7/RGB_VSYNC/MCU_CS
35	FLASH_RDY	I pull up	NAND FLASH R/B
36	FLASH_RDN	O	NAND FLASH RD
37	FLASH_CS0	O	NAND FLASH CS0
38	FLASH_D7	B	nand flash data[7]
39	FLASH_D6	B	nand flash data[6]
40	FLASH_D5	B	nand flash data[5]
41	FLASH_D4	B	nand flash data[4]
42	FLASH_D3	B	nand flash data[3]
43	FLASH_D2	B	nand flash data[2]
44	FLASH_D1	B	nand flash data[1]
45	FLASH_D0	B	nand flash data[0]
46	FLASH_CLE	O	nand flash cle
47	FLASH_ALE	O	nand flash ale
48	FLASH_WRN	O	NAND FLASH WR
49	VDD	P	CORE POWER SUPPLY 1.2V
50	VSS	P	GROUND
51	VDDIO	P	IO POWER SUPPLY 3.3V
52	PB2/SDDATA0/SPI_MISO	IO Pull up/B/B	GPIO B2/SD DATA OUT/SPI_MISO

53	PB3/SDCMD/SPI_MOSI	IO Pull up/B/B	GPIO B3/SD DATA IN/SPI_MOSI
54	PB5/SDCLK/SPI_CLK	IO Pull up/O/O	GPIO B3/SD CLK/SPI_CLK
55	CODEC_AIL1	AI	L-channel analog input 1
56	CODEC_AIR1	AI	R-channel analog input 1
57	CODEC_MIC	AI	Mic input
58	CODEC_VCOM	AO	Internal biasing voltage
59	CODEC_VSSA	P	Ground for Codec
60	CODEC_VDDA	P	Power supply for CODEC, 3.3V
61	CODEC_AOHPL	AO	L-channel headphone output
62	CODEC_VSSAO	P	Ground for amplifiers
63	CODEC_VDDAO	P	Power supply for amplifiers 3.3V
64	CODEC_AOHPR	AO	R-channel headphone output
65	PA5/FLASH_CS1	IO Pull up/O	GPIO A5/FLASH CS1
66	SDA/FLASH_CS3/PB7	IO Pull up/O/O	SCL/NAND FLASH CS3/GPIOB7
67	SCL/FLASH_CS2/PB6	IO Pull up/O/O	SCL/NAND FLASH CS2/GPIOB6
68	PC0	IO Pull down	GPIO C0
69	PC1/LCD_DEN	IO Pull down/O	GPIO C1/RGB DEN
70	PC2/I2S_SCLK	IO Pull down/B	GPIO C2/I2S SCLK
71	PC3/I2S_LRCK	IO Pull down/B	GPIO C3/I2S LRCK
72	PC4/I2S_SDI	IO Pull down/I	GPIO C4/I2S DATA IN
73	PC5/I2S_SDO	IO Pull down/O	GPIO C5/I2S DATA OUT
74	PC6/I2SCLK	IO Pull down/O	GPIO C6/I2S CLOCK OUT
75	PD3/SD_CKE	IO Pull up/O	GPIO D3/SDRAM CKE
76	PD4/PWM0	IO /O	GPIO D4/PWM0
77	VDDIO	P	IO POWER SUPPLY 3.3V
78	VSS	P	GROUND
79	VDD	P	CORE POWER SUPPLY 1.2V
80	SDT_D15	B	SDRAM/SRAM data[15]
81	SDT_D14	B	SDRAM/SRAM data[14]
82	SDT_D13	B	SDRAM/SRAM data[13]
83	SDT_D12	B	SDRAM/SRAM data[12]
84	SDT_D11	B	SDRAM/SRAM data[11]
85	VBUS_DET	I Pull down	USB VBUS detect
86	LADC_AIN0	A	10bit adc channel0 input
87	LADC_AIN1	A	10bit adc channel1 input
88	LADC_AIN2	A	10bit adc channel2 input
89	LADC_VSSA	P	10bit adc analog ground
90	PHY_VDDA	P	USB PHY Analog Power 3.0V-3.3V
91	PHY_DN	A	USB DN
92	PHY_DP	A	USB DP
93	PHY_VSSA	P	USB PHY Analog Ground
94	PHY_REF	A	Resistor for USB 6.04K 1%
95	PHY_VDDP	P	USB PHY PLL POWER 1.2V
96	PHY_VSSP	P	USB PHY PLL Ground
97	SDT_D10	B	SDRAM/SRAM data[10]
98	SDT_D9	B	SDRAM/SRAM data[8]
99	SDT_D8	B	SDRAM/SRAM data[8]
100	SDT_D7	B	SDRAM/SRAM data[7]
101	SDT_D6	B	SDRAM/SRAM data[6]
102	SDT_D5	B	SDRAM/SRAM data[5]
103	SDT_D4	B	SDRAM/SRAM data[4]
104	SDT_D3	B	SDRAM/SRAM data[3]
105	SDT_D2	B	SDRAM/SRAM data[2]

106	SDT_D1	B	SDRAM/SRAM data[1]
107	SDT_D0	B	SDRAM/SRAM data[0]
108	SD_DQM1	O	SDRAM dqm[1]
109	SD_DQM0	O	SDRAM dqm[0]
110	SD_WEN	O	SDRAM wen
111	VDDIO	P	IO POWER SUPPLY 3.3V
112	VSS	P	GROUND
113	VDD	P	CORE POWER SUPPLY 1.2V
114	SD_CASN	O	SDRAM casn
115	SD_RASN	O	SDRAM rasn
116	SD_CLK	O	SDRAM clkout
117	SD_CSN	O	SDRAM csn
118	SD_BA0	O	SDRAM ba[0]
119	SD_BA1	O	SDRAM ba[1]
120	PD7	IO Pull down	GPIO D7
121	SDT_A11/PF1	O/IO Pull up	SDRAM/SRAM addr[11]/GPIOF1
122	SDT_A10	O	SDRAM/SRAM addr[10]
123	SDT_A9	O	SDRAM/SRAM addr[9]
124	SDT_A8	O	SDRAM/SRAM addr[8]
125	SDT_A7	O	SDRAM/SRAM addr[7]
126	ZPLL_VDDA	P	DSP PLL power 1.2V
127	PLL_VSSA	P	DSP PLL Ground
128	APLL_VDDA	P	ARM PLL power 1.2V