



M30245FCGP Pinout

12	VSS1	36	P60/CTS0/RTS0/SS0/WS0
62	VSS1	35	P61/CLK0/SCK0
94	AVSS	34	P62/RxD0/SCL0/STxD0/SRxD0
		34	P62/RxD0/SCL0/STxD0/SRxD0
		33	P63/TxD0/SDA0/SRxD0/STxD0
		32	P64/CTS1/RTS1/SS1/WS1
		31	P65/CLK1/SCK1
		30	P66/RxD1/SCL1/STxD1/SRxD1
		29	P67/TxD1/SDA1/SRxD1/STxD1
		28	P70/TxD2/SDA2/SRxD2/TA0out/LED0
		27	P71/RxD2/SCL2/STxD2/TA0in/LED1
		26	P72/CLK2/TA1out/SCK2/LED2
		25	P73/CTS2/RTS2/SS2/TA1in/LED3
		24	P74/TxD3/SDA3/SRxD3/TA2out/LED4
		23	P75/RxD3/SCL3/STxD3/TA2in/LED5
		22	P76/CLK3/SCK3/TA3out/LED6
		21	P77/CTS3/RTS3/SS3/TA3in/LED7
		20	P80/TA4out
		19	P81/TA4in
		18	P82/INT0
		17	P83/INT1
		16	P84/INT2
		9	P86/XCout
		8	P87/XCin
		88	P100/AN0/KI0
		93	P101/AN1/KI1
		92	P102/AN2/KI2
		91	P103/AN3/KI3
		90	P104/AN4/KI4
		89	P105/AN5/KI5
		88	P106/AN6/KI6
		87	P107/AN7/KI7
		100	P90/ATTACH
		99	P92/SOF
		99	P93/ADTrg
		2	UVcc
		3	VbusDTCT
		4	USB D+
		5	USB D-
		95	LPF
		10	RESET
		11	Xout
		13	Xin
		15	NMI
		6	BYTE
		7	CNVss
		79	D15

Header 40x2 Pinout

1	A0	21	D10
2	A1	22	D11
3	A2	23	D12
4	A3	24	D13
5	A4	25	D14
6	A5	26	D15
7	A6	27	P10.4
8	A7	28	P10.5
9	A8	29	P10.6
10	A9	30	P10.7
11	A10	31	P10.8
12	A11	32	P10.9
13	A12	33	P10.10
14	A13	34	P10.11
15	A14	35	P10.12
16	A15	36	P10.13
17	A16	37	P10.14
18	A17	38	P10.15
19	A18	39	P10.16
20	A19	40	P10.17
21	A20	41	P10.18
22	A21	42	P10.19
23	A22	43	P10.20
24	A23	44	P10.21
25	A24	45	P10.22
26	A25	46	P10.23
27	A26	47	P10.24
28	A27	48	P10.25
29	A28	49	P10.26
30	A29	50	P10.27
31	A30	51	P10.28
32	A31	52	P10.29
33	A32	53	P10.30
34	A33	54	P10.31
35	A34	55	P10.32
36	A35	56	P10.33
37	A36	57	P10.34
38	A37	58	P10.35
39	A38	59	P10.36
40	A39	60	P10.37
41	A40	61	P10.38
42	A41	62	P10.39
43	A42	63	P10.40
44	A43	64	P10.41
45	A44	65	P10.42
46	A45	66	P10.43
47	A46	67	P10.44
48	A47	68	P10.45
49	A48	69	P10.46
50	A49	70	P10.47
51	A50	71	P10.48
52	A51	72	P10.49
53	A52	73	P10.50
54	A53	74	P10.51
55	A54	75	P10.52
56	A55	76	P10.53
57	A56	77	P10.54
58	A57	78	P10.55
59	A58	79	P10.56
60	A59	80	P10.57

NOTES:

Single-Chip Mode:
 S5_2 (open) - CNVss to GND
 S5_1 (open) - BYTE to GND

Microprocessor Mode:
 S5_2 (closed) - CNVss to Vcc
 S5_3 (open) - HOLD to Vcc
 S5_4 (closed) - RDY to Vcc

Boot Mode:
 S5_2 (closed) - CNVss to Vcc
 S5_3 (closed) - EPM to GND
 S5_5 (closed) - CE to Vcc