

```

}
void wdt_off ()
{
    __watchdog_reset ();
    MCUSR &= ~(1<<WDRF);
    WDTCSR |= (1<<WDCE) | (1<<WDE);
    WDTCSR = 0x00;
}

void main()

wdt_init (); //Initialisiere Watchdog

init_usart (); //ADC initialisieren

```

```

000062 E00E LDI R16,0x0E
000064 9300 0060 STS WDTCSR,R16
}
000068 9508 RET
__watchdog_reset ();
wdt_off:
00006A 95A8 WDR
MCUSR &= ~(1<<WDRF);
00006C B704 IN R16,MCUSR
00006E 7F07 ANDI R16,0xF7
000070 BF04 OUT MCUSR,R16
WDTCSR |= (1<<WDCE) | (1<<WDE);
000072 9100 0060 LDS R16,WDTCSR
000076 6108 ORI R16,0x18
000078 9300 0060 STS WDTCSR,R16
WDTCSR = 0x00;
00007C E000 LDI R16,0x00
00007E 9300 0060 STS WDTCSR,R16
}
-----

```

