

3.3.2 J3, Peripheral Expansion Connector

Table 3: J3, Peripheral Expansion Connector

Pin	Signal	I/O	Description	Pin	Signal	I/O	Description
1	12V	Vcc	12V voltage supply pin	2	-12V	Vcc	-12V voltage supply pin
3	GND	Vss	System ground	4	GND	Vss	System ground
5	5V	Vcc	5V voltage supply pin	6	5V	Vcc	5V voltage supply pin
7	GND	Vss	System ground	8	GND	Vss	System ground
9	5V	Vcc	5V voltage supply pin	10	5V	Vcc	5V voltage supply pin
11	N/C	-	No connect	12	N/C	-	No connect
13	N/C	-	No connect	14	N/C	-	No connect
15	N/C	-	No connect	16	N/C	-	No connect
17	N/C	-	No connect	18	N/C	-	No connect
19	3.3V	Vcc	3.3V voltage supply pin	20	3.3V	Vcc	3.3V voltage supply pin
21	CLKX0	I/O	McBSP0 transmit clock	22	CLKS0	I	McBSP0 clock source
23	FSX0	I/O	McBSP0 transmit frame sync	24	DX0	O	McBSP0 transmit data
25	GND	Vss	System ground	26	GND	Vss	System ground
27	CLKR0	I/O	McBSP0 receive clock	28	N/C	-	No connect
29	FSR0	I/O	McBSP0 receive frame sync	30	DR0	I	McBSP0 receive data
31	GND	Vss	System ground	32	GND	Vss	System ground
33	CLKX1	I/O	McBSP1 transmit clock	34	CLKS1	I	McBSP1 clock source
35	FSX1	I/O	McBSP1 transmit frame sync	36	DX1	O	McBSP1 transmit data
37	GND	Vss	System ground	38	GND	Vss	System ground
39	CLKR1	I/O	McBSP1 receive clock	40	N/C	-	No connect
41	FSR1	I/O	McBSP1 receive frame sync	42	DR1	I	McBSP1 receive data
43	GND	Vss	System ground	44	GND	Vss	System ground
45	TOUT0	O	Timer 0 output	46	TINP0	I	Timer 0 input
47	N/C	-	No connect	48	EXT_INT5	I	External interrupt 5
49	TOUT1	O	Timer 1 output	50	TINP1	I	Timer 1 input
51	GND	Vss	System ground	52	GND	Vss	System ground
53	EXT_INT4	I	External interrupt 4	54	N/C	-	No connect
55	N/C	-	No connect	56	N/C	-	No connect
57	N/C	-	No connect	58	N/C	-	No connect
59	RESET	O	System reset	60	N/C	-	No connect
61	GND	Vss	System ground	62	GND	Vss	System ground
63	CNTL1	O	Daughtercard control 1	64	CNTL0	O	Daughtercard control
65	STAT1	I	Daughtercard status 1	66	STAT0	I	Daughtercard status
67	EXT_INT6	I	External interrupt 6	68	EXT_INT7	I	External interrupt 7
69	ACE3#	O	Chip enable 3	70	N/C	-	No connect
71	N/C	-	No connect	72	N/C	-	No connect
73	N/C	-	No connect	74	N/C	-	No connect
75	DC_DET#	Vss	System ground	76	GND	Vss	System ground
77	GND	Vss	System ground	78	ECL KOUT	O	EMIF Clock
79	GND	Vss	System ground	80	GND	Vss	System ground

3.3.3 J1, HPI Expansion Connector

Table 4: J1, HPI Expansion Connector

Pin	Signal	I/O	Description	Pin	Signal	I/O	Description
1	N/C	-	No connect	2	N/C	-	No connect
3	GND	Vss	System ground	4	HPI_RESETn	I	HPI reset input
5	CLKOUT3	O	Clock output3	6	N/C	-	No connect
7	GND	Vss	System ground	8	GND	Vss	System ground
9	HD1/AXR1[7]	I/O	HPI data 1	10	N/C	-	No connect
11	HD3/AMUTE1	I/O	HPI data 3	12	HD0/AXR1[4]	I/O	HPI data 0
13	HD5/AHCLKX1	I/O	HPI data 5	14	HD2/AFSX1	I/O	HPI data 2
15	HD7/GP0[3]	I/O	HPI data 7	16	HD4/GP0[0]	I/O	HPI data 4
17	GND	Vss	System ground	18	HD6/AHCLKR1	I/O	HPI data 6
19	HD8/GP0[8]	I/O	HPI data 8	20	GND	Vss	System ground
21	HD10/GP0[10]	I/O	HPI data 10	22	HD9/GP0[9]	I/O	HPI data 9
23	HD12/GP0[12]	I/O	HPI data 12	24	HD11/GP0[11]	I/O	HPI data 11
25	HD14/GP0[14]	I/O	HPI data 14	26	HD13/GP0[13]	I/O	HPI data 13
27	GND	Vss	System ground	28	HD15/GP0[15]	I/O	HPI data 15
29	HDS2z/AXR1[5]	I/O	Host data strobe 2	30	GND	Vss	System ground
31	GND	Vss	System ground	32	HASz/ACLKX1	I/O	Host address strobe
33	HDS1z/AXR1[6]	I/O	Host data strobe 1	34	GND	Vss	System ground
35	GND	Vss	System ground	36	HCNTL0/AXR1[3]	I/O	Host control 1
37	HCSz/AXR1[2]	I/O	Host chip select	38	GND	Vss	System ground
39	GND	Vss	System ground	40	HHWIL/AFSR1	I/O	Host half-word select
41	HCNTL1/AXR1[1]	I/O	Host control 1	42	GND	Vss	System ground
43	GND	Vss	System ground	44	HINTz/GP0[1]	I/O	Host interrupt
45	HRDYz/ACLKR1	I/O	Host Ready	46	GND	Vss	System ground
47	GND	Vss	System ground	48	N/C	-	No connect
49	HR/Wz/AXR1[0]	I/O	Host R/W strobe	50	N/C	-	No connect
51	N/C	-	No connect	52	N/C	-	No connect
53	N/C	-	No connect	54	N/C	-	No connect
55	N/C	-	No connect	56	GND	Vss	System ground
57	N/C	-	No connect	58	N/C	-	No connect
59	N/C	-	No connect	60	N/C	-	No connect
61	GND	Vss	System ground	62	N/C	-	No connect
63	N/C	-	No connect	64	N/C	-	No connect
65	N/C	-	No connect	66	N/C	-	No connect
67	N/C	-	No connect	68	SCL0	I/O	I2C0 Clock
69	N/C	-	No connect	70	GND	Vss	System ground
71	GND	Vss	System ground	72	SDA0	I/O	I2C0 Data
73	N/C	-	No connect	74	GND	Vss	System ground
75	GND	Vss	System ground	76	N/C	-	No connect
77	N/C	-	No connect	78	GND	Vss	System ground
79	GND	Vss	System ground	80	CLKOUT2/GP0[2]	I/O	GP I/O 0 bit 2