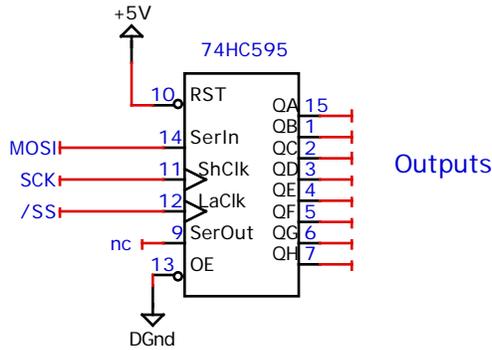
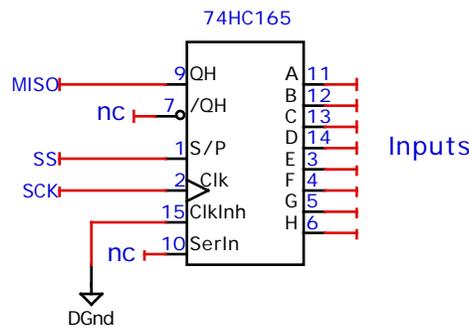


Output Only Example. 8-bit General purpose outputs. SS pin used as a latch clock. It can be set up as either a slave-select or a general purpose output to transfer SR contents to the output latch after the data is shifted in. This is a positive edge triggered device. It requires: CPOL=CPHA=1.



Input Only Example. 8-bit General purpose inputs. SS pin used as a latch clock. It must be set up as a general purpose output to transfer the input latch contents to the SR before the data is shifted in. This is a positive edge triggered device so it requires: CPOL= 1, CPHA=0.



Both Input and Output Example. This is the circuit that goes with SpiDemo.s12. The SS pin is used as the 74HC595 latch clock as described above. PP0 is used for the 74HC165 latch clock. Note the software must change the clock phase, CPHA, for reads/writes to this circuit.

