

Figure 6a. Memory Expansion Hardware Schematic

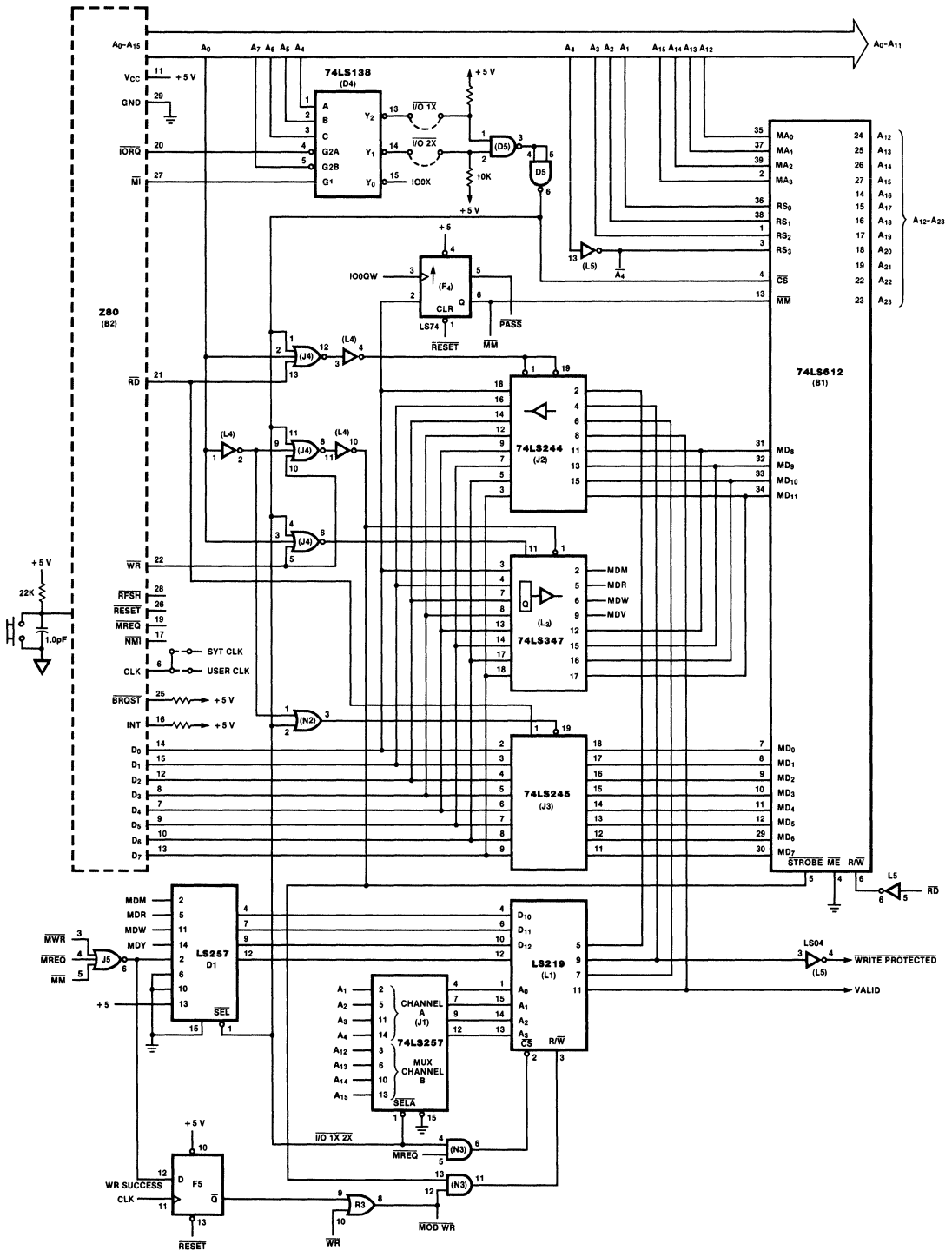


Figure 6b. Memory Expansion Hardware Schematic (Continued)