

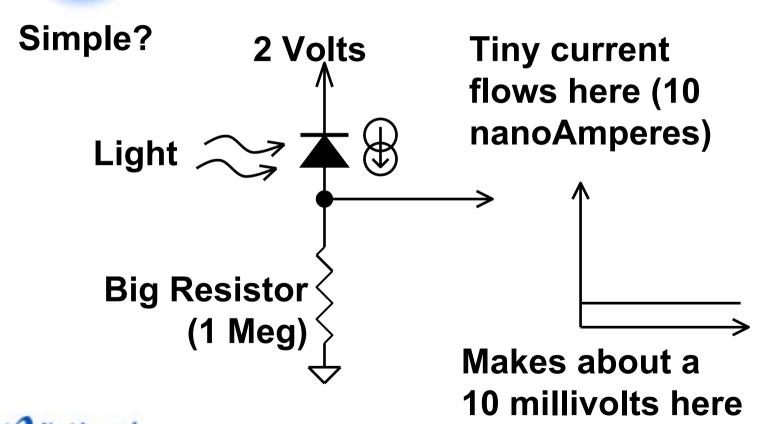
Photodiode Amplifiers

Changing Light to Electricity

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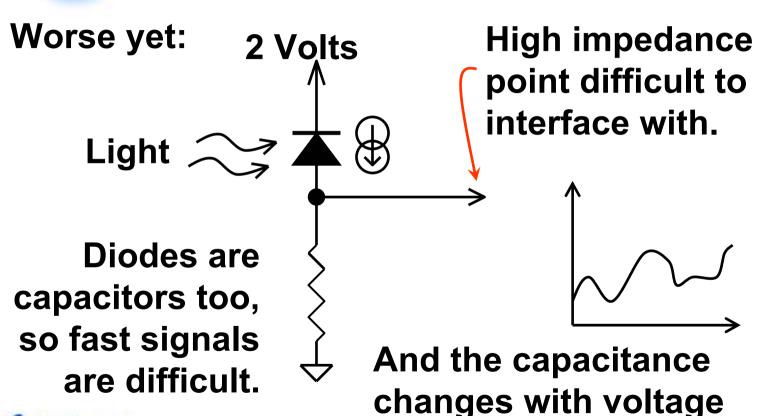




Dark Current No, not really 2 Volts (diode leakage) simple: flows too and is worse with temp. Light Big resistors make noise 10 millivolts is not very useful.







across the diode.



Still Worse:

2 Volts

Light $\overline{\ }$

To make the diode more sensitive to light you make the P-N junction big.

And that big junction has even more capacitance.

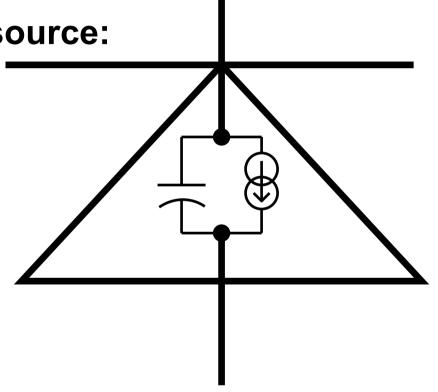




Inside the Photodiode:

A cap and a current source:

The bigger the voltage across the diode the further the junction boundaries are pushed apart and the lower the capacitance.



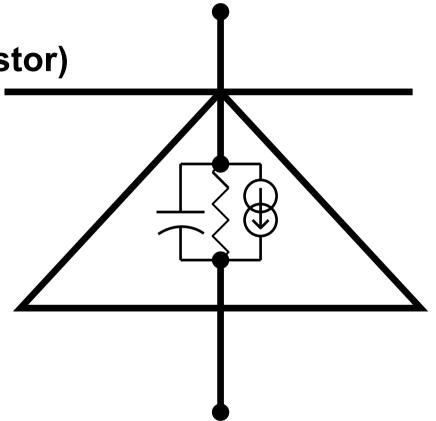




Inside the Photodiode:

(And a really big resistor)

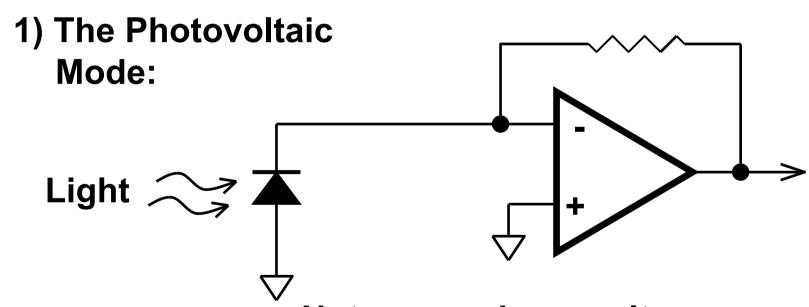
There is also a bulk resisistivity to the diode but it is usually very high (100 M Ω). This represents the "Dark Current".







Two ways to use the diode:





Note ground— no voltage across diode.



The Photovoltaic Mode:

No voltage across diode means no current though the big resistor ~

No dark current.

Also:

- Linear output
- Low Noise





Use Photovoltaic Mode:

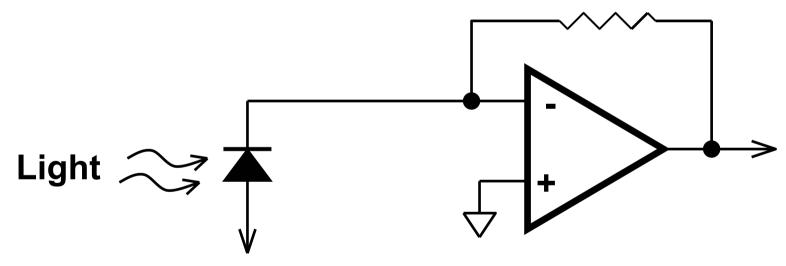
 Where precision is more important then speed.

The lack of dark current removes an entire error term. The lower noise makes smaller measurements possible. The linear output makes calculations trivial.





The Photoconductive Mode:



- 10V, there is voltage across the diode.





Use Photoconductive Mode:

Where speed is more important then precision.

The voltage across the diode lowers it's capacitance. This allows faster amplifiers:

 Less capacitance allows a faster amplifier while maintaining stability.

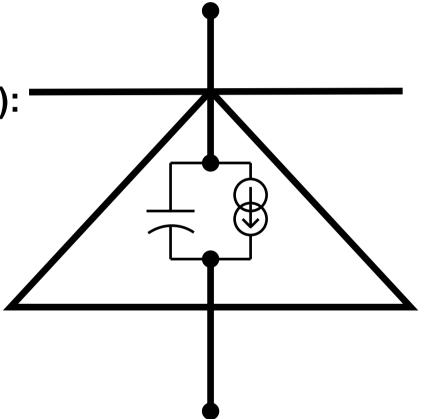




Biasing the Photodiode:

Apply a big voltage (that doesn't change):

We want a low capacitance so put a big voltage across the diode. We want fast response so don't let the voltage ever change. How?

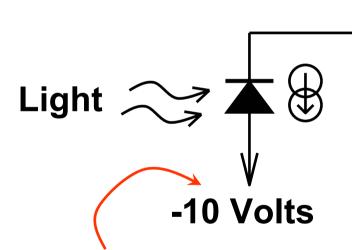




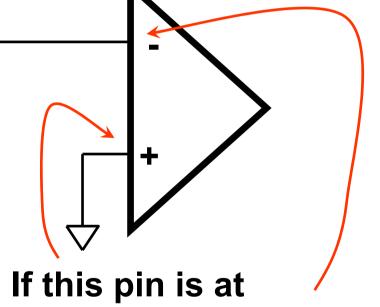


The Photodiode Amplifier:

Connect the diode to a virtual ground:



As much reverse voltage as the diode can stand.



ground so must this

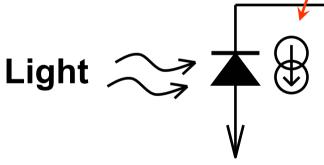
pin be at ground.

15

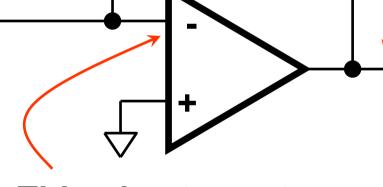


The Photodiode Amplifier:

Oh yeah, add some feedback: This current makes positive voltage here



-10 Volts



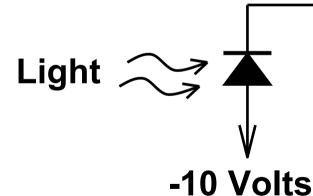
This pin stays at ground so output goes more positive with more light.

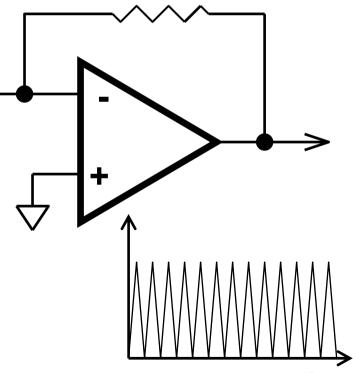




The Photodiode Amplifier:

So it oscillates and/or clips, what is wrong?





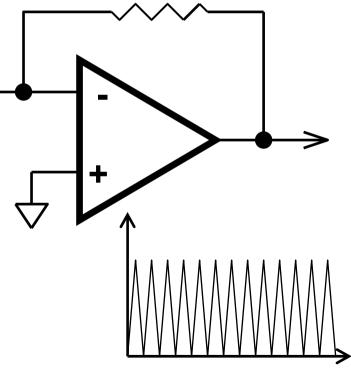


Amplifier Stability:

 Oscillations caused by capacitive diode on input.

Photodiode looks like cap to amp

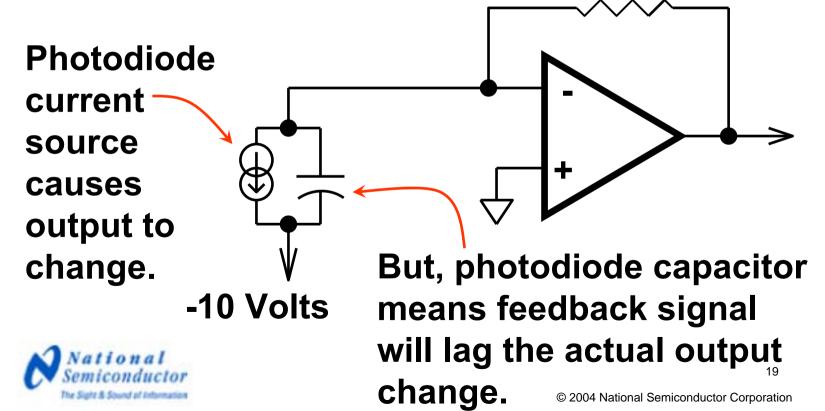
-10 Volts





Amplifier Stability:

 Input pole (freq domain) or feedback lag (time domain) is bad.





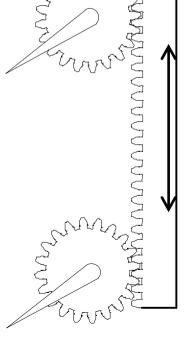
Mechanical Analogy:

A gear and rack mechanical servo.

This gear is the amp output stage.

You are the amplifier front-end trying to keep the pointers the same.

This gear is the feedback.



This rack is the output voltage.





 Input cap is like backlash in feedback mechanism.

Backlash here is a lag in the feedback.

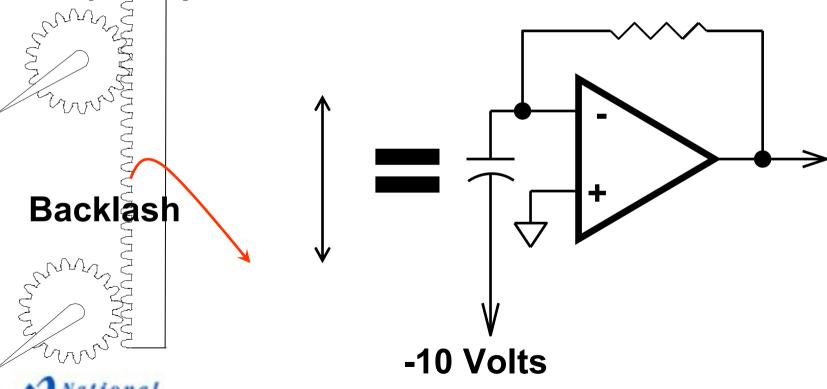
The lag in your feedback pointer will cause you to oscillate the rack.





Mechanical Analogy:

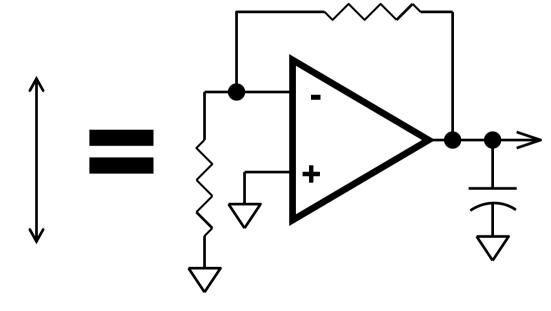
Input cap is like backlash in feedback mechanism.



Mechanical Analogy:

Backlash

Interesting note: Driver backlash is like output capacitance.



Without compensation either cap will cause oscillations.



Compensated Amplifier:

• Add a feedback cap to compensate.

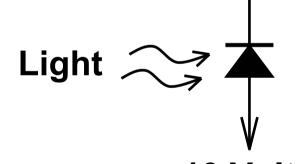






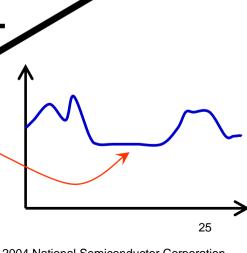


 The output is stable but there is a big DC offset. Why?



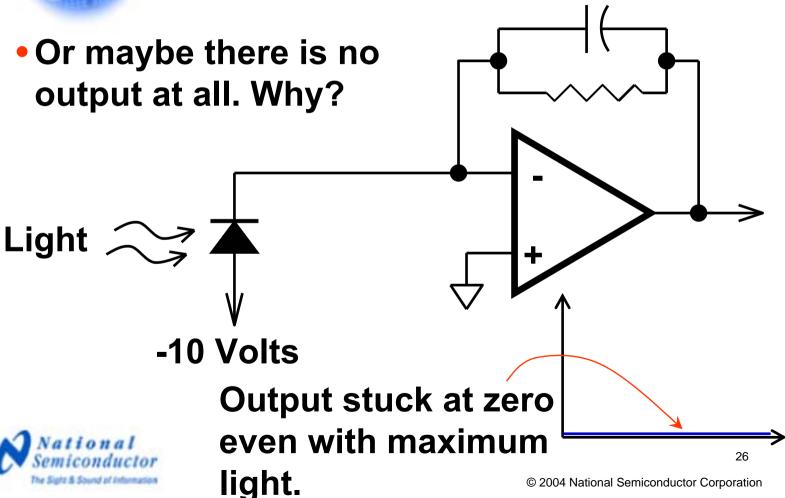
-10 Volts

Output never goes below here even with no light.



Semiconductor







• Answer: Input bias current.

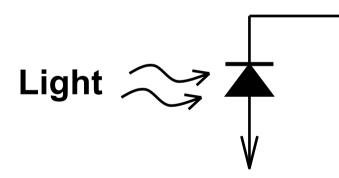
-10 Volts



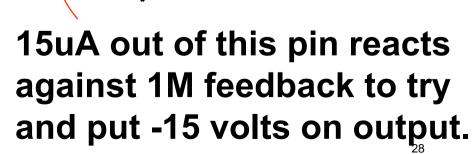




 Bias current may exceed photodiode current.



-10 Volts

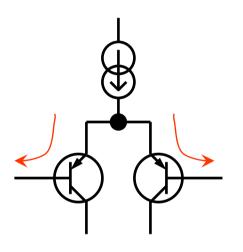




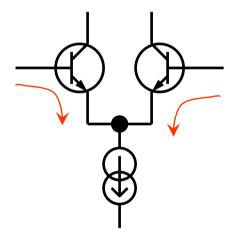


Amplifier Input Stage:

Input transistors have base current.



PNP Input Stage Input bias current may be 15 µA, but won't vary much over temperature.



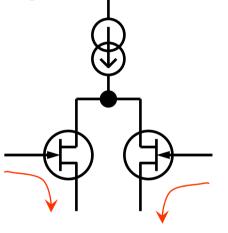
NPN Input Stage





Amplifier Input Stage:

Input JFETs have large drift.



JFET Input Stage

Input bias current may be 15 pA, but will double every 10°C.

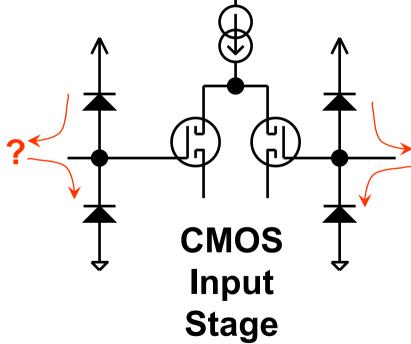




Amplifier Input Stage:

CMOS parts have ESD diodes.

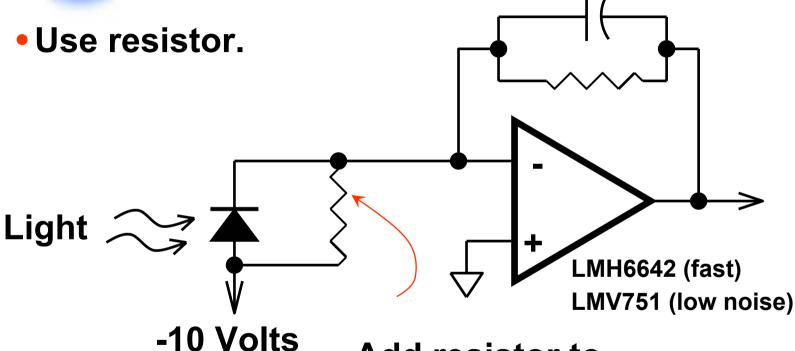
MOSFET has no DC bias current but mis-match in ESD diodes causes bias current to flow in (or out) of pin.







Correcting DC Bias

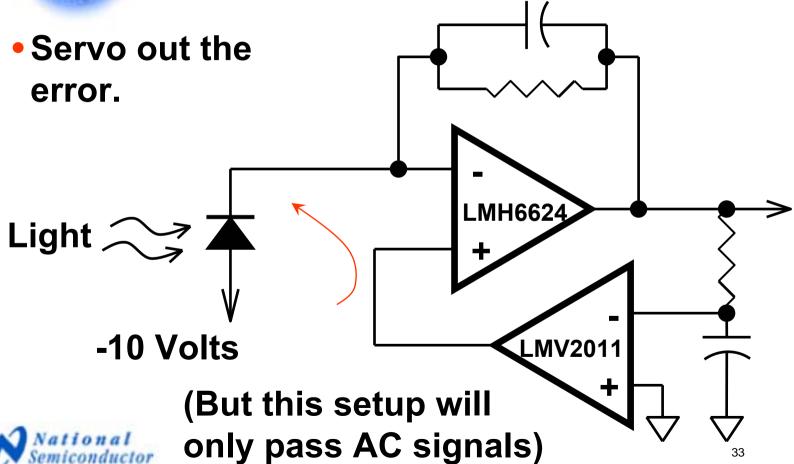




Add resistor to compensate for bias current.



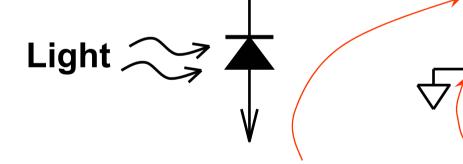
Correcting DC Bias





Amplifier noise:

 With stability and bias solved, next problem is noise.



Current noise important on this pin.

Voltage noise important on this pin.





Amplifier noise:

- Low current and low voltage noise in the same part is hard.
- JFET amplifiers have low current noise.
- Bipolar amplifiers have low voltage noise.
- Choppers can cause problems.

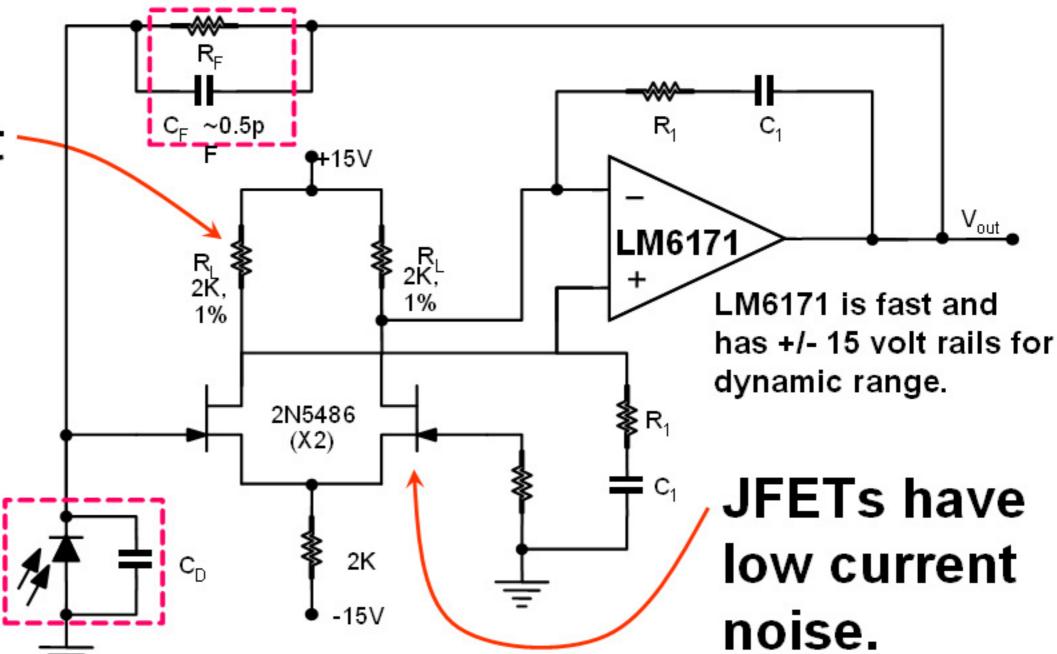




A Composite Amplifier:

One solution: a compound amp.

Run a lot of current to reduce the voltage noise.



 V_{out}



Some Potential Parts:

Device	Input Noise Voltage (nV/RtHz)	Input Noise Current (pA/RtHz)	Input Capacitance (pF)	I _{bias} (max)	GBWP (MHz)	GBWP/C _{in} (MHz/pF)
LMH6628	2	2	1.5	20μΑ	200	133
LMH6626*	1.0	1.8	0.9	20μΑ	500	556
LMH6624*	0.92	2.3	0.9	20μΑ	500	556
LMH6622	1.6	1.5	0.9	10μΑ	200	222
LMH6654 /6655	4.5	1.7	1.8	12μΑ	150	83
LMH6672	4.5	1.7	2	14μΑ	100	50
LF411A	25	0.01	4	200pA	4	1
LMV751	7	0.005	5	100pA	5	1
LMC662	22	0.0002	4	0.01pA (typical)	1.4	0.3
LMV771	8	0.001	4	100pA	4	1





- Photodiode amplifiers are tricky.
- The design should be tailored for the application, DC, Data, etc.
- The design requires a lot of trial and error.
- Be prepared to do a lot of study.
- National Applications Engineering is here to help you.





- AN-1244: Photo-Diode Current-to-Voltage Converters.
- Amplifier WEBENCH®

 On-line simulation of amplifier performance
- Photodiode Amplifiers: OP AMP Solutions by Jerald Graeme
- Photodetection and Measurement: Maximizing
 Performance in Optical Systems by Mark Johnson
- Photodetectors: Devices, Circuits and Applications by Silvano Donati



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