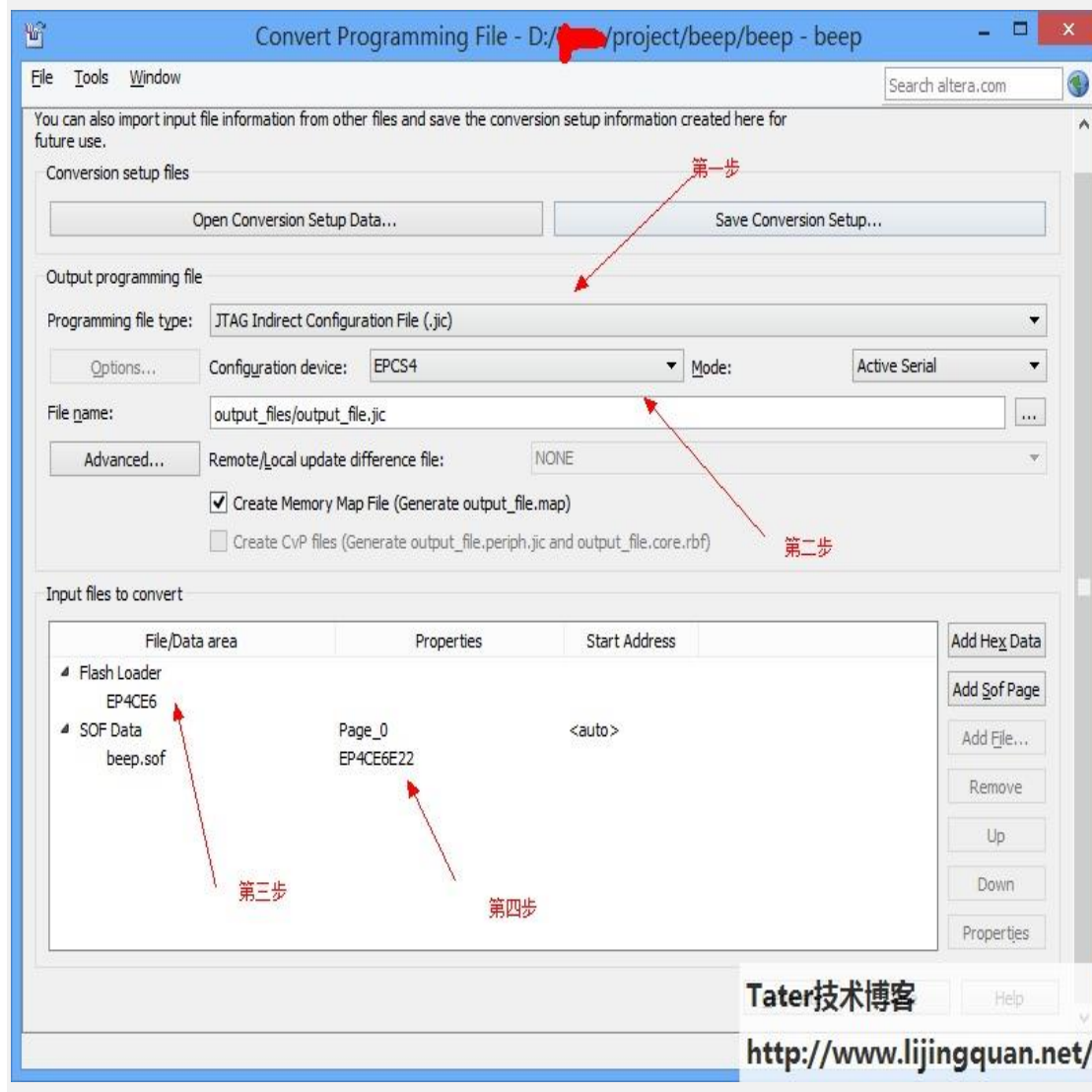


感谢 TaterLi, 这篇文档我只做整理部分, 版权非己有

首先说说,我的配置芯片是 EPCS4SI8N,我的 FPGA 芯片是 EP4CE6E22C8N,假设你已经会编译,会做 JTAG 烧写 POF 文件.下面说说怎样使得 POF 文件转换成固化到 FPGA Flash 的文件.

选择 File->Convert Programming Files,调出 Convert Programming Files.



1. 第一步选择 jic.
2. 第二步,选配置芯片型号.
3. 点击 Flash Loader,Add Device(右边).
4. 导入你生成的 pof 文件,
5. 其他可选.

进入 JTAG 烧写,sof 文件必须删掉,取消复选框也不行,啊哈.

Programmer - D:/[redacted]project/beep/beep - beep - [output_files/beep.cdf]*

File Edit View Processing Tools Window Help Search altera.com

Hardware Setup... USB-Blaster [USB-0] Mode: JTAG Progress: []

Enable real-time ISP to allow background programming (for MAX II and MAX V devices)

File	Device	Checksum	Usercode	Program/Configure	Verify	Blank-Check
Factory default enhanced SFL image	EP4CE6	000CF915	FFFFFFFF	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
output_files/output_file5.jic	EPCS4	0275ED98		<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
output_files/beep.sof	EP4CE6E22	0009360C	0009360C	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

sof文件必须删掉

Start Stop Auto Detect Delete Add File... Change File... Save File Add Device... Up Down

Replaces a selected programming file

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烧写过程要比别人要长,烧写完成后关闭电源重新上电,程序就可以跑起来了.

Programmer - D:\[redacted]/project/beep/beep - beep - [output_files/beep.cdf]*

File Edit View Processing Tools Window Help

Hardware Setup... USB-Blaster [USB-0] Mode: JTAG Progress: 100% (Successful)

Enable real-time ISP to allow background programming (for MAX II and MAX V devices)

File	Device	Checksum	Usercode	Program/Configure	Verify	Blank-Check
Factory default enhanced SFL image	EP4CE6	000CF915	FFFFFFFF	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
output_files/output_file5.jic	EPCS4	0275ED98		<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

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