

PINS	DESCRIPTION	74F (U.L.) HIGH/LOW	LOAD VALUE HIGH/LOW
J0, J1	J inputs	1.0/1.0	20 $\mu$ A/0.6mA
$\bar{K}0, K1$	K inputs	1.0/1.0	20 $\mu$ A/0.6mA
CP0, CP1	Clock inputs (active rising edge)	1.0/1.0	20 $\mu$ A/0.6mA
$\bar{S}D0, \bar{S}D1$	Set inputs (active Low)	1.0/3.0	20 $\mu$ A/1.8mA
$\bar{R}D0, \bar{R}D1$	Reset inputs (active Low)	1.0/3.0	20 $\mu$ A/1.8mA
$\underline{Q}0, \underline{Q}1,$ $\overline{Q}0, \overline{Q}1$	Data outputs	50/3.3	1.0mA/20mA

NOTE: One (1.0) FAST unit load is defined as: 20 $\mu$ A in the High state and 0.6mA in the Low state.