



8-2. LZ92K38 GATE ARRAY SIGNAL DESCRIPTION PC-1475

Pin No.	Signal name	In-Out	Description, standby = power off
1	BA	In	RAM bank select
2	CE	In	Chip enable, active low
3	DO0	In	Data bus input D0
6	DO3	In	Data bus input D3
7	GND	In	⊕ supply
8	PO1	In	11-pin IO1 (11-pin interface output data), P-ch open output
9	PO2	Out	11-pin IO2 (11-pin interface output data), P-ch open output
10	PO3	Out	11-pin DOUT (11-pin interface output data) and SIO PRQ (Printer Req/Status, P-ch open output)
11	PO4	Out	11-pin DIN (11-pin interface output data), P-ch open output
12	PO5	Out	11-pin BUSY (11-pin interface output data), P-ch open output
13	PO6	Out	High by the SIO ER OPEN instruction
14	PO7	Out	SIO RR (PC ready to receive)
15	PO8	Out	SIO RS (PC send request)
16	AS1	Out	System ROM address A14 and RAM card slot A11
17	AS2	Out	System ROM address A15 and RAM card slot A12
18	AS3	Out	System ROM address A16 and RAM card slot A13
19	AF15	Out	Slot address A15
20	AF16	Out	Slot address A16
21	SLT1	Out	RAM card slot S1 chip enable
22	SLT2	Out	RAM card slot S2 chip enable
23	DSP1	Out	LCD driver chip enable
24	DSP2	Out	LCD driver chip enable
25	RESO	Out	Reset to CPU RES
26	KON	Out	Break to CPU KON
27	LB	In	Low battery signal from the low battery IC, normally high
28	RES1	In	Input from the RESET switch
29	BRK	In	Input from the BRK key
30	Vcc	In	⊕ supply
31	GND	In	⊖ supply
32	CAN	In	Low battery detect signal select
33	KS1	Out	Key strobe, P-ch open output
40	KS8	Out	Key strobe, P-ch open output
41	RW	In	Write lock
42	A9	In	Address bus input A9
47	A15	In	Address bus input A15

GA(LZ92K38) block diagram

