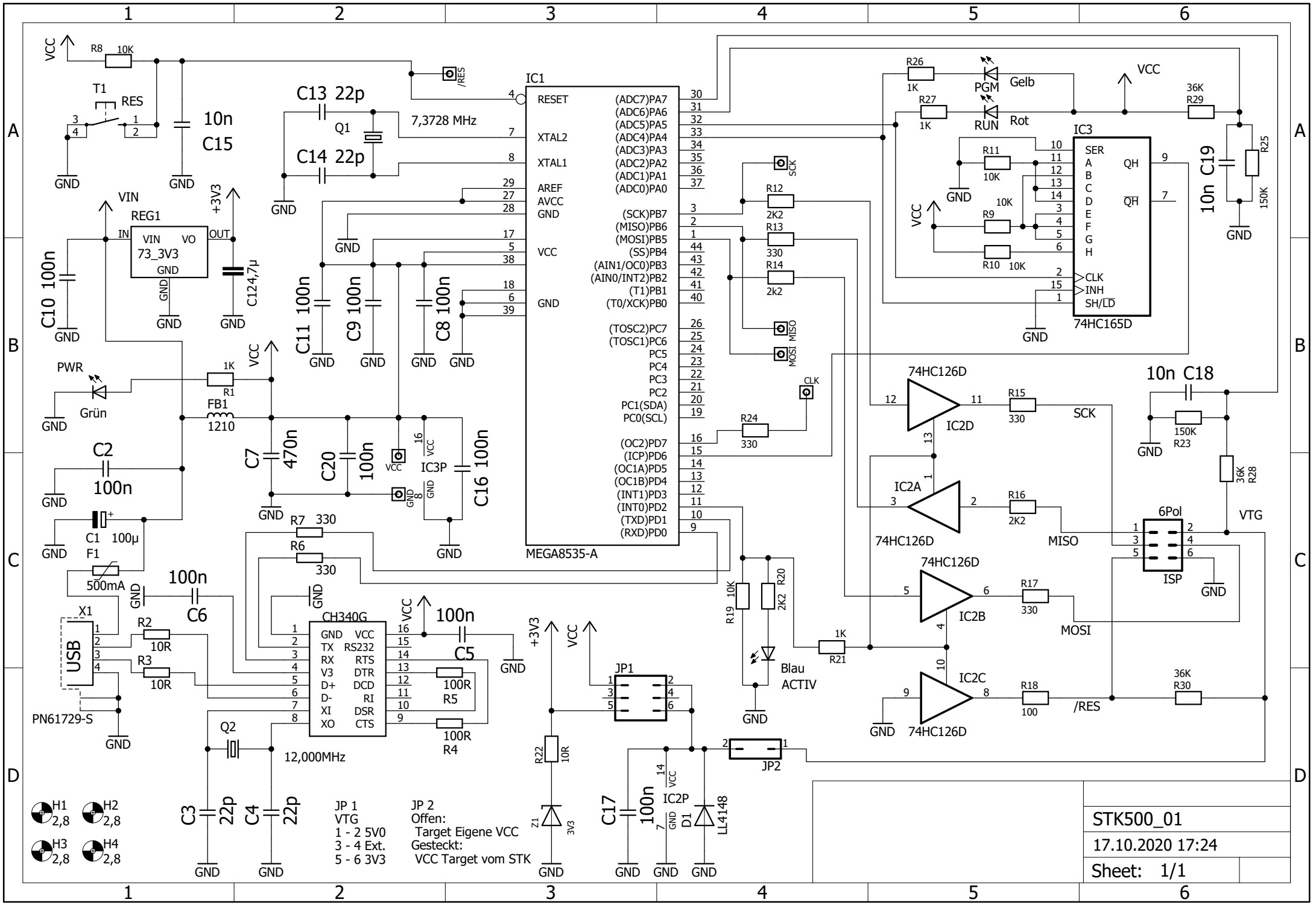
IC1
MEGA8535-A
RESET
XTAL2
XTAL1
AREF
AVCC
GND
VCC
(ADC7)PA7
(ADC6)PA6
(ADC5)PA5
(ADC4)PA4
(ADC3)PA3
(ADC2)PA2
(ADC1)PA1
(ADC0)PA0
(SCK)PB7
(MISO)PB6
(MOSI)PB5
(SS)PB4
(AIN1/OC0)PB3
(AIN0/INT2)PB2
(T1)PB1
(T0/XCK)PB0
(TOSC2)PC7
(TOSC1)PC6
PC5
PC4
PC3
PC2
PC1(SDA)
PC0(SCL)
(OC2)PD7
(ICP)PD6
(OC1A)PD5
(OC1B)PD4
(INT1)PD3
(INT0)PD2
(TXD)PD1
(RXD)PD0
R8 10K
T1
RES
/RES
C15 10n
C13 22p
C14 22p
Q1
7,3728 MHz
REG1
73_3V3
VIN
+3V3
C10 100n
C124,7µ
C11 100n
C9 100n
C8 100n
PWR
Grün
R1 1K
FB1
1210
C2 100n
C1 100µ
F1 500mA
C7 470n
C20 100n
IC3P
C16 100n
R7 330
R6 330
X1
USB
PN61729-S
R2 10R
R3 10R
CH340G
C6 100n
C5 100n
R5 100R
R4 100R
Q2
12,000MHz
C3 22p
C4 22p
H1 2,8
H2 2,8
H3 2,8
H4 2,8
JP 1
VTG
1 - 2 5V0
3 - 4 Ext.
5 - 6 3V3
JP 2
Offen:
Target Eigene VCC
Gesteckt:
VCC Target vom STK
JP1
JP2
R22 10R
Z1 3V3
C17 100n
IC2P
D1 LL4148
R19 10K
R20 2K2
R21 1K
Blau
ACTIV
R26 1K
PGM
Gelb
R27 1K
RUN
Rot
IC3
74HC165D
R11 10K
R9 10K
R10 10K
SER
QH
CLK
INH
SH/LD
36K R29
10n C19
150K R25
SCK
MISO
MOSI
CLK
R12 2K2
R13 330
R14 2k2
R24 330
74HC126D
IC2D
IC2A
IC2B
IC2C
R15 330
R16 2K2
R17 330
R18 100
10n C18
150K R23
36K R28
36K R30
6Pol
ISP
VTG
MISO
MOSI
/RES
STK500_01
17.10.2020 17:24
Sheet: 1/1