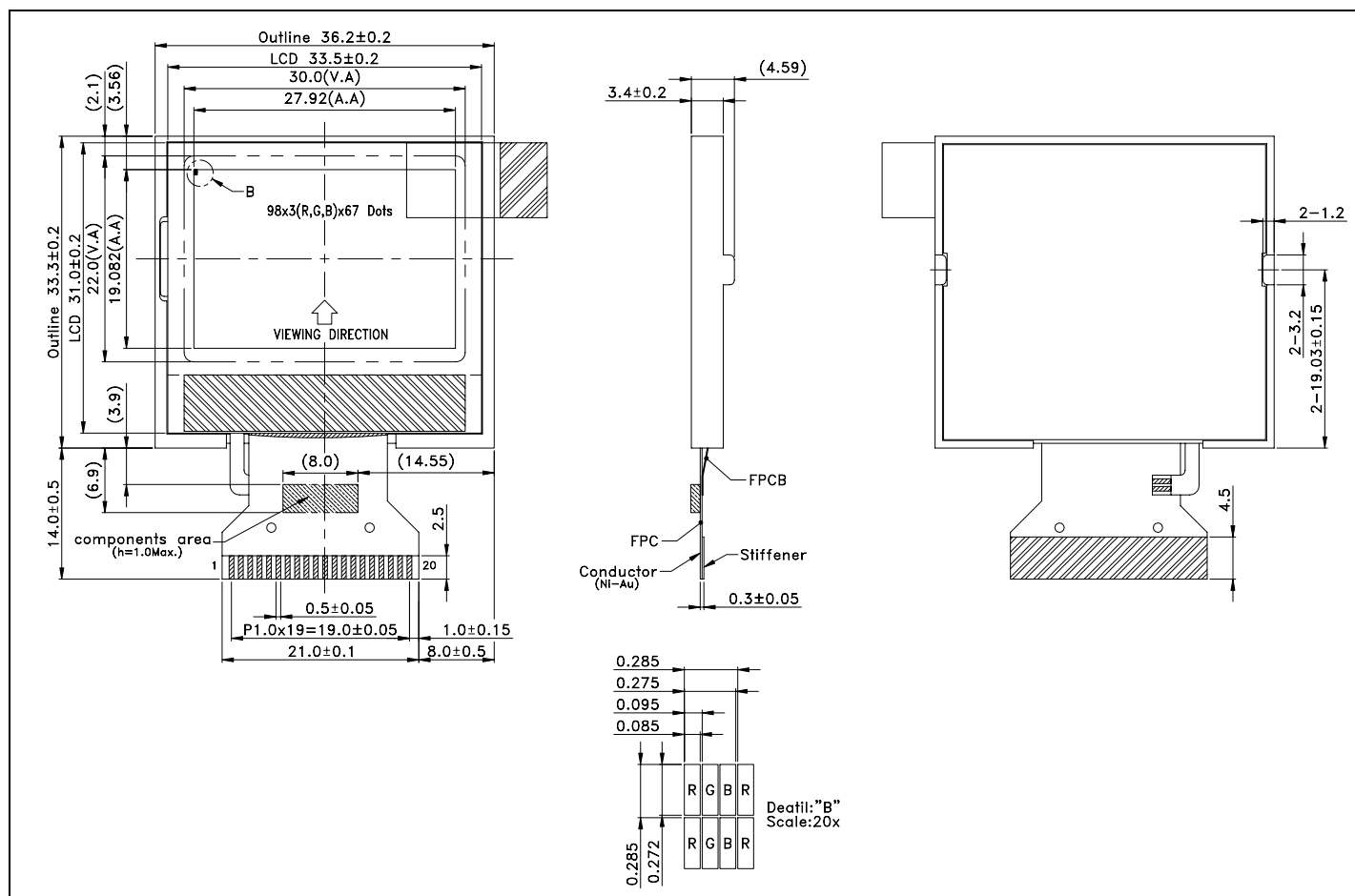




Features

Item	Standard Value
Display Type	98*(R、G、B) * 67 Dots
LCD Type	CSTN (Negative / Transmissive)
Driver Condition	LCD Module : 1/67 Duty , 1/9 Bias
Screen size (inch)	1.33 (Diagonal)
Viewing Direction	6 O'clock
Color configuration	R.G.B. vertical stripe
Backlight Type	White LED B / L
Interface	8 Bits data bus
Driver IC	ST7626-G3 (Support 65K Colors)

Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	36.2(W) * 33.3(L) * 4.59(H)	mm
Viewing Area	30.0(W) * 22.0(L)	mm
Active Area	27.92(W) * 19.082(L)	mm
Dot Size	0.275(W) * 0.272(L)	mm
Dot Pitch	0.285(W) * 0.285(L)	mm

Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
System Power Supply Voltage	VDD	-	-0.5	4.0	V
Power Supply Voltage	VLCD	-	-0.5	+20	V
Input Voltage	V _{IN}		-0.5	V _{DD} + 0.5	V

Electrical Characteristics

V_{DD} = 2.8 V , V_{SS} = 0V , Ta = 25°C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Logic Supply Voltage	VDD	-	-	2.8	-	V
Supply Current	V _{DD}	V _{DD} = 2.8V;V _{OP} =10.2V; Pattern= Full display*1	-	1	-	mA
	I _{DD}	V _{DD} = 2.8V;V _{OP} =10.2V; Pattern= Picture*1	-	1	1.5	mA
LCD Driver Voltage	V _{OP}	-20°C	11.1	11.3	11.5	V
		25°C *2	9.8	10.1	10.4	
		70°C	9.2V	9.4V	9.6V	

NOTE: *1 The Maximum current display; *2 The VOP test point is V0-VSS

Interface Pin Description

Pin No.	Symbol	Function									
1	LED_A	Anode of LED circuit.									
2	LED_K	Cathode of LED circuit.									
3	/CS	Chip select signal. When /CS=Low. Data / instruction I/O is enable.									
4	/RESET	When /RESET is “L”, initialization is executed.									
5	A0	When A0 is“H”,D0 to D7 are display data When A0 is“L”,D0 to D7 are control data									
6	/WR	Read/Write execution control pin. 80 Series : Connect /WR Signal , active at high. 68 Series : High = Read mode ; Low = Write mode.									
7	/RD	80 Series : Connect /RD Signal , active at Low. 68 Series : Enable signal , active at high.									
8	D0~7	8-bit bi-directional data bus that is connected to the standard 8-bit microprocessor data bus. When chip select is active, D0 to D7 may be low impedance.									
9	V _{SS}	Ground for logic circuit (0V).									
10	V _{DD}	Power supply for logic circuit (2.8V).									
11	IF1	<table><tr><td>IF1</td><td>IF3</td><td>MPU Interface type</td></tr><tr><td>H</td><td>L</td><td>8080-series 8-bit MPU interface</td></tr><tr><td>L</td><td>H</td><td>6800-series 8-bit MPU interface</td></tr></table>	IF1	IF3	MPU Interface type	H	L	8080-series 8-bit MPU interface	L	H	6800-series 8-bit MPU interface
IF1	IF3	MPU Interface type									
H	L	8080-series 8-bit MPU interface									
L	H	6800-series 8-bit MPU interface									
12	IF3										
13	V0	LCD driver supply voltages.									