

<p>4 bit Zähler, modulo 11:</p> <ul style="list-style-type: none"> <li>13 Total logic elements</li> <li>09 -- Combinational with no register</li> <li>00 -- Register only</li> <li>04 -- Combinational with a register</li>   <li>Logic elements by mode</li> <li>09 -- normal mode</li> <li>04 -- arithmetic mode</li> <li>00 -- qfbk mode</li> <li>00 -- register cascade mode</li> <li>00 -- synchronous clear/load mode</li> <li>04 -- asynchronous clear/load mode</li> </ul>	<p>4 bit Zähler, modulo 7:</p> <ul style="list-style-type: none"> <li>22 Total logic elements</li> <li>18 -- Combinational with no register</li> <li>00 -- Register only</li> <li>04 -- Combinational with a register</li>   <li>Logic elements by mode</li> <li>15 -- normal mode</li> <li>07 -- arithmetic mode</li> <li>00 -- qfbk mode</li> <li>00 -- register cascade mode</li> <li>00 -- synchronous clear/load mode</li> <li>04 -- asynchronous clear/load mode</li> </ul>	<p>4 bit Zähler, modulo 3:</p> <ul style="list-style-type: none"> <li>24 Total logic elements</li> <li>20 -- Combinational with no register</li> <li>00 -- Register only</li> <li>04 -- Combinational with a register</li>   <li>Logic elements by mode</li> <li>17 -- normal mode</li> <li>07 -- arithmetic mode</li> <li>00 -- qfbk mode</li> <li>00 -- register cascade mode</li> <li>00 -- synchronous clear/load mode</li> <li>04 -- asynchronous clear/load mode</li> </ul>
<p>8 bit Zähler, modulo 172:</p> <ul style="list-style-type: none"> <li>022 Total logic elements</li> <li>014 -- Combinational with no register</li> <li>000 -- Register only</li> <li>008 -- Combinational with a register</li>   <li>Logic elements by mode</li> <li>10 -- normal mode</li> <li>12 -- arithmetic mode</li> <li>00 -- qfbk mode</li> <li>00 -- register cascade mode</li> <li>00 -- synchronous clear/load mode</li> <li>08 -- asynchronous clear/load mode</li> </ul>	<p>8 bit Zähler, modulo 31:</p> <ul style="list-style-type: none"> <li>082 Total logic elements</li> <li>074 -- Combinational with no register</li> <li>000 -- Register only</li> <li>008 -- Combinational with a register</li>   <li>Logic elements by mode</li> <li>50 -- normal mode</li> <li>32 -- arithmetic mode</li> <li>00 -- qfbk mode</li> <li>00 -- register cascade mode</li> <li>00 -- synchronous clear/load mode</li> <li>08 -- asynchronous clear/load mode</li> </ul>	<p>8 bit Zähler, modulo 3:</p> <ul style="list-style-type: none"> <li>107 Total logic elements</li> <li>099 -- Combinational with no register</li> <li>000 -- Register only</li> <li>008 -- Combinational with a register</li>   <li>Logic elements by mode</li> <li>68 -- normal mode</li> <li>39 -- arithmetic mode</li> <li>00 -- qfbk mode</li> <li>00 -- register cascade mode</li> <li>00 -- synchronous clear/load mode</li> <li>08 -- asynchronous clear/load mode</li> </ul>
<p>16 bit Zähler, modulo 45831:</p> <ul style="list-style-type: none"> <li>049 Total logic elements</li> <li>033 -- Combinational with no register</li> <li>000 -- Register only</li> <li>016 -- Combinational with a register</li>   <li>Logic elements by mode</li> <li>019 -- normal mode</li> <li>030 -- arithmetic mode</li> <li>000 -- qfbk mode</li> <li>000 -- register cascade mode</li> <li>000 -- synchronous clear/load mode</li> <li>016 -- asynchronous clear/load mode</li> </ul>	<p>16 bit Zähler, modulo 1023:</p> <ul style="list-style-type: none"> <li>280 Total logic elements</li> <li>264 -- Combinational with no register</li> <li>000 -- Register only</li> <li>016 -- Combinational with a register</li>   <li>Logic elements by mode</li> <li>175 -- normal mode</li> <li>105 -- arithmetic mode</li> <li>000 -- qfbk mode</li> <li>000 -- register cascade mode</li> <li>000 -- synchronous clear/load mode</li> <li>016 -- asynchronous clear/load mode</li> </ul>	<p>16 bit Zähler, modulo 3:</p> <ul style="list-style-type: none"> <li>417 Total logic elements</li> <li>401 -- Combinational with no register</li> <li>000 -- Register only</li> <li>016 -- Combinational with a register</li>   <li>Logic elements by mode</li> <li>270 -- normal mode</li> <li>147 -- arithmetic mode</li> <li>000 -- qfbk mode</li> <li>000 -- register cascade mode</li> <li>000 -- synchronous clear/load mode</li> <li>016 -- asynchronous clear/load mode</li> </ul>