

PD30SIM [C:\Daten\Software\M16C\test2\test2\Release\test2.x30] - [Program Window [eingang.c]]

File Edit View Environment Debug Option BasicWindows OptionalWindows Help

Go Come Step Over Return Stop Break Reset S/... H/...

View Source Mix Di...

Line	Address	BRK	Objcode	Label	Source/Mnemonic
00006	000000	-	7CF200	_eingang	< ENTER #00H
00007	000003	-	760F03082		if<in1 == 0> TST.B #22H,0003H
00008	000008	-	6E26		JNE 0002FH
00009	00000A	-	9C01		< OR.B #01H,R0L
00010	00000C	-	0101		MOV.B R0L,01H[SB]
00011	00000E	-	97FE0501		AND.B #FEH,0105H
00012	000012	-	B4		MOV.B #0,R0L
00013	000013	-	760F03010		TST.B #01H,0103H
00014	000018	-	7E2000FA		BMNE 0,R0
00015	00001C	-	9104		AND.W R0,A0
00016	00001E	-	7240		MOV.B A0,R0L
00017	000020	-	0F0501		MOV.B 0105H,R0H
00018	000023	-	7EB000		BTST 0,R0
00019	000026	-	7E2009FA		BMNE 9,R0
00020	00002A	-	070501		MOV.B R0H,0105H
00021	00002D	-	FE24		JMP.B 00052H
00022	00002F	-	97FE0301		else AND.B #FEH,0103H
00023	000033	-	9F010501		OR.B #01H,0105H
00024	000037	-	B4		MOV.B #0,R0L
00025	000038	-	760F03010		TST.B #01H,0103H
00026	00003D	-	7E2000FA		BMNE 0,R0
00027	000041	-	7204		MOV.B R0L,A0
00028	000043	-	7240		MOV.B A0,R0L
00029	000045	-	0F0501		MOV.B 0105H,R0H
00030	000048	-	7EB000		BTST 0,R0
00031	00004B	-	7E2009FA		BMNE 9,R0
00032	00004F	-	070501		MOV.B R0H,0105H
00033	000052	-	7DF2		> EXITD

Ready 00 h 00 m 00 sec 049 msec 150 usec (0000983012 cycle) MCU : STOP

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Line	Address	BRK	Objcode	Label	Source/Mnemonic
00045	00007D	-	7E9F3900		cm11 = 1; /* switch to PLL */ BSET 1,0007H
00046	000081	-	7E8F5000		prc0 = 0; /* disable access to clock registers */ BCLR 0,000AH
00047					
00048	000085	-	7E9F5100		prc1 = 1; BSET 1,000AH
00049	000089	-	7E9F2800		pm10 = 1; // enable data flash area BSET 0,0005H
00050	00008D	-	7E8F5100		prc1 = 0; BCLR 1,000AH
00051					
00052	000091	-	C7640304		a = 100; MOU.B #64H,0403H
00053	000095	-	C7140404		b = 20; MOU.B #14H,0404H
00054	000099	-	7E9BF0		in1 = 1; BSET 0,-2H[FB]
00055	00009C	-	7E9BF1		in2 = 1; BSET 1,-2H[FB]
00056	00009F	-	7E8BF0		in1 = 0; BCLR 0,-2H[FB]
00057	0000A2	-	7E8BF1		in2 = 0; BCLR 1,-2H[FB]
00058					for(;;)
00059					<
00060					for(T1=0;T1<0xffff;T1++)
	0000A5	-	D90FFE00		MOU.W #0H,00FEH
	0000A9	-	D1FFFE00		CMP.W #-1H,00FEH
	0000AD	-	683B		JGEU 000E9H
00061					<
00062					for(T2=0;T2<0xffff;T2++)
	0000AF	-	D90F0001		MOU.W #0H,0100H
	0000B3	-	D1FF0001		CMP.W #-1H,0100H
	0000B7	-	680A		JGEU 000C2H
00063					i++;
	0000B9	-	A70204		INC.B 0402H
	0000BC	-	C91F0001		ADD.W #1H,0100H
00064	0000C0	-	33F2		JMP.B 000B3H
	0000C2	-	E7640204		if(i==100) CMP.B #64H,0402H
	0000C6	-	CF000204		STZ #00H,0402H
00065					i=0;
00066	0000CA	-	7EAF601F		if(p6_0 == 1)
00067					p6_0 = 0;
00068					else
00069					p6_0 = 1;
00070	0000CE	-	FD000000		eingang(); JSR.A _eingang 00000H