



View Source Mix Di...

Line	Address	BRK	Objcode	Label	Source/Mnemonic
00066					for<T2=0;T2<0xffff;T2++>
	0E806B	-	D90F0404		MOU.W #0H,0404H
	0E806F	-	D1FF0404		CMP.W #-1H,0404H
	0E8073	-	680A		JGEU E807EH
00067					i++;
	0E8075	-	A70C04		INC.B 040CH
	0E8078	-	C91F0404		ADD.W #1H,0404H
	0E807C	-	FEP2		JMP.B E806FH
00068					if<i==100>
	0E807E	-	E7640C04		CMP.B #64H,040CH
	0E8082	-	CF000C04		STZ #00H,040CH
00069					i=0;
00070					eingang();
	0E8086	-	F51B00		JSR.W _eingang E80A2H
00071					c = Berechne(a, b);
	0E8089	-	744F0E04		PUSH.B 040EH
	0E808D	-	72F20D04		MOU.B 040DH,R1L
	0E8091	-	F56400		JSR.W \$Berechn E80F6H
	0E8094	-	7DB1		ADD.B #1H,SP
	0E8096	-	730F0604		MOU.W R0,0406H
	0E809A	-	C91F0204		ADD.W #1H,0402H
	0E809E	-	FEC6		JMP.B E8065H
	0E80A0	-	FEC0		JMP.B E8061H
00072					>
00073					>
00074					>
00075					
00076					void eingang(void)
00077					<
00078	0E80A2	-	7CF200	_eingang	ENTER #00H
	0E80A5	-	760F08040		if<in1 == 0>
	0E80AA	-	6E26		TST.B #01H,0408H
00079					JNE E80D1H
00080					<
	0E80AC	-	9F010804		OR.B #01H,0408H
00081	0E80B0	-	97FE0A04		AND.B #FEH,040AH
00082					out2 = in1;
	0E80B4	-	B4		MOU.B #0,R0L
	0E80B5	-	760F08040		TST.B #01H,0408H
	0E80BA	-	7E2000FA		BMNE 0,R0
	0E80BE	-	7204		MOU.B R0L,A0
	0E80C0	-	7240		MOU.B A0,R0L
	0E80C2	-	0F0A04		MOU.B 040AH,R0H
	0E80C5	-	7EB000		BTST 0,R0
	0E80C8	-	7E2009FA		BMNE 9,R0
	0E80CC	-	070A04		MOU.B R0H,040AH
00083					>
	0E80CF	-	FE24		JMP.B E80F4H

PD30SIM [C:\Daten\Software\M16C\test2\test2\Release\test2.x30] - [Program Window [eingang.c]]

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Go Come Step Over Return Stop Break Reset S/... H/...

View Source Mix Di...

Line	Address	BRK	Objcode	Label	Source/Mnemonic
00007	000000	-	7CF200	_eingang	ENTER #00H
00008	000003	-	760F09082		TST.B #22H,0809H
00009	000008	-	6E26		JNE 0002FH
00010	00000A	-	9C01		OR.B #01H,R0L
	00000C	-	08		MOU.B R0H,R0L
	00000D	-	04		NOP
00011	00000E	-	97FE0A04		AND.B #FEH,040AH
00012	000012	-	B4		MOU.B #0,R0L
	000013	-	760F08040		TST.B #01H,0408H
	000018	-	7E2000FA		BMNE 0,R0
	00001C	-	9104		AND.W R0,A0
	00001E	-	7240		MOU.B A0,R0L
	000020	-	0F0A04		MOU.B 040AH,R0H
	000023	-	7EB000		BTST 0,R0
	000026	-	7E2009FA		BMNE 9,R0
	00002A	-	070A04		MOU.B R0H,040AH
00013	00002D	-	FE24		JMP.B 00052H
00014					else
00015					<
00016	00002F	-	97FE0804		AND.B #FEH,0408H
00017	000033	-	9F010A04		OR.B #01H,040AH
00018	000037	-	B4		MOU.B #0,R0L
	000038	-	760F08040		TST.B #01H,0408H
	00003D	-	7E2000FA		BMNE 0,R0
	000041	-	7204		MOU.B R0L,A0
	000043	-	7240		MOU.B A0,R0L
	000045	-	0F0A04		MOU.B 040AH,R0H
	000048	-	7EB000		BTST 0,R0
	00004B	-	7E2009FA		BMNE 9,R0
	00004F	-	070A04		MOU.B R0H,040AH
00019					>
00020	000052	-	7DF2		EXITD

if<in1 == 0>
in1 = 1;
out1 = 0;
out2 = in1;
in1 = 0;
out1 = 1;
out2 = in1;

Ready 00 h 00 m 00 sec 049 msec 151 usec (0000983029 cycle) MCU : STOP