

## Fuse-Bits

Prozessortakt: 6,144MHz  
 Bootloader wird verwendet

	BODLEVEL	BODEN	SUT		CKSEL			
Bit	7	6	5	4	3	2	1	0
			1	0	3	2	1	0
Default	1	1	1	0	0	0	0	1
Default (Pony)	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>
Oszillator	0	0	1	1	1	1	1	1
Oszillator (Pony)	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

Tabelle 1: Fuse-Bits (lowbyte)

	RSTDISBL	WDTON	SPIEN	CKOPT	EESAVE	BOOTSZ1	BOOTSZ0	BOOTRST
Bit	7	6	5	4	3	2	1	0
Default	1	1	0	1	1	0	0	1
Default (Pony)	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>
Boot	-	1	-	1	1	0	1	0
Boot (Pony)	-	<input type="checkbox"/>	-	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>

Tabelle 2: Fuse-Bits (highbyte)

	N/A	N/A	BLB12	BLB11	BLB02	BLB01	Lock02	Lock01
Bit	7	6	5	4	3	2	1	0
Default			1	1	1	1	1	1
Default (Pony)			<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
Lock			0	0	1	1	1	1
Lock (Pony)			<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

Tabelle 3: LOCK-Bits

Oszillatorbyte: 0xAB