



Tested Programming Values:
 F input = 50 MHz, F_{IF} = 36.0 MHz

 DIV1: 0x06 N = 1700 (FL.O. = 85000 MHz)
 DIV2: 0xA4
 CB: 0x00 FREF = 50KHZ, CP = 250uA
 BB: 0x01 LOW BAND SELECTED
 GAIN(max) = 50dB

Title		TD1316AS/IVP Tuner Test Platine	
Used in	Test and Development	Repl for	N/A
Scale:	N/A	Prj. Nr	100066G
Drw. Type:	PCB Schematic	Drw. Nr	100066G-PHT-TD1316
Size:	A	Rev.	1
File:		TD1316AS.sch	

PCB Name:		TD1416AS/IVP Development Board	
PCB Nr:	60000-0201	Change Index	
PCB Type:	FR4 DS-ILL 2SM 0.062	Change Notification Nr.	
		Date	
		Name	

Drawn		Date	
Modified		16-Jan-2011	
Checked			
		<input checked="" type="checkbox"/> Prototype	<input type="checkbox"/> Production

Unless otherwise specified, all values are as follows:
 resistor values are in Ohms, i.e. 0R47, 5R6, 220, 5K6, 22K
 capacitor values are in Microfarads, i.e. 220p, 0.1u, 2u2, 33
 inductor values are in Microhenries, i.e. 0u22, 5u6, 68, 2m2

Approval:
 This drawing is our property. Duplication, unauthorized use of any kind is prohibited.
 Without Approval use only for inquiry.