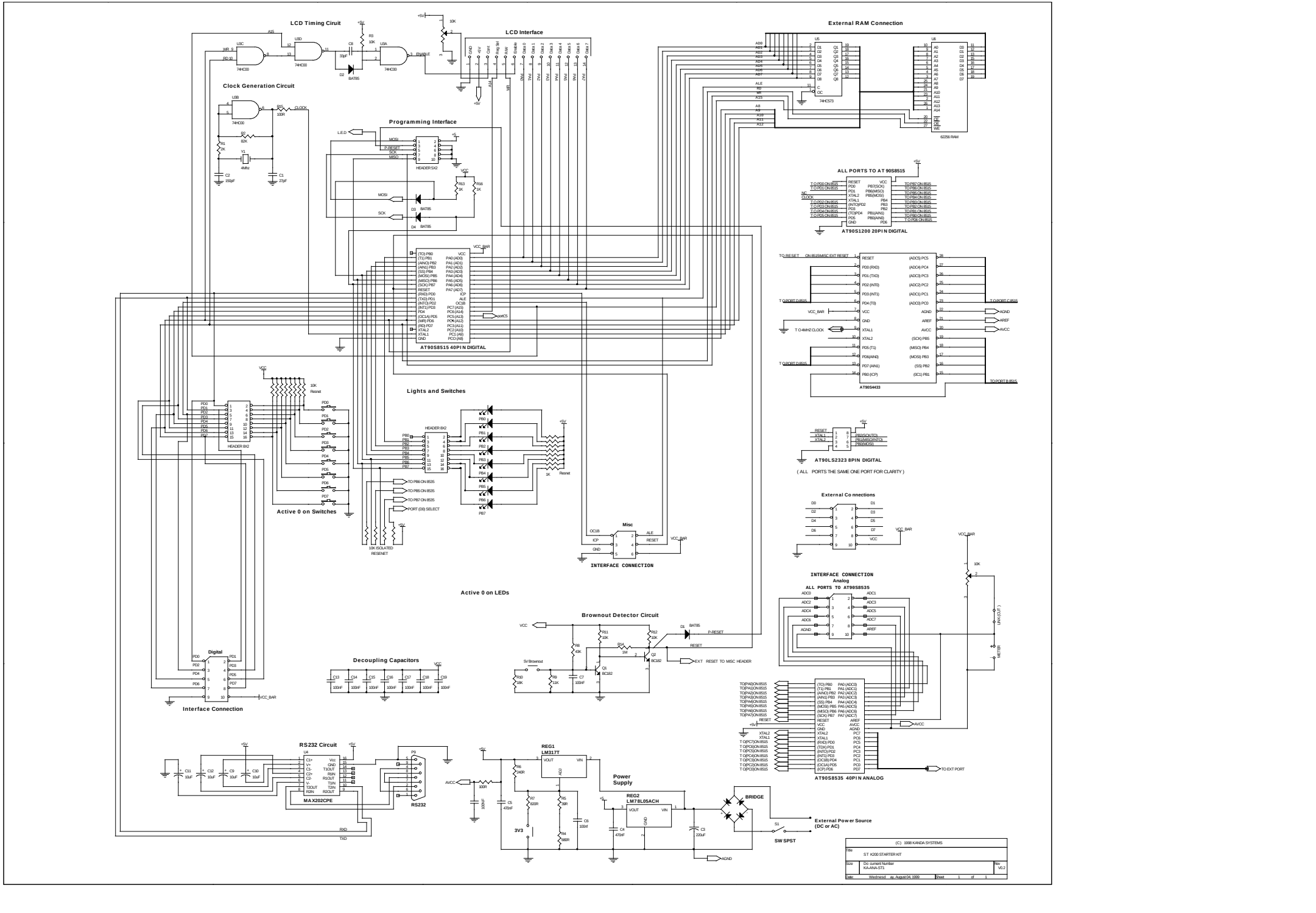
LCD Timing Ciruit
LCD Interface
External RAM Connection
Clock Generation Circuit
Programming Interface
ALL PORTS TO AT90S8515
AT90S1200 20PIN DIGITAL
AT90S8515 40PIN DIGITAL
AT90S4433
AT90LS2323 8PIN DIGITAL
(ALL PORTS THE SAME ONE PORT FOR CLARITY)
Lights and Switches
Active 0 on Switches
Active 0 on LEDs
Misc
INTERFACE CONNECTION
External Connections
INTERFACE CONNECTION
Analog
ALL PORTS TO AT90S8535
Digital
Interface Connection
Decoupling Capacitors
Brownout Detector Circuit
RS232 Circuit
MAX202CPE
RS232
Power Supply
REG1
LM317T
REG2
LM78L05ACH
BRIDGE
3V3
External Power Source
(DC or AC)
SW SPST
AT90S8535 40PIN ANALOG
62256 RAM
74HC573
74HC00
HEADER 8X2
HEADER 3X2
10K Resnet
10K ISOLATED RESENET
1K Resnet
BAT85
TO PB0 ON 8535
TO PB5 ON 8535
TO PB7 ON 8535
PORT (D0) SELECT
INTERFACE CONNECTION
TO EXT PORT
(C) 1998 KANDA SYSTEMS
S.T. K200 STARTER KIT
Do cument Number
KA-ANA-ST1
Rev
V0.2
Wednesd ay, August 04, 1999
Sheet 1 of 1