

Funktion	Rs Rw	U/L Bits	Wartezeit
Funktion Set	00	0x28	40us
Entry Mode Set	00	0x16	40us
Display On	00	0x0C	40us
Clear Display	00	0x01	1.64ms
		Write	
DD RAM ADDRESS 1	00	0x80	40us
E	10	0x45	40us
DD RAM ADDRESS 2	00	0x81	40us
M	10	0x4D	40us
DD RAM ADDRESS 3	00	0x82	40us
B	10	0x43	40us
DD RAM ADDRESS 4	00	0x83	40us
E	10	0x45	40us
DD RAM ADDRESS 5	00	0x84	40us
D	10	0x44	40us
DD RAM ADDRESS 6	00	0x85	40us
D	10	0x44	40us
DD RAM ADDRESS 7	00	0x86	40us
E	10	0x45	40us
DD RAM ADDRESS 8	00	0x87	40us
D	10	0x44	40us
DD RAM ADDRESS 9	00	0x88	40us
	10	0x00	40us
DD RAM ADDRESS 10	00	0x89	40us
S	10	0x53	40us
DD RAM ADDRESS 11	00	0x8A	40us
Y	10	0x59	40us
DD RAM ADDRESS 12	00	0x8B	40us
S	10	0x53	40us
DD RAM ADDRESS 13	00	0x8C	40us
T	10	0x54	40us
DD RAM ADDRESS 14	00	0x8D	40us
E	10	0x45	40us
DD RAM ADDRESS 15	00	0x8E	40us
M	10	0x4D	40us
DD RAM ADDRESS 16	00	0x8F	40us
S	10	0x53	40us