

DVD-RW Drive

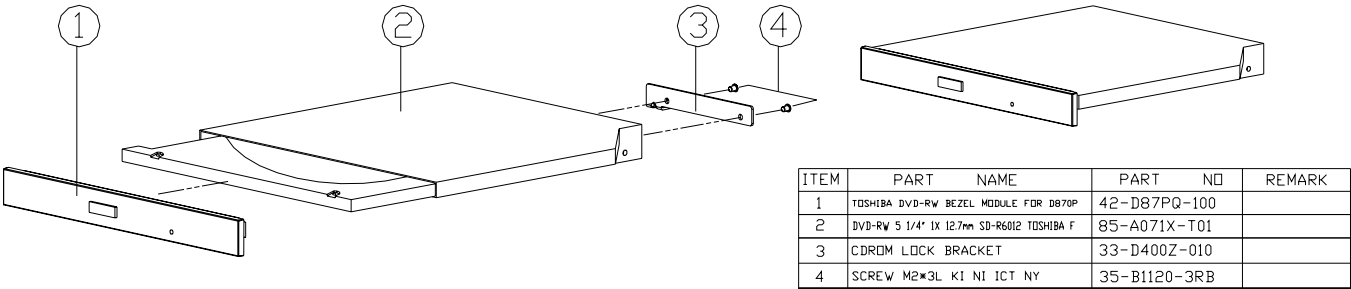
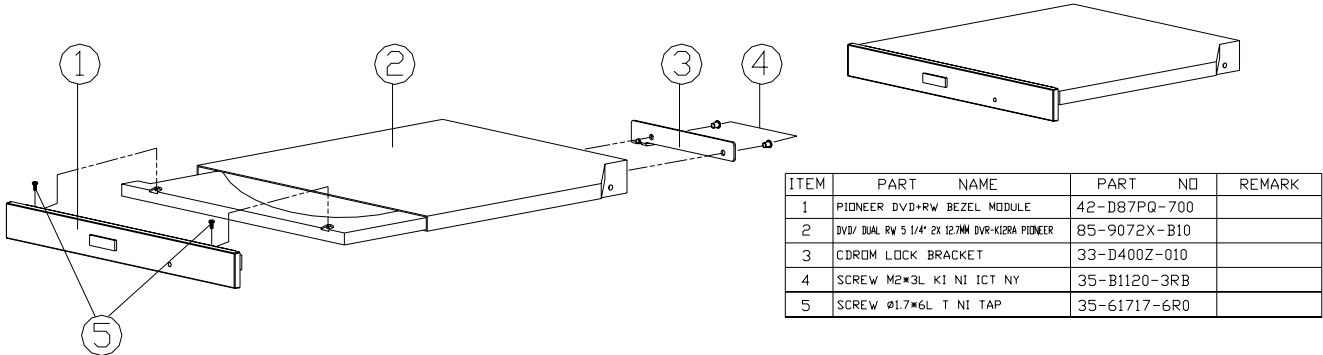


Figure A-11
DVD-RW Drive

Part Lists

DVD+RW Drive - Pioneer

Figure A-12
DVD+RW Drive -
Pioneer



DVD+RW Drive - Toshiba

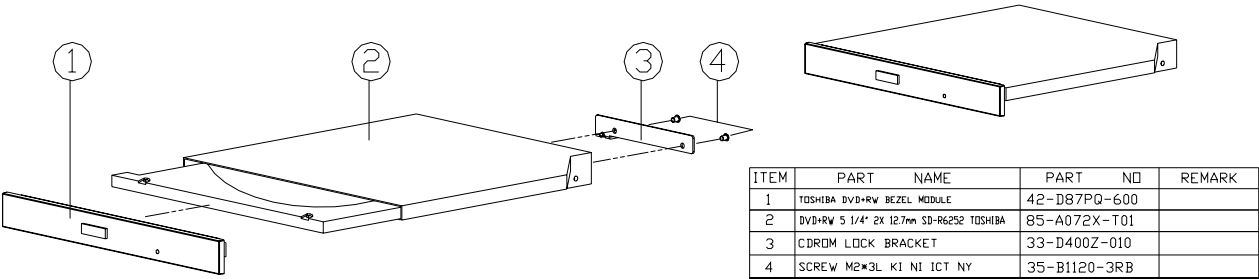
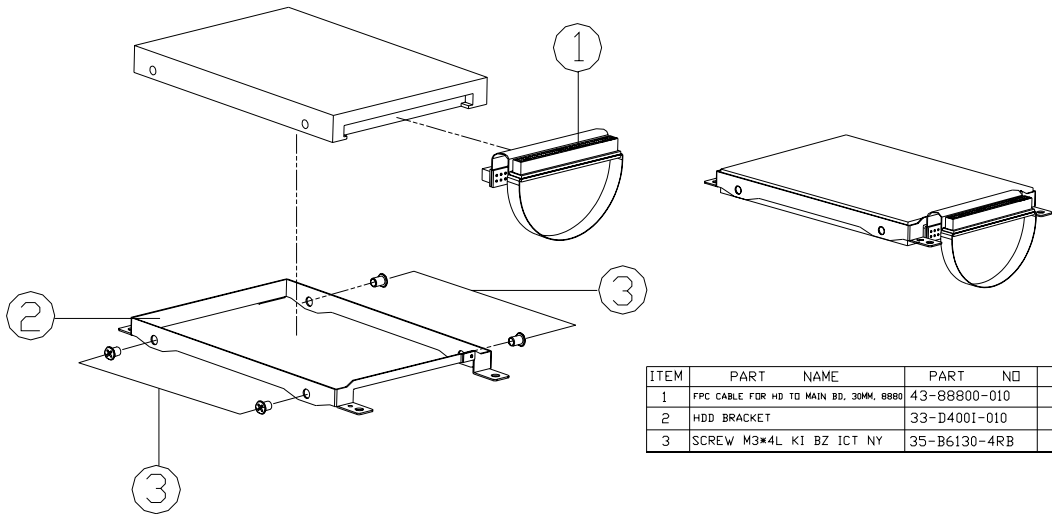


Figure A-13
DVD+RW Drive -
Toshiba

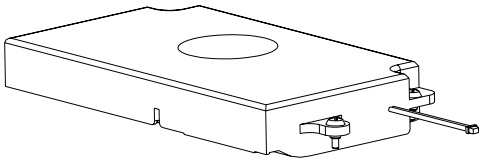
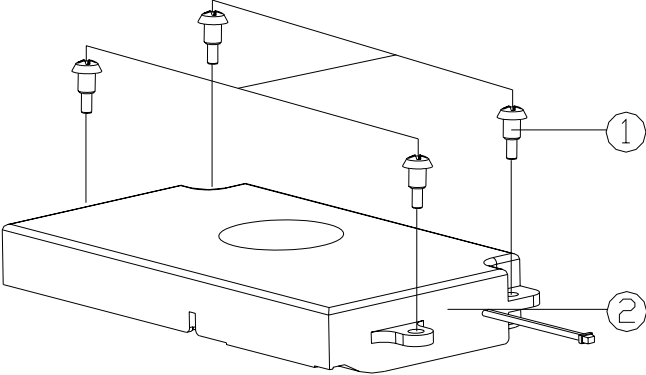
Hard Disk Device

Figure A-14
Hard Disk Device



ITEM	PART NAME	PART NO	REMARK
1	FPC CABLE FOR HD TO MAIN BD, 30MM, 8880	43-88800-010	
2	HDD BRACKET	33-D4001-010	
3	SCREW M3*4L KI BZ ICT NY	35-B6130-4RB	

Sub Woofer Speaker

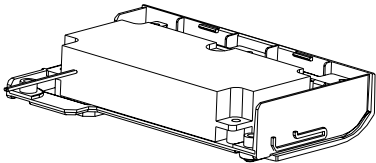
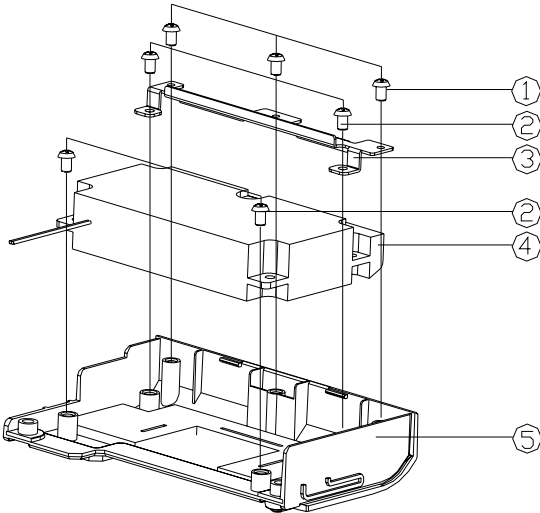


ITEM	PART NAME	PART NO	REMARK
1	SCREW M2.5*BL B LIMITED3.5MM NI ICT	35-41125-8RA	
2	SUB SPERKER MODULE 3W 40MM34S803/SUB	23-55030-100	

Figure A-15
Sub Woofer
Speaker

Sub Woofer Speaker 8

Figure A-16
Sub Woofer
Speaker 8



ITEM	PART NAME	PART NO	REMARK
1	SCREW M2*4L 1 BZ ICT NY	35-C6120-4RA	
2	SCREW M2.5*5L K1 BNI ICT NY	35-B9125-5R0	
3	D870P SUB BRACKET	33-D87P3-050	
4	SPK WITH CABLE 35MM,3W,3 OHM,345B03	23-5A430-352	
5	D870P SUB SPERKER CASE MODULE	42-D87PE-401	

TV Tuner

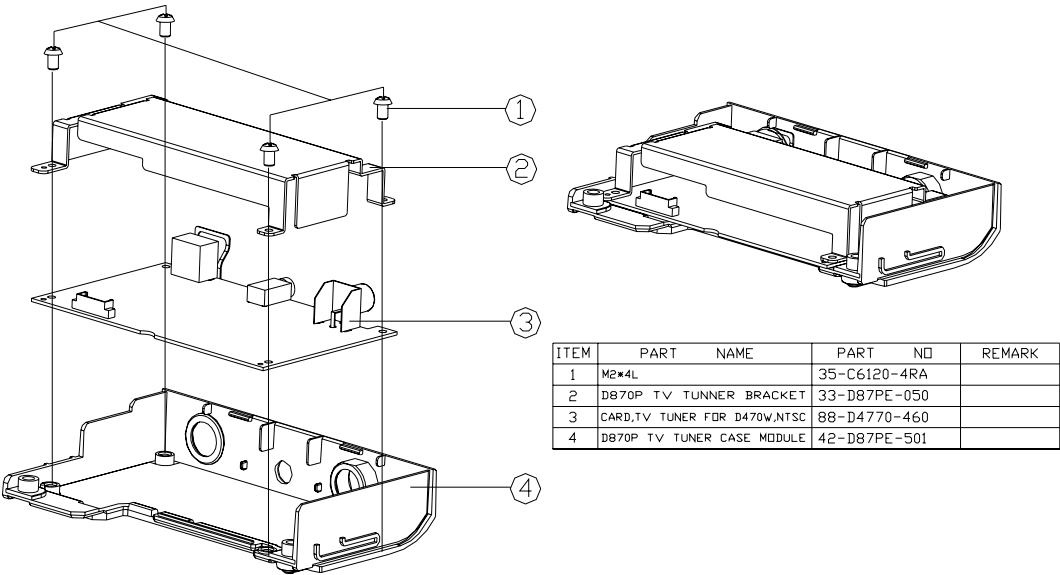
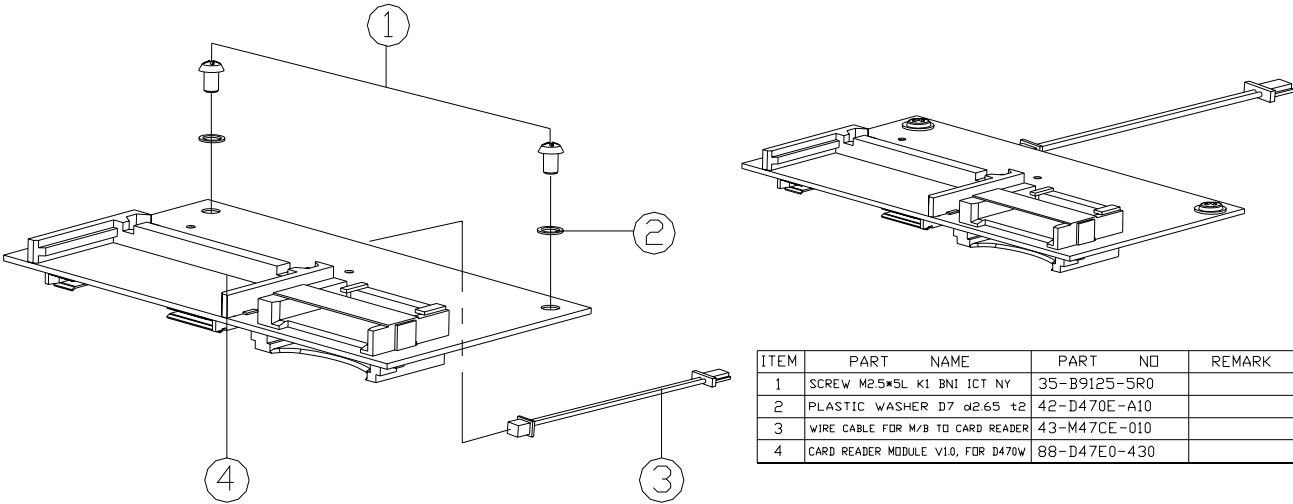


Figure A-17
TV Tuner

Part Lists

Card Reader

Figure A-18
Card Reader



ITEM	PART NAME	PART NO	REMARK
1	SCREW M2.5*5L K1 BNI ICT NY	35-B9125-5R0	
2	PLASTIC WASHER D7 d2.65 t2	42-D470E-A10	
3	WIRE CABLE FOR M/B TO CARD READER	43-M47CE-010	
4	CARD READER MODULE V1.0, FOR D470W	88-D47E0-430	

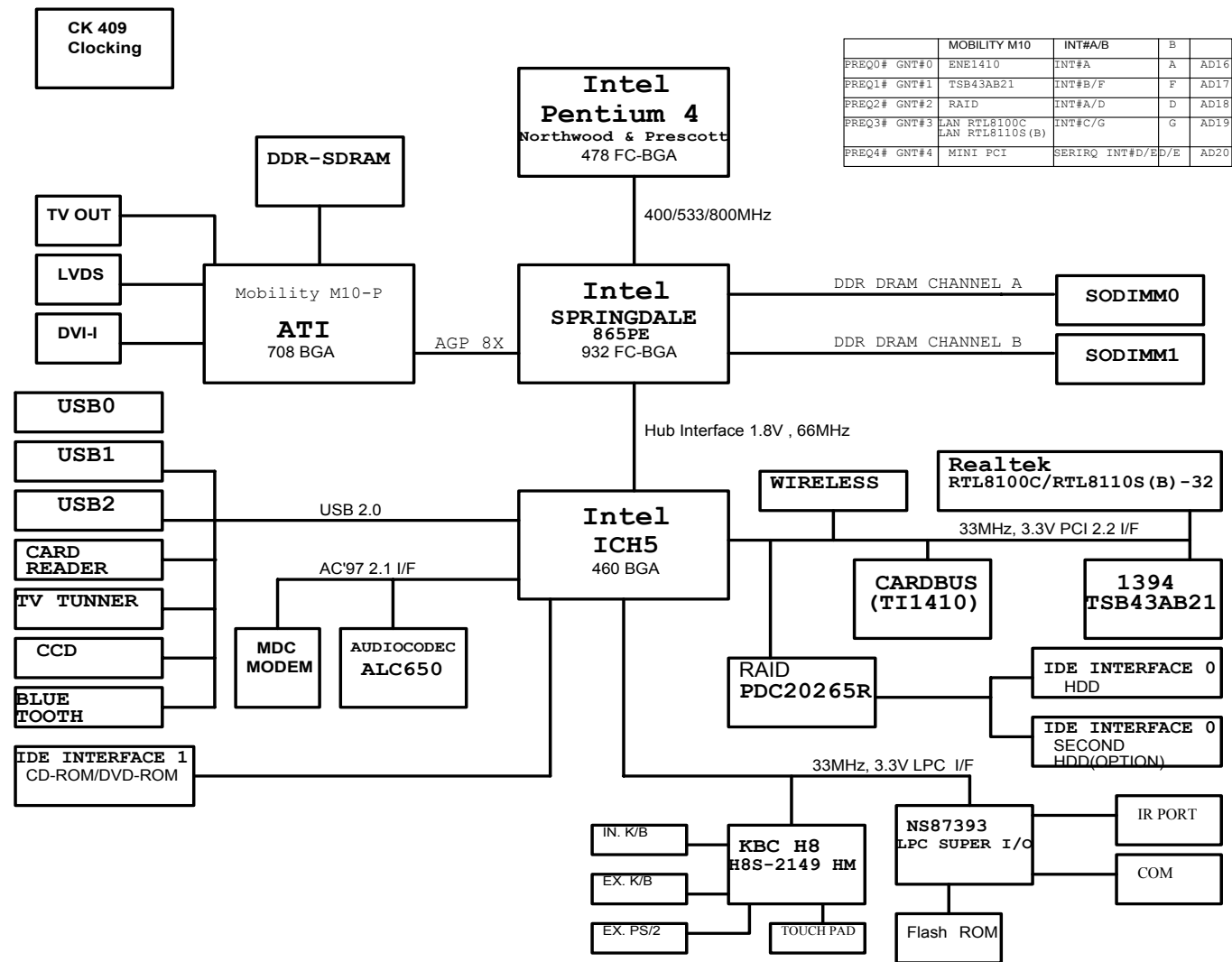
Appendix B: Schematic Diagrams for D870P

This appendix has circuit diagrams of the **D870P notebook** computer's PCBs.

Diagram - Page	Diagram - Page	Diagram - Page
<i>System Block Diagram - Page B - 2</i>	<i>VGA DDR DRAM Termination - Page B - 16</i>	<i>PCMCIA ENE1410 - Page B - 30</i>
<i>CPU Northwood & Prescott (1 of 2) - Page B - 3</i>	<i>Mobility M10-P_POW - Page B - 17</i>	<i>IEEE 1394 TSB43AB21 - Page B - 31</i>
<i>CPU Northwood & Prescott (2 of 2) - Page B - 4</i>	<i>DVI, TV Out & LVDS - Page B - 18</i>	<i>LAN RTL8100C/RTL8110S(B)-32 - Page B - 32</i>
<i>CPU Decoupling - Page B - 5</i>	<i>ICH5 (1 of 2) - Page B - 19</i>	<i>Power Plane - Page B - 33</i>
<i>CLK409 - Page B - 6</i>	<i>ICH5 (2 of 2) - Page B - 20</i>	<i>Vcore - Page B - 34</i>
<i>Springdale (HOST, AGP, Hub) - Page B - 7</i>	<i>USB Port & RTC - Page B - 21</i>	<i>System Power 1 - Page B - 35</i>
<i>Springdale (DDR Interface) - Page B - 8</i>	<i>RAID PDC20265R - Page B - 22</i>	<i>System Power 2 - Page B - 36</i>
<i>DDR Termination - Page B - 9</i>	<i>HDD & CD-R/W - Page B - 23</i>	<i>Charger - Page B - 37</i>
<i>DDR SODIMM - Page B - 10</i>	<i>AMP TPA0202 / ALC650 - Page B - 24</i>	<i>3VH8, VDD1.8 - Page B - 38</i>
<i>Springdale (Voltage, PLL, VSS) - Page B - 11</i>	<i>Audio Jack & Fan Control - Page B - 25</i>	<i>S/W Board & Hot-Key - Page B - 39</i>
<i>Mobility M10-P - Page B - 12</i>	<i>NS87393 LPC Bridge & Super I/O - Page B - 26</i>	<i>TouchPad Switchboard - Page B - 40</i>
<i>Mobility M10-P MEM A/B - Page B - 13</i>	<i>Flash ROM/LPT1 - Page B - 27</i>	
<i>VGA DDR DRAM Channel A - Page B - 14</i>	<i>I/O, FDD, LED & Debug - Page B - 28</i>	
<i>VGA DDR DRAM Channel B - Page B - 15</i>	<i>KBC H8 - Page B - 29</i>	

System Block Diagram

D870P SCHEMATIC

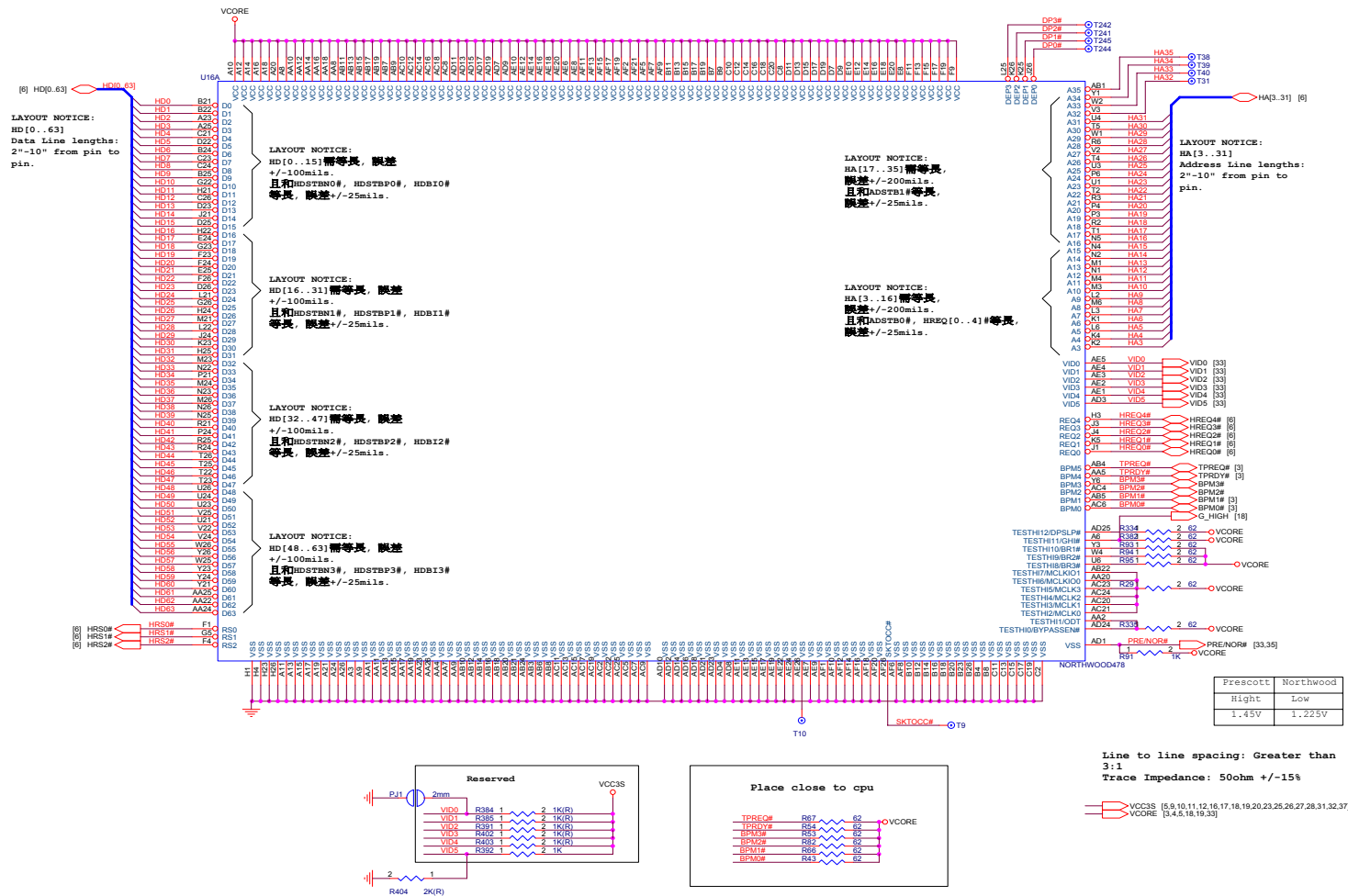


Sheet 1 of 39
System Block
Diagram

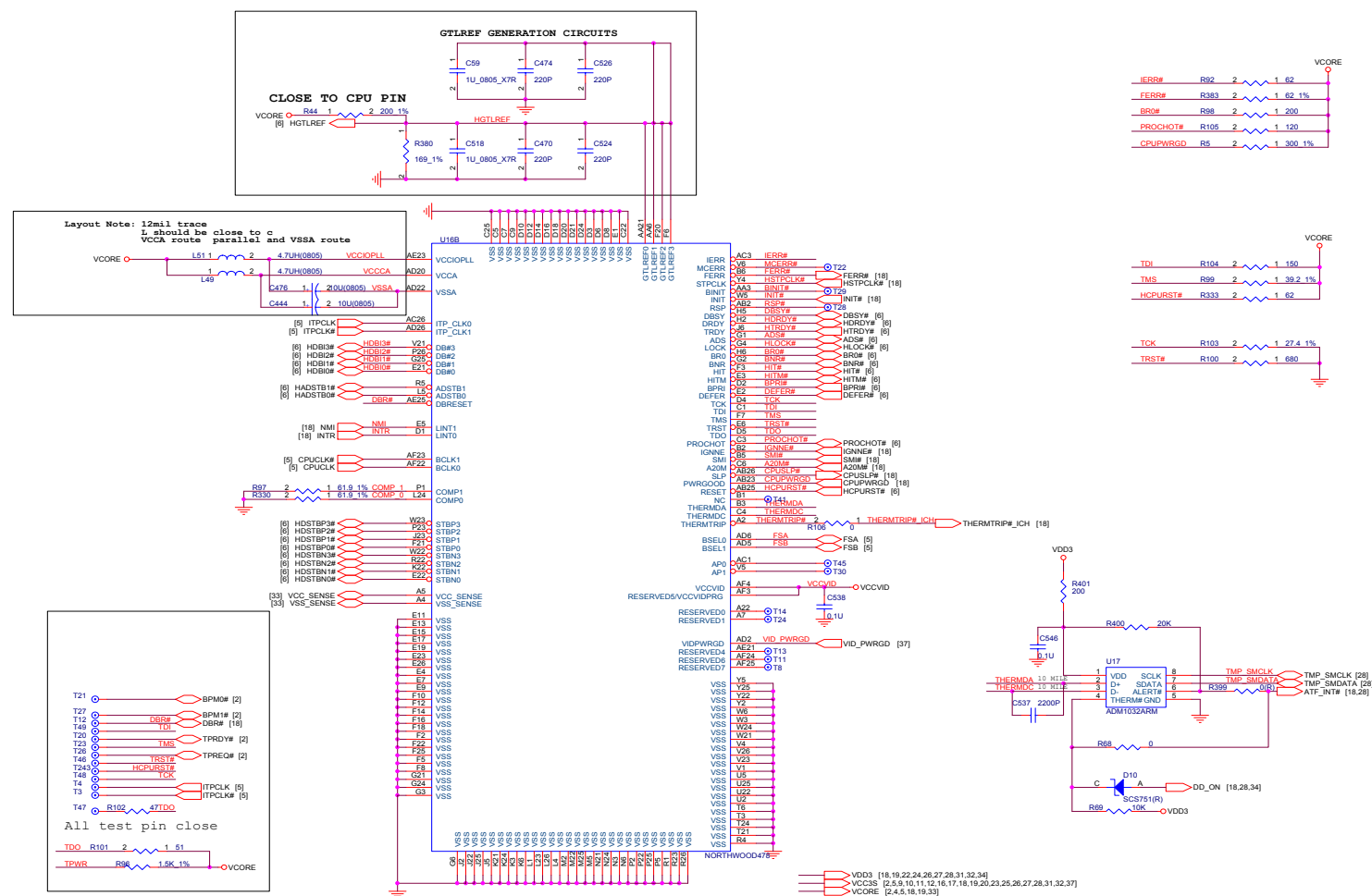
Schematic Diags

CPU Northwood & Prescott (1 of 2)

Sheet 2 of 39
CPU Northwood &
Prescott
(1 of 2)

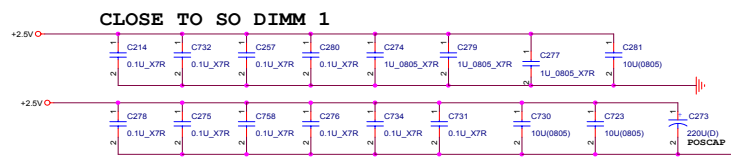
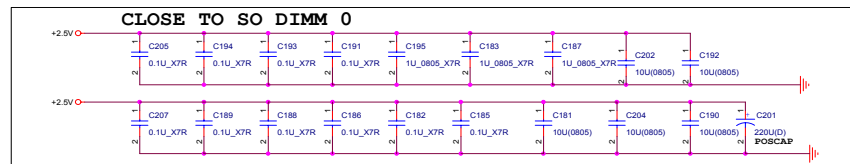
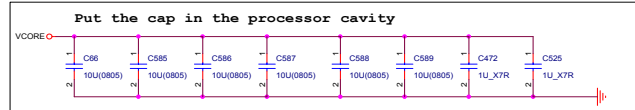
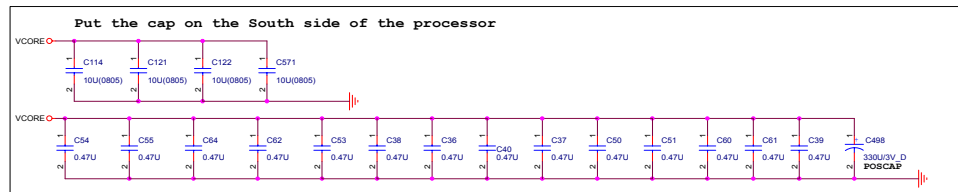
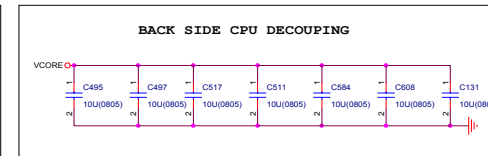
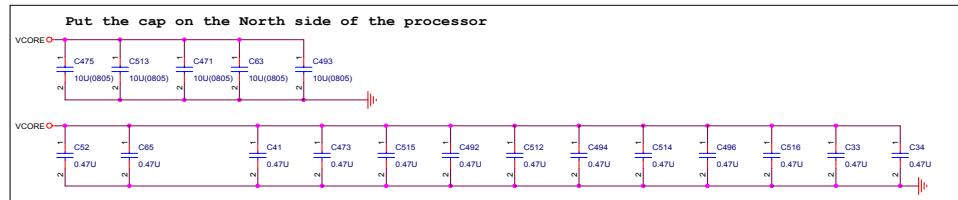


Sheet 3 of 39
CPU Northwood &
Prescott
(2 of 2)



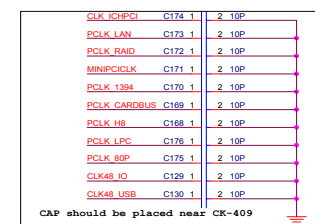
CPU Decoupling

Sheet 4 of 39
CPU Decoupling



+2.5V [7.9,10,35,36]
VCCORE [2,3,5,16,19,33]

Sheet 5 of 39
CLK409



Sheet 6 of 39
Springdale
(HOST, AGP, Hub)

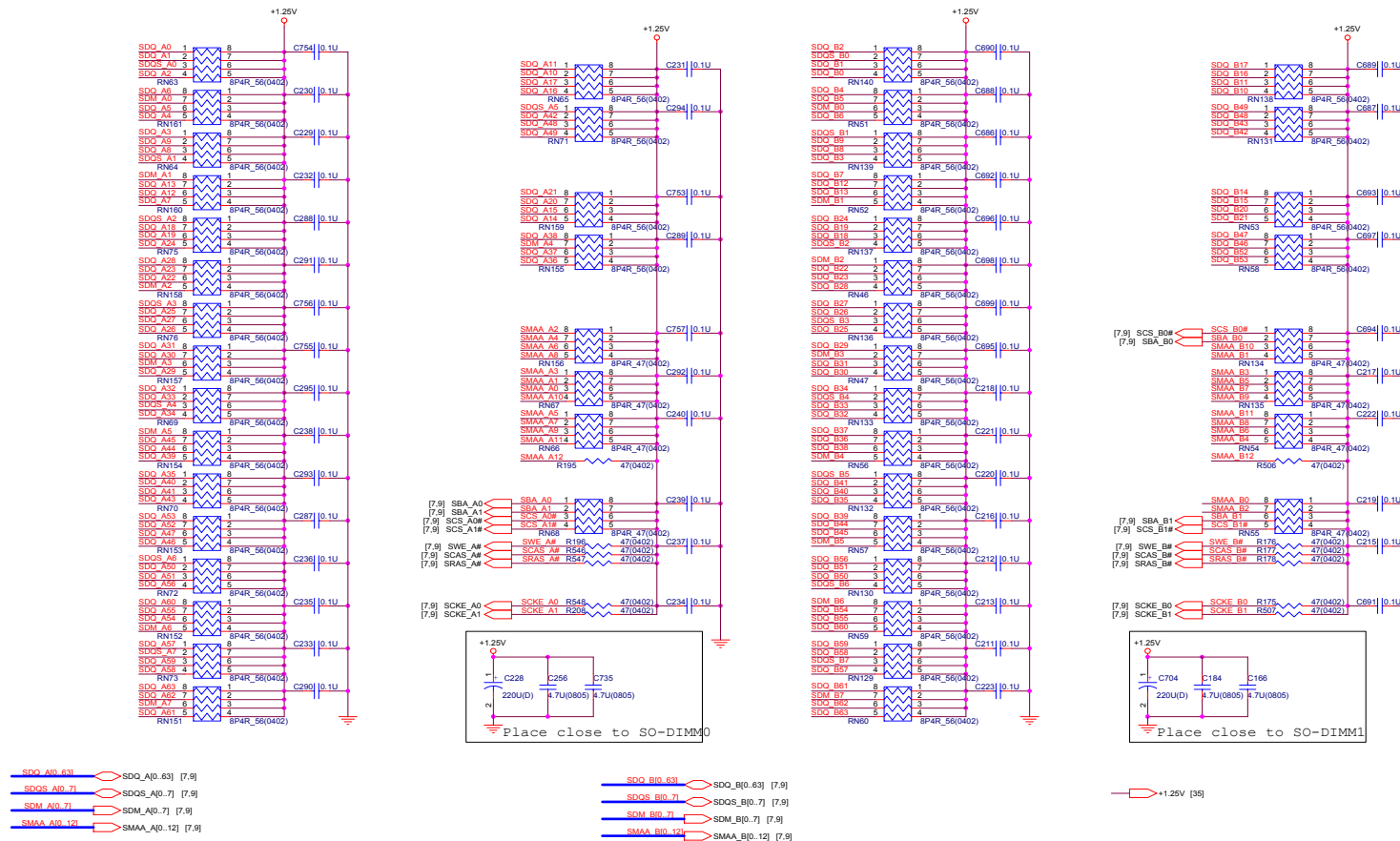


Sheet 7 of 39
Springdale
(DDR Interface)



DDR Termination

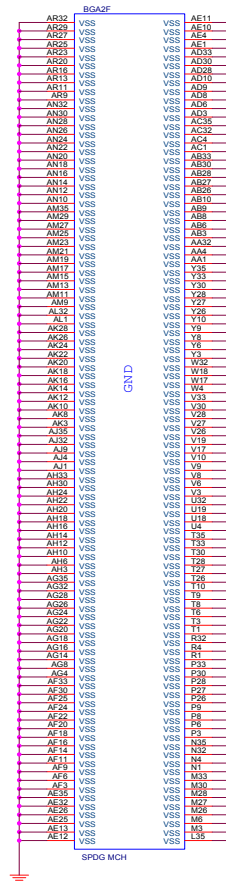
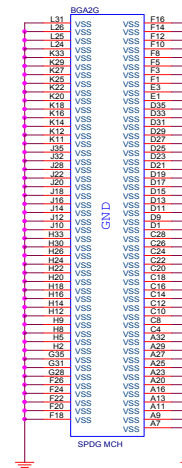
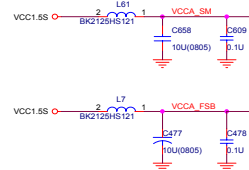
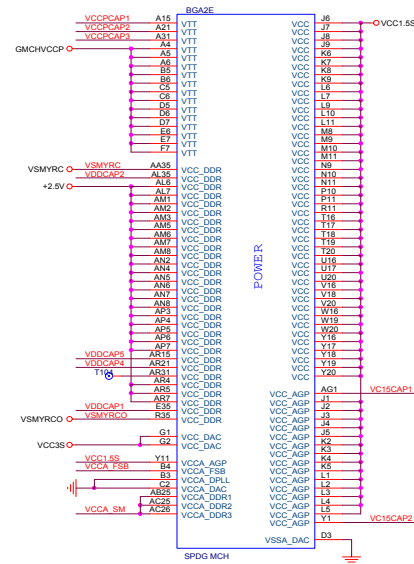
Sheet 8 of 39
DDR Termination



Sheet 9 of 39
DDR SODIMM



Springdale (Voltage, PLL, VSS)



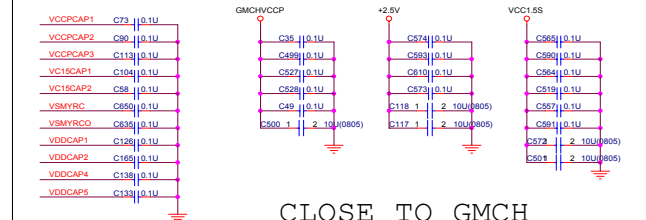
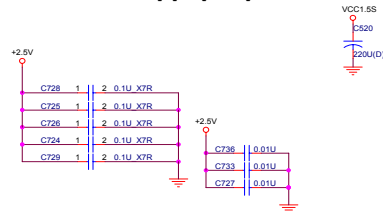
Sheet 10 of 39
Springdale
(Voltage, PLL, VSS)

Schematic Diags

+2.5V [4,7,9,35,36]
VCC1S [2,5,8,11,12,16,17,18,19,20,23,25,26,27,28,31,32,37]
VCC1.5S [8,16,19,32]

Decoupling capacitors (Place near DDR SDRAM DIMM modules)

One 0.1u cap per power pin.

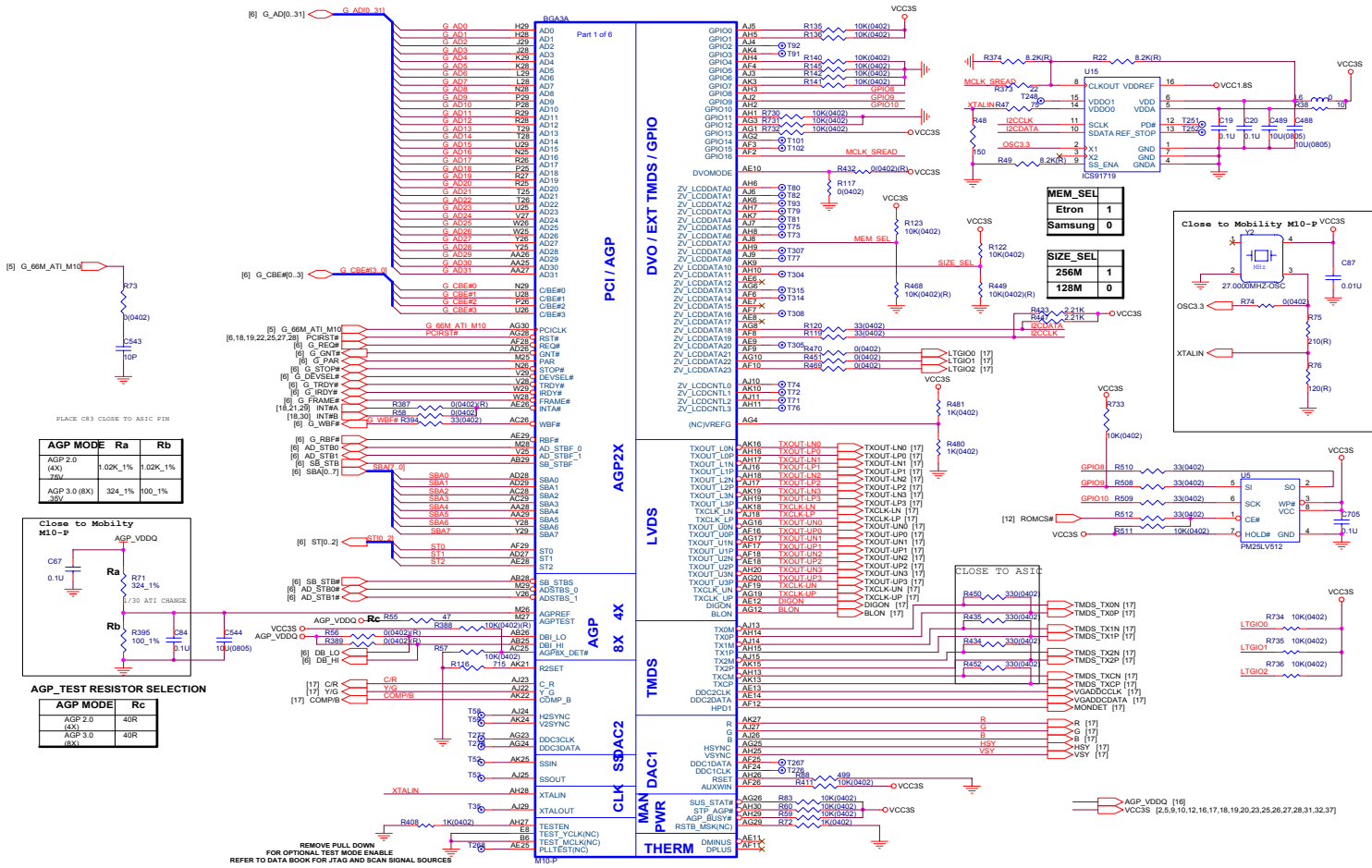


CLOSE TO GMCH

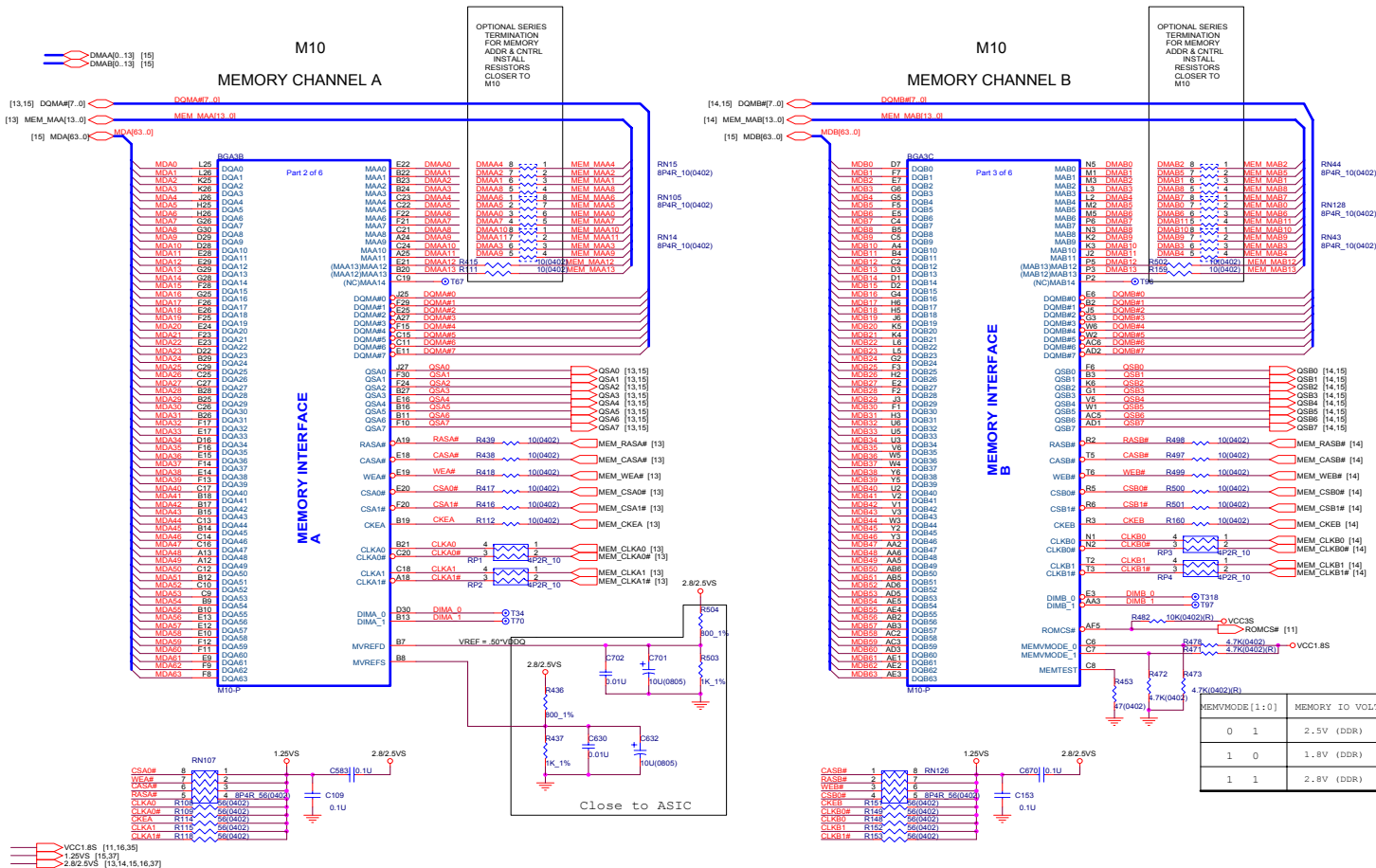
Schematic Diagrams

Mobility M10-P

Sheet 11 of 39
Mobility M10-P



Mobility M10-P MEM A/B



Sheet 13 of 39
VGA DDR DRAM
Channel A



Sheet 14 of 39
VGA DDR DRAM
Channel B



B - 16

