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Vincent Uh	EMERGING DISPLAY	ISSUE: FEB.03, 2010
ROVED BY:	TECHNOLOGIES CORPORATION	TOTAL PAGE: 29
David Chang		VERSION: 3
CUSTOMER	ACCEPTANCE SPEC	CIFICATIONS
(MO	DEL NO.:	
	E T V 5 7 0 G 2 D H U	
	(RoHS)	
FOR	MESSRS:	
CUSTOMER'S APPROV	AL	
DATE:		
BY:		

MODEL NO. VERSION **PAGE** EMERGING DISPLAY TECHNOLOGIES CORPORATION ETV570G2DHU 3 0 - 1DOC . FIRST ISSUE APR.21, 2009 RECORDS OF REVISION REVISED DATE **PAGE** SUMMARY NO. AUG.25, 2009 4. ELECTRICAL CHARACTERISTICS POWER SUPPLY CURRENT FOR VCOM DRIVER: TYP.= $(540) \rightarrow 550$, MAX.= $(700) \rightarrow 710$ FEB.03, 2010 2 3.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS. INPUST SIGNAL VOLTAGE: MAX.=VCC+0.3 →VDD+0.3 3.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS. ITEM REMARK AMBIENT TEMPERATURE NOTE (1): Ta AT -20°C:48HR MAX. \rightarrow -30°C:48HRS MAX. 70°C:168HR MAX. → 80°C:168HRS MAX. 27 13.4.1 STANDARD SPECIFICATIONS FOR RELIABILITY OF LCD MODULE ITEM DESCRIPTION High temperature The sample should be allowed to stand at +60°C for 240 hrs operation Low temperature 2 The sample should be allowed to stand at -10°C for 240 hrs operation High temperature 3 The sample should be allowed to stand at +70°c for 240 hrs storage Low temperature 4 The sample should be allowed to stand at -20°C for 240 hrs storage DESCRIPTION NO ITEM High temperature The sample should be allowed to stand at +70°C for 240 hrs operation Low temperature 2 The sample should be allowed to stand at -20°C for 240 hrs operation High temperature 3 The sample should be allowed to stand at +80°c for 240 hrs storage Low temperature 4 The sample should be allowed to stand at -30°C for 240 hrs storage

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1. GENERAL SPECIFICATIONS			
1.1 APPLICATION NOTES FOR PLEASE REFER TO:	CONTROLLER/DRIVER		
	X H X 8 2 5 0 X H X 8 6 7 8		
INCLUDING PROHIBITED M	LY WITH EUROPEAN ROHS R IATERIALS/COMPONENTS CO KAVALENT CHROMIUM, POLY LYBROMINATED	NTAINING	LEAD,
2. MECHANICAL SPECIFICATION (1) DIAGONALS	NS 5.7 inch		
(2) NUMBER OF DOTS	640W * (RGB) * 480	H DOTS	
(3) MODULE SIZE	147.6W * 100H *11.	7D (MAX) mr	n
	(WITHOUT FPC)		
(4) EFFECTIVE AREA	117.2W * 88.4H mm	n (T/P)	
(5) ACTIVE AREA	115.2W * 86.4H mm	(LCD)	
	116.2W * 87.4H mm	(T/P)	
	0.06W * 0.18H mm		
(7) PIXEL SIZE			
(8) LCD TYPE		VE	
	262K		
(10) VIEWING DIRECTION			
(11) DACK LIGHT	LED , COLOR : WH	ITE	
(12) INTERFACE MODE			

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3. ABSOLUTE MAXIMUM RATINGS

3.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS.

PARAMETER	SYMBOL	MIN.	MAX.	UNIT	REMARK
POWER SUPPLY	VDD-VSS	-0.3	7.0	V	
VOLTAGE	VCC-VSS	-0.3	7.0	V	
INPUT SIGNAL VOLTAGE	VIN-VSS	- 0.3	VDD+0.3	V	
STATIC ELECTRICITY	_	_	_	V	NOTE (1)
LED BACKLIGHT POWER	PD		1.28	W	
DISSIPATION	1 D		1.20	VV	
LED BACKLIGHT FORWARD	IF		60	mA	
CURRENT	IΓ		00	IIIA	
LED BACKLIGHT	VR		45	V	
REVERSE VOLTAGE	V K		43	V	

NOTE (1): LCM SHOULD BE GROUNDED DURING HANDING LCM.

3.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS.

ITEM	OPERATING		STORAGE		REMARK	
I I E IVI	MIN.	MAX.	MIN. MAX.		KEMAKK	
AMBIENT TEMPERATURE	-20°C	70°C	-30°C	80°C	NOTE (1), (2)	
HUMIDITY	NOTE (3)		NOT	E (2)	WITHOUT	
HOMIDIT I			NOTE (3)		CONDENSATION	
VIBRATION		2.45 m/s^2		11.76 m/s^2	10~55Hz	
VIBRATION	BRATION —			(1.2 G)	X, Y, Z, EACH 2HRS	
					6 m SECONDS	
SHOCK		29.4 m/s^2		490 m/s^2	XYZ	
SHOCK		(3 G)	_	(50G)	DIRECTIONS	
					3 TIMES EACH	
CORROSIVE GAS	NOT ACCEPTABLE		NOT ACCEPTABLE			

NOTE (1): Ta AT -30°C: 48HRS MAX.

80°C:168HRS MAX.

NOTE (2): BACKGROUND COLOR CHANGES SLIGHTLY DEPENDING ON AMBIENT

TEMPERATURE THIS PHENOMENON IS REVERSIBLE.

NOTE (3): $Ta \le 60^{\circ}C : 90\%RH MAX (96HRS MAX)$.

Ta > 60°C: ABSOLUTE HUMIDITY MUST BE LOWER THAN THE HUMIDITY

OF 90%RH AT 60°C(96HRS MAX).

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4. ELECTRICAL CHARACTERISTICS

 $Ta = 25 \, ^{\circ}C$

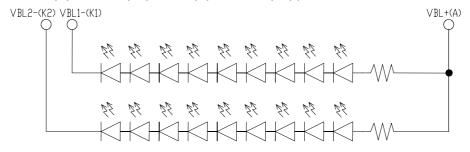
						1a - 25 C
SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
VDD-VSS		3	3.3	3.6	V	
VCC-VSS		3	3.3	3.6	V	
IDD	VDD-VSS =3.3V		8	12	mA	NOTE (1)
ICC	VCC-VSS = 3.3V LED B/L=ON	_	550	710	mA	
VIL		0	_	0.3*VDD	V	NOTE (2)
VIH	_	0.7*VDD	_	VDD	V	NOTE (2)
VOL	$IOL = 400 \mu A$	0	_	0.2*VDD	V	NOTE (2)
VOH	$IOH = -400 \mu A$	0.8*VDD		VDD	V	NOTE (3)
fFRAME		50	60	72	Hz	
V_{F}	I _F =40mA	28	30	32	V	NOTE (4)
		30000	40000		HRS	
	VDD-VSS VCC-VSS IDD ICC VIL VIH VOL VOH fFRAME	VDD-VSS — VCC-VSS — IDD VDD-VSS = 3.3V VCC-VSS = 3.3V LED B/L=ON VIL — VIH — VOL IOL = 400μA VOH IOH = -400μA fframe —	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	VDD-VSS — 3 3.3 VCC-VSS — 3 3.3 IDD VDD-VSS =3.3V — 8 ICC = 3.3V — 550 VIL — 0 — VIL — 0.7*VDD — VIH — 0.7*VDD — VOL IOL = 400μA 0 — VOH IOH = -400μA 0.8*VDD — fFRAME — 50 60 V _F I _F =40mA 28 30	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	VDD-VSS — 3 3.3 3.6 V VCC-VSS — 3 3.3 3.6 V IDD VDD-VSS = 3.3V = - 8 12 mA ICC = 3.3V = - 550 710 = mA VIL — 0 — 0.3*VDD V VIH — 0.7*VDD — VDD V VOL IOL = 400μA 0 — 0.2*VDD V VOH IOH = -400μA 0.8*VDD — VDD V fFRAME — 50 60 72 Hz V _F I _F =40mA 28 30 32 V

NOTE (1): THE DISPLAY PATTERN IS ALL "WHITE".

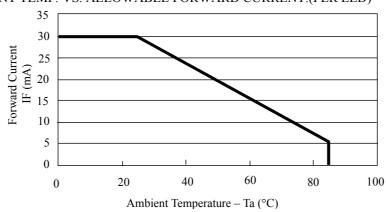
NOTE (2): APPLIED TO TERMINALS / RESET, HSYNC, VSYNC, ENB, DCLK, B5~B0, G5~G0, R5~R0.

NOTE (3): APPLIED TO TERMINALS B5~B0, G5~G0, R5~R0.

NOTE (4): INTERNAL CIRCUIT DIAGRAM OF BACKLIGHT (VF=VBL+(A)—VBL1-(K1)=VBL+(A)—VBL2-(K2))



NOTE (5): AMBIENT TEMP. VS. ALLOWABLE FORWARD CURRENT.(PER LED)

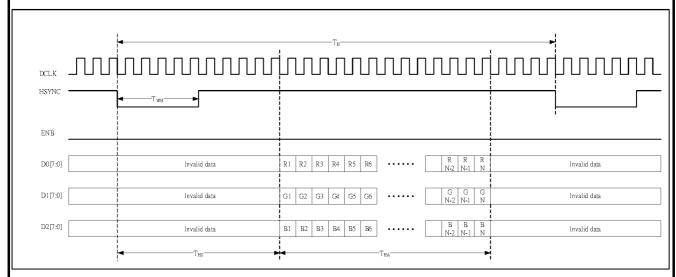


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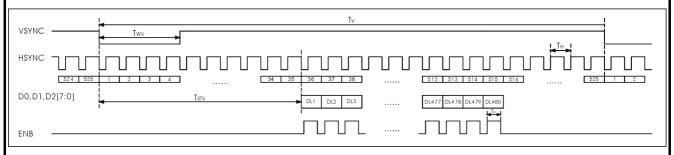
5. TIMING CHARACTERISTICS

5.1 DIGITAL PARALLEL RGB INTERFACE (SYNC MODE)

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
DCLK FREQUENCY	F_{CPH}	22.66	25.175	27.69	MHz
DCLK PERIOD	T_{CPH}	36.11	39.7	44.13	ns
DCLK PULSE DUTY	T_{CWH}	40	50	60	%
HSYNC PERIOD	T_{H}	750	800	850	T_{CPH}
HSYNC PULSE WIDTH	$T_{ m WH}$	5	30	_	T_{CPH}
HSYNC FIRST HORIZONTAL DATA TIME	T_{HS}	112	144	175	T_{CPH}
HORIZONTAL ACTIVE DATA AREA	T_{HA}	_	640	_	T_{CPH}
VSYNC PULSE WIDTH	T_{WV}	1	3	5	T _H
FIRST LINE DATA INPUT TIME	T_{STV}	_	35	_	T_{H}
VSYNC PERIOD	T_{V}	515	525	535	T_{H}



HORIZONTAL TIMING

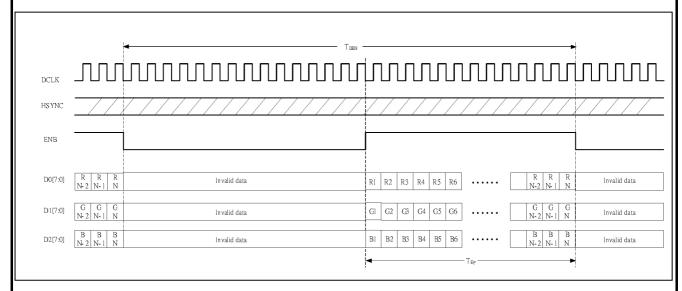


VERTICAL TIMING

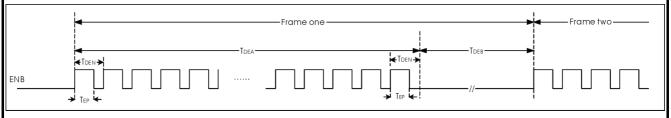
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5.2 DIGITAL PARALLEL RGB INTERFACE (DE MODE)

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
DCLK FREQUENCY	F_{CPH}	22.66	25.175	27.69	MHz
DCLK PERIOD	T_{CPH}	36.11	39.7	44.13	ns
DCLK PULSE DUTY	T_{CWH}	40	50	60	%
ENB PERIOD	$T_{ m DEN}$	750	800	850	T_{CPH}
ENB PULSE WIDTH	T_{EP}		640		T_{CPH}
ENB FRAME ACTIVE TIME	T_{DEA}	_	480	_	T_{DEN}
ENB FRAME BLANKING TIME	T_{DEB}	10	45	110	T_{DEN}



HORIZONTAL TIMING



VERTICAL TIMING

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6. OPTICAL CHARACTERISTICS (NOTE1)

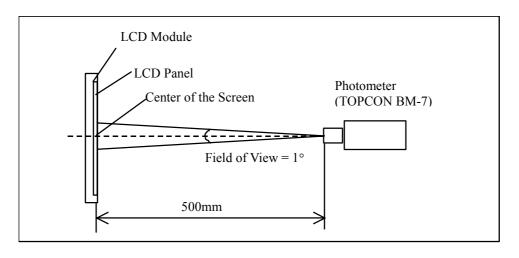
6.1 OPTICAL CHARACTERISTICS

 $Ta = 25 \pm 2$ °C

I T E M		SYMBOL	COND	ITION	MIN.	TYP.	MAX.	UNIT	REMARK	
		θ_{y^+}		0 -00	50	55				
VIEWING ANGL	E	$\theta_{ ext{y-}}$	$\theta_{x}=0^{\circ}$	θ_{x} –0	47	52		4	NOTE(2)	
VIEWING ANGL	E	$\theta_{\mathrm{x}^{+}}$	$CR \ge 10$ $\theta_y=0^{\circ}$		0 -00	60	65		deg.	NOTE (3)
		θ_{x}		60	65					
CONTRAST RAT	Oľ	CR	θx=0°,	θy=0°	300	350			NOTE (3)	
RESPONSE TIME	2	T _R (rise)	0 00 0 00			15	30	msec	NOTE (4)	
KESFONSE HIVIE	2	T_F (fall)	$\theta x = 0$,	$\theta x=0^{\circ}$, $\theta y=0^{\circ}$		35	50			
	WHITE	Wx			0.26	0.31	0.36			
		Wy			0.30	0.35	0.40			
COLOROE	RED -	Rx			0.56	0.61	0.66			
COLOR OF CIE		Ry	$\theta x=0^{\circ}$, $\theta y=0^{\circ}$ IF = 40mA		0.31	0.36	0.41	_	NOTE (5)	
COORDINATE	GREEN	Gx		: 50 %	0.28	0.33	0.38	_	NOTE (5)	
COORDINATE	UKEEN	Gy			0.51	0.56	0.61			
	BLUE	Bx		0	0.09	0.14	0.19			
	BLUE	Ву				0.12	0.17			
THE BRIGHTNESS		В			350	400		cd/m ²		
OF MODULE		D		θy=0°	330	400		Cu/III	NOTE (6)	
THE UNIFORMITY OF			IF = 4	40mA	75	80		%	110112 (0)	
MODULE					75	00		70		

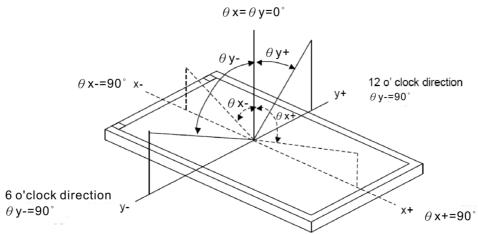
NOTE (1): TEST EQUIPMENT SETUP:

AFTER STABILIZING AND LEAVING THE PANEL ALONE AT A GIVEN TEMPERATURE FOR 30 MINUTES, THE MEASUREMENT SHOULD BE EXECUTED. MEASUREMENT SHOULD BE EXECUTED IN A STABLE, WINDLESS, AND DARK ROOM. OPTICAL SPECIFICATIONS ARE MEASURED BY TOPCON BM-7 (FAST) WITH A VIEWING ANGLE OF 1° AT A DISTANCE OF 50cm AND NORMAL DIRECTION.



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NOTE (2): DEFINITION OF VIEWING ANGLE:

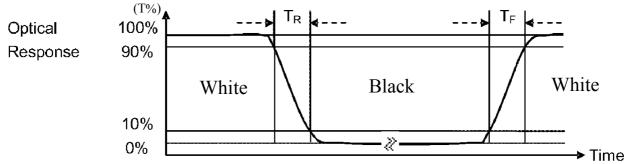


Normal

NOTE (3): DEFINITION OF CONTRAST RATIO:

 $\label{eq:contrast_ratio} \text{CONTRAST RATIO(CR)} = \frac{\text{BRIGHTNESS MEASURED WHEN LCD IS AT "WHITE STATE"}}{\text{BRIGHTNESS MEASURED WHEN LCD IS AT "BLACK STATE"}}$

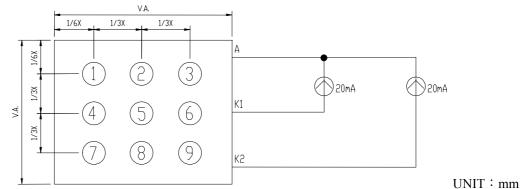
NOTE (4) : DEFINITION OF RESPONSE TIME : T_R AND T_F THE FIGURE BELOW IS THE OUTPUT SIGNAL OF THE PHOTO DETECTOR.



NOTE (5): THE 100% TRANSMISSION IS DEFINED AS THE TRANSMISSION OF LCD PANEL WHEN ALL THE INPUT TERMINALS OF MODULE ARE ELECTRICALLY OPENED.

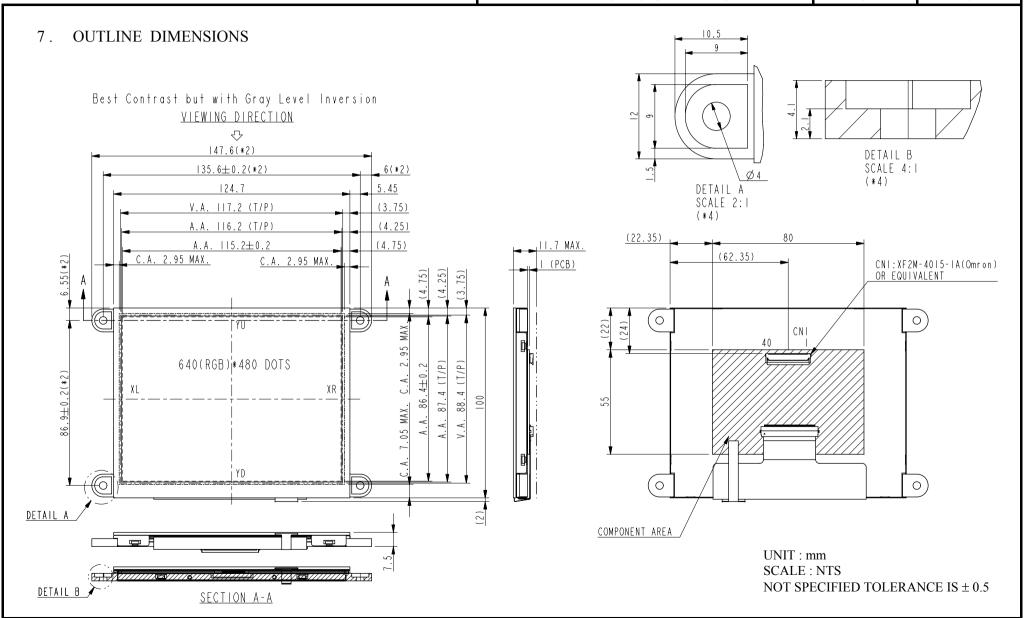
NOTE (6): BRIGHTNESS MEASURED WHEN LCD IS AT "WHITE STATE"

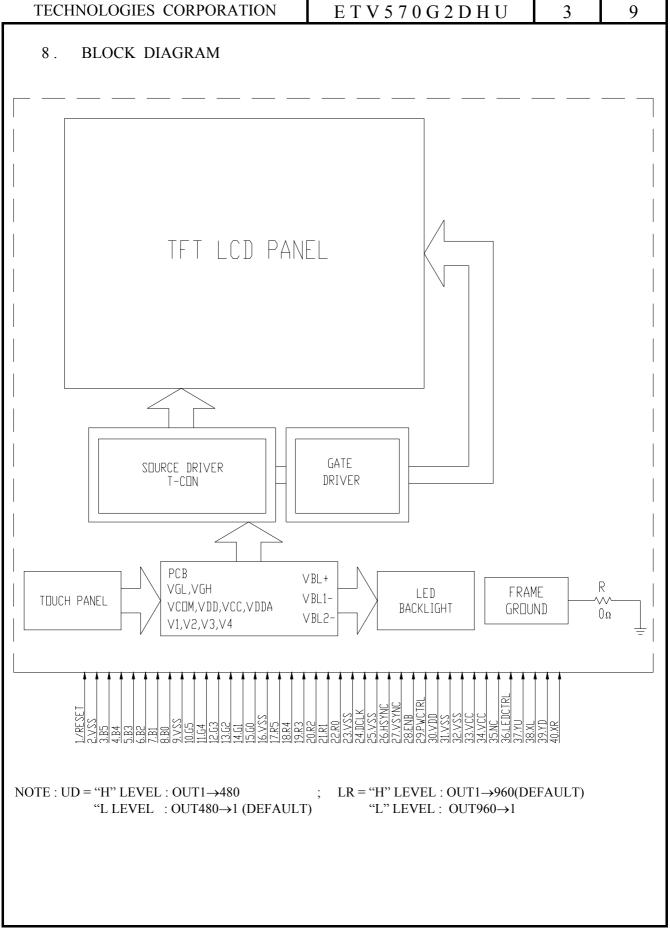
6.2 THE TEST METHOD OF BRIGHTNESS AND UNIFORMITY



6.3 THE CALCULATING METHOD OF UNIFORMITY

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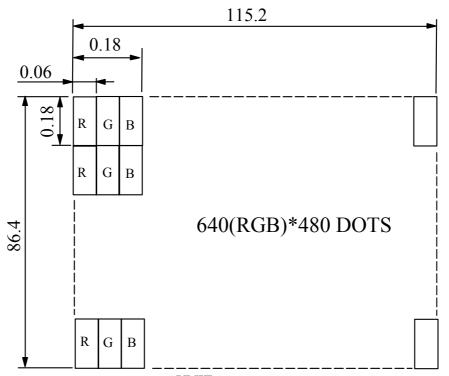


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9. DETAIL DRAWING OF DOT MATRIX



UNIT : mm SCALE : NTS

NOT SPECIFIED TOLERANCE IS \pm 0.1 DOTS MATRIX TOLERANCE IS \pm 0.01

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10. INTERFACE SIGNALS

PIN NO	SYMBOL	I/O	FUNCTION
1	/RESET	I	HARDWARE RESET
2	VSS	P	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)
3	В5	I	BLUE DATA BIT 5
4	B4	I	BLUE DATA BIT 4
5	В3	I	BLUE DATA BIT 3
6	B2	I	BLUE DATA BIT 2
7	B1	I	BLUE DATA BIT 1
8	В0	I	BLUE DATA BIT 0
9	VSS	P	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)
10	G5	I	GREEN DATA BIT 5
11	G4	I	GREEN DATA BIT 4
12	G3	I	GREEN DATA BIT 3
13	G2	I	GREEN DATA BIT 2
14	G1	I	GREEN DATA BIT 1
15	G0	I	GREEN DATA BIT 0
16	VSS	P	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)
17	R5	I	RED DATA BIT 5
18	R4	I	RED DATA BIT 4
19	R3	I	RED DATA BIT 3
20	R2	I	RED DATA BIT 2
21	R1	I	RED DATA BIT 1
22	R0	I	RED DATA BIT 0
23	VSS	P	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)
24	DCLK	I	DOT DATA CLOCK
25	VSS	P	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)
26	HSYNC	I	HORIZONTAL SYNC INPUT
27	VSYNC	I	VERTICAL SYNC INPUT
28	ENB	I	DATA ENABLE INPUT

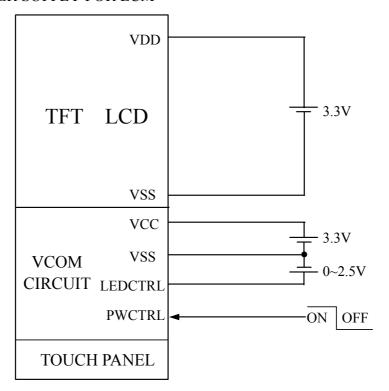
PIN NO	SYMBOL	I/O		FUNCTION		
				PWCTI	RL	REMARK
			LOGIC LEVEL	Н		POWER ON
29	PWCTRL	I	H=3.3V L=0V	L		SHUTDOWN
			WHEN INTERNAL LE	D DRIVER : J	P15 1-2(DE	FAULT)
			WHEN EXTERNAL LE	ED DRIVER : J	IP15 2-3	
30	VDD	P	POWER SUPPLY FOR	DIGITAL CIR	CUIT	
31	VSS	P	GROUND (VSS IS CON CONDUCTIVE TAPE)	NNECTED TO	METAL H	OUSING WITH
32	VSS	P	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)			
33	VCC	P	POWER SUPPLY FOR VCOM DRIVER CIRCUIT			
34	VCC	P	POWER SUPPLY FOR VCOM DRIVER CIRCUIT			
35	NC		NON CONNECTION (USING INTERNAL LED DRIVER) OR ANODE (USING EXTERNAL LED DRIVER) WHEN INTERNAL LED DRIVER: JP5 1-2 (DEFAULT) WHEN EXTERNAL LED DRIVER: JP5 2-3			
36	LEDCTRL	I	BRIGHTNESS CONTROL FOR LED BACKLIGHT; LEDCTRL (USING INTERNAL LED DRIVER) OR CATHODE (USING EXTERNAL LED DRIVER) WHEN INTERNAL LED DRIVER: JP6 1-2 (DEFAULT) JP14 1-2 (DEFAULT) WHEN EXTERNAL LED DRIVER: JP6 2-3 JP14 NON CONNECTION			
37	YU	_	TOP PANEL			
38	XL	_	LEFT PANEL			
39	YD				TOUCH PA	ANEL
40	XR		RIGHT PANEL		1	

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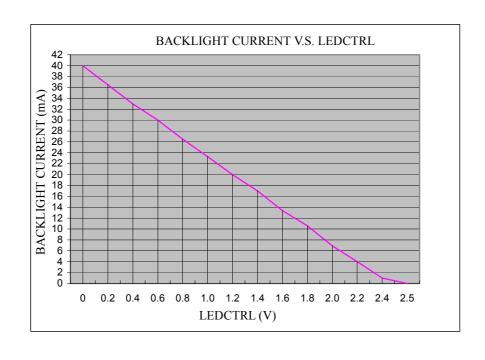
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11. POWER SUPPLY

11.1 POWER SUPPLY FOR LCM



11.2 THE BRIGHTNESS CONTROLLED BY BACKLIGHT CURRENT OF LEDCTRL



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12. TOUCH PANEL SPECIFICATION

12.1 ELECTRICAL CHARACTERISTICS

 $Ta = 25^{\circ}C$

ITEM	CONDITION	SPEC.	UNIT
LINEARITY	_	≤ 1.5	%
TRANSMISSION	ASTM D1003	80 OR MORE	%
ON LOAD	POLYACETAL PEN INPUT	15 ~ 80	g
TERMINAL RESISTANCE	X AXIS	400 ~ 1000	Ω
TERMINAL RESISTANCE	Y AXIS	200 ~ 700	52
INSULATION RESISTANCE	DC25V	≥ 10	$M\Omega$
INPUT VOLTAGE	_	5	V

12.2 PRECAUTIONS IN USE OF TOUCH PANEL

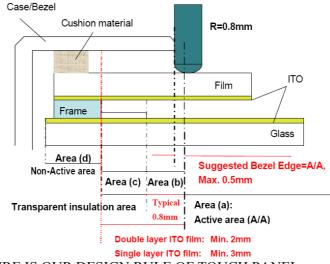
12.2.1 PURPOSE:

IN ORDER TO PREVENT ACCIDENTAL USE AND PERFORMANCE DETERIORATION, PLEASE KEEP THE FOLLOWING PRECAUTIONS AND INHIBITED POINTS.

12.2.2 ITEM AND ILLUSTRATION:

(1) STRUCTURE, AREA DEFINITION

THE STRUCTURE AND THE PERFORMANCE GUARANTEED AREA OF THIS TOUCH PANEL ARE DEFINED BELOW:



THE ABOVE FIGURE IS OUR DESIGN RULE OF TOUCH PANEL. IF IT CANNOT MEET YOUR REQUIREMENT, PLEASE CONTACT WITH OUR ENGINEERS FOR FURTHER DISCUSSION.

ABOVE FIGURE ILLUSTRATES THE RECOMMENDED BEZEL AND CUSHION DESIGN. IN ORDER TO PREVENT

UNUSUAL PERFORMANCE DEGRADATION AND MALFUNCTION OF A TOUCH PANEL, PLEASE CARRY OUT THE SET

CASE DESIGNING AND A TOUCH PANEL ASSEMBLING METHOD AFTER SURELY CONSIDERING THE DEFINITION OF EACH AREA ILLUSTRATED IN ABOVE FIGURE.

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AREA(a) : ACTIVE AREA

THE ACTIVE AREA IS GUARANTEED THE POSITION DATA DETECTABLE PRECISION, OPERATION FORCE AND OTHER OPERATIONS. IT IS STRONGLY RECOMMENDED TO PLACE THE OPERATION BUTTON OR MENU KEYS WITHIN THE ACTIVE AREA. DUE TO STRUCTURE, THE ACTIVE AREA IS LESS DURABLE AT THE EDGE OR CLOSE TO THE EDGE.

AREA(b): OPERATION NON-GUARANTEED AREA

THIS AREA DOES NOT GUARANTEE A TOUCH PANEL OPERATION AND ITS FUNCTION. WHEN THIS AREA IS PRESSED, TOUCH PANEL SHOWS DEGRADATION OF ITS PERFORMANCE AND DURABILITY SUCH AS A PEN SLIDING DURABILITY BECOMES ABOUT ONE-TENTH COMPARED WITH THE ACTIVE AREA (AREA-(A) AS GUARANTEED AREA) AND ITS OPERATION FORCE REQUIRES ABOUT DOUBLE. ABOUT 0.5 MM OUTSIDE FROM A BOUNDARY OF THE ACTIVE AREA CORRESPONDS TO THIS AREA.

AREA(c): PRESSING PROHIBITION AREA

THE AREA WHICH FORBIDS PRESSING, BECAUSE AN EXCESSIVE LOAD IS APPLIED TO A TRANSPARENT ELECTRODE (ITO) AND A SERIOUS DAMAGE IS GIVEN TO A TOUCH PANEL FUNCTION BY PRESSING.

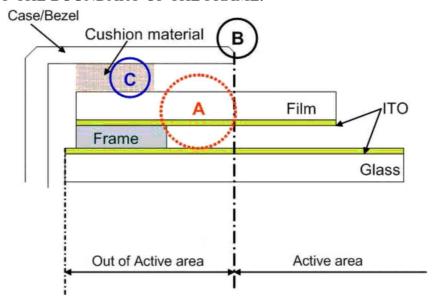
AREA(d): NON-ACTIVE AREA

THE AREA DOES NOT ACTIVATE EVEN IF PRESSED.

- (2) CAUTIONS FOR INSTALLING AND ASSEMBLING
 - (i) DO NOT GIVE EXCESSIVE STRAIN TO THE PRODUCT.
 - (ii) FLEXIBLE PATTERN CABLE IS CONNECTED TO THE BODY BY THERMAL PRESSURE METHOD. SO, DO NOT APPLY EXCESSIVE FORCES TO THE FLEXIBLE PATTERN. DO NOT ADD AN EXCESSIVE FORCE TO A FPC(FLEX TAIL) THAT MAKES PEELING OFF OF THE FPC FROM THE PRODUCT. DO NOT FIX, ADHERE OR MOUNT ANY ADDITIONAL GOODS ON THE FPC SUCH AS ADDITIONAL FILM/PLATE ON THE FPC, BECAUSE SUCH ADDITIONAL GOODS WILL APPLY A STRESS AT THE FPC BONDING AREA. IT MAY AFFECT THE CONDUCTIVITY OF FPC WITH TOUCH PANEL.
 - (iii) IN ORDER NOT TO APPLY LOAD ON THE DISPLAY, PROVIDE A CLEARANCE OF AT LEAST 0.3MM BETWEEN THE PRODUCT AND DISPLAY.
 - (iv) WE RECOMMEND THE DESIGN OF A CASE OR BEZEL SHOULD COVERS THE BOUNDARY OF THE ACTIVE AREA INSIDE IN ORDER TO PREVENT AN OPERATION AT OUTSIDE OF THE ACTIVE AREA WHICH CAN NOT GUARANTEE THE FUNCTION OR DURABILITY (REFER TO ITEM 5.1.2. STRUCTURE, AREA DEFINITION).

BEZEL'S EDGE PART MAY GUIDE THE PEN SLIDING ON THE SAME POSITION REPEATEDLY. IF THE BEZEL IS PLACED OUTSIDE OF THE ACTIVE AREA, IT MAY CAUSE THE DAMAGE OF THE ITO FILM.

(v) PRESSING INSIDE OF BOUNDARY OF THE FRAME(PART (A) AS SHOWN IN BELOW) MAY CAUSES FAULT OPERATION, SO PLEASE DESIGN TO AVOID PRESSING OF TOUCH PANEL AT PART (A) SUCH AS HAVING GASKET/CUSHION AT PART (C). PARTICULARLY THE AREA (B) SHALL BE FREE FROM BURR. THE GASKET/CUSHION MATERIAL AT THE PART (C) SHOULD NOT BE EXCEEDED TO INSIDE OF THE BOUNDARY OF THE FRAME.



- (vi) TO PREVENT GIVING DISTORTION TO THE FILM OF THE PRODUCT AND PEELING OFF OF THE FILM FROM THE PRODUCT, DO NOT FIX THE FILM AND A SET CASE OR A SHOCK ABSORBING MATERIAL ADHERED TO A SET CASE BY ADHESION.
- (vii) WIPE OFF THE STAIN ON THE PRODUCT BY USING SOFT CLOTH MOISTENED WITH ETHANOL. TAKE CARE NOT TO ALLOW ETHANOL TO SOAK INTO THE JOINT OF UPPER FILM AND BOTTOM GLASS. IT MAY OTHERWISE CAUSE PEELING OR DEFECTIVE OPERATION. DO NOT USE ANY ORGANIC SOLVENT OR DETERGENT OTHER THAN ETHANOL.
- (viii) THE CORNERS OF THE PRODUCT ARE NOT CHAMFERED AND ARE SHARP. WHEN POSITIONING AND FIXING THE PRODUCT ON THE CASE, PROVIDE A ROUND PART ON THE CORNER OF THE CASE SO AS NOT TO APPLY LOAD ON THE CORNER OF THE TRANSPARENT TOUCH PANEL.
 - (ix) DO NOT PRESS THE FILM OF THE PRODUCT WHEN THIS PRODUCT IS BUILT INTO A SET.
- (3) CAUTIONS FOR OPERATION
 - (i) OPERATE IT WITH A POLYACETAL PEN (TIP R0.8 OR OVER) OR A BELLY OF A FINGER WITHOUT APPLYING EXCESSIVE LOAD. NEVER USE ANY MECHANICAL PENCILS, BALL POINT PENS AND HARD FINGERTIPS WHO'S TIP IS HARD FOR INPUT, OTHERWISE MALFUNCTIONS MAY RESULT.

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- (ii) THE INPUT POSITION MAY BE FLUCTUATED A LITTLE THROUGH LONG-TIME USE. IT IS DESIRABLE TO PROVIDE A ZERO-ADJUSTMENT FUNCTION BY USING A CIRCUIT AND SOFTWARE.
- (iii) OPERATION AT THE OUT OF ACTIVE AREA IS OUT OF OUR GUARANTEE. IT CAUSES A SERIOUS DAMAGE OF A TRANSPARENT ELECTRODE. DO NOT OPERATE AT THE OUT OF ACTIVE AREA.
- (iv) IN CASE OF CLEANING THE PART OF THE CASE BOUNDARY OF ACCOMPLISHED SET, USE A SOFT CLOTH WITH A FINGER BERRY OR A COTTON BUD. DO NOT CLEAN WITH A THI NG OTHER THAN THE FINGER SUCH AS HARD OR SHARP EDGES LIKE A FINGER NAIL ETC. ON THE CLOTH, BECAUSE IT CAUSE TRANSPARENT CONDUCTIVE FILM CRACKS. PLEASE ADVISE THIS PROHIBITION TO YOUR LAST CUSTOMERS.

12.3 DURABILITY

12.3.1 STYLUS HITTING:

ONE MILLION TIMES OR OVER NO DAMAGE ON FILM SURFACE PEN: R8 mm SILICON RUBBER

LOAD: 250g

FREQUENCY: 240 times/min MEASUREMENT POSITION:

1 POINT OF TOUCH PANEL ACTIVE AREA

REPEATED: OVER 1,000,000 TIMES

12.3.2 PEN TOUCH SLIDING DURABILITY:

100,000 TIMES OR OVER

WRITING WITH R0.8mm PLASTIC STYLUS PEN; WRITING FORCE 150g IN ACTIVE AREA.

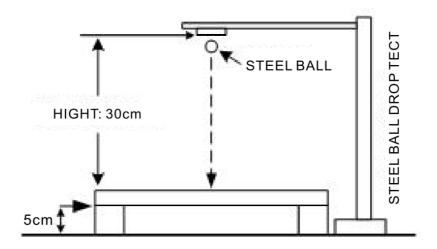
SPEED IS 60mm/sec.

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12.4 STEEL BALL DROP TEST

BY USING F9mm STEEL BALL FROM THE HEIGHT OF 30cm AND FALLING ON TOUCH PANEL SURFACE, MUST PASS BELOW CONDITIONS:

APPEARANCE: THE APPEARANCE WITHOUT ANY CHANGE, INCLUDING THE PANEL BROKEN.



12.5 APPEARANCE INSPECTION

PURPOSE:

TO ESTABLISH APPEARANCE STANDARD AND MAINTAIN PRODUCT QUALITY \circ

SCOPE:

TOUCH PANEL VIEW AREA WITHIN TOUCH PANEL •

12.5.1 RULE:

INSPECTION CONDITION

- (A) ENVIRONMENTAL LUMINANCE: 500 LUX •
- (B) DISTANCE BETWEEN HUMAN EYES AND PANEL: 30 CM (PANEL MUST BE TESTED UNDER LIGHT TRANSPARENT) •
- (C) VISUAL ANGEL : $> 60^{\circ}$
- (D) LIGHT SOURCE: FLUORESCENT LIGHT SOURCE •

12.5.2 JUDGE CRITERION:

JUDGEMENT UNDER ABOVE MENTIONED CRITERION (PANEL MUST BE TESTED UNDER LIGHT TRANSPARENT),

TESTING GOODS DEFECT CAN BE VISIBLE WITHIN 10 SECONDS, WHICH WILL BE JUDGED AS MAJOR DEFECTS •

SAMPLING STANDARD:

THE SAMPLING STANDARD WILL BE CONFIRMED BY BOTH OF EDT AND CUSTOMER.

INSPECTION ITEMS	SEPC.		JUDGE CRITERION	OPERATION GUIDELINE	
SCRATCH	W ≤ 0.05mm & L≤10mm		ACCEPTABLE	REFL	
SCRATCH	W > 0.051	mm or $L > 10$ mm	NOT ACCEPTABLE	BACK GROUND TESTING GOODS	
LINEAR FOREIGN	$W \leq 0.03$	5mm & L ≤5mm	ACCEPTABLE		
OBJECT	W > 0.05	5mm or L >5mm	NOT ACCEPTABLE	300mm = ≥	
	D:	≤ 0.25mm	ACCEPTABLE	60° ENVIRONMENTAL IUMINANCE : 500Lux	
GRANULAR FOREIGN OBJECT	0.25mm < D ≤0.30mm		MAX. 2 EA	REFL FLUORESCENT LIGHT SOURCE TESTING GOODS 300mm	
OBJECT	D >0.30mm		NOT ACCEPTABLE	60° ENVIRONMENTAL IUMINANCE : 500Lux	
PET BUBBLES	D ≤0.5mm D >0.5mm		ACCEPTABLE		
TET BOBBEES			NOT ACCEPTABLE	D	
CHIP ON GLASS	CORNER $X \le 3mm$ $Y \le 3mm$ $Z < t$ $t = /thickness$		ACCEPTABLE	Chip of glass	
	EDGE	$W \le 3mm \cdot Y \le 3mm \cdot Z < t$		X X X	

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13. INSPECTION CRITERION

13.1 APPLICATION

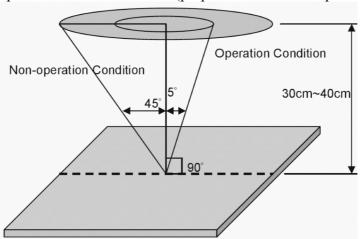
This inspection standard is to be applied to the LCD module delivered from EMERGING DISPLAY TECHNOLOGIES CORP.(E.D.T) to customers

13.2 INSPECTION CONDITIONS

13.2.1 (1)Observation Distance: 35cm±5cm

(2) View Angle:

Non-operation Condition : $\pm 5^{\circ}$ (perpendicular to LCD panel surface) Operation Condition : $\pm 45^{\circ}$ (perpendicular to LCD panel surface)



13.2.2 Environment Conditions:

Amb	ient Temperature	20°C~25°C
Ambient Humidity		65±20%RH
Ambient	Cosmetic Inspection	More than 600Lux
Illumination	Functional Inspection	300~500 Lux

13.2.3 Inspection lot

Quantity per delivery lot for each model

13.2.4 Inspection method

A sampling inspection shall be made according to the following provisions to judge The acceptability

(a) Applicable standard: MIL-STD-105E

Normal inspection, single sampling

Level Π

(b)AQL : Major defect : AQL 0.65 Minor defect : AQL 1.0

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13.3 INSPECTION STANDARDS

13.3.1 VISUAL DEFECTS CLASSIFICATION

TYPE OF DEFECT	INSPECTION ITEM	DEFECT FEATURE	AQL
	1.DISPLAY ON	DEFECT TO MISS SPECIFIED DISPLAY FUNCTION, FOR ALL AND SPECIFIED DOTS EX: DISCONNECTION, SHORT CIRCUIT ETC	0.65
MAJOR DEFECT	2.BACKLIGHT	NO LIGHT FLICKERING AND OTHER ABNORMAL ILLUMINATION	0.65
	3.DIMENSIONS	• SUBJECT TO INDIVIDUAL ACCEPTANCE SPECIFICATIONS	
	1.DISPLAY ZONE	 BLACK/WHITE SPOT BUBBLES ON POLARIZER NEWTON RING BLACK/WHITE LINE SCRATCH CONTAMINATION LEVER COLOR SPREED 	
MINOR DEFECT	2.BEZEL ZONE	STAINSSCRATCHESFOREIGN MATTER	1.0
	3.SOLDERING	 INSUFFICIENT SOLDER SOLDERED IN INCORRECT POSITION CONVEX SOLDERING SPOT SOLDER BALLS SOLDER SCRAPS 	
	4.DISPLAY ON (ALL ON)	• LIGHT LINE	

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13.3.2 MODULE DEFECTS CALSSIFICATION

NO.	ITEM		CRI	ΓERIA	
1.	DISPLAY ON INSPECTION	(1)INCORRECT PATTERN (2)MISSING SEGMENT (3)DIM SEGMENT (4)OPERATING VOLTAGE BEYOND SPEC			
2.	OVERALL DIMENSIONS	(1)OVERALL DIMENSION BEYOND SPEC			
3.	DOT DEFECT	I BRIGHT DOT DARK DOT TOAL BRIGHT NOTE: 1. THE DEFINITIO THE SIZE OF A REGARDED AS 2. BRIGHT DOT: DOTS APPEAR PANEL IS DISPL 3. DARK DOT: DOTS APPEAR	REENS. TEMS AND DARK DOTS ON OF DOT: DEFECTIVE DOT ONE DEFECTIVE BRIGHT AND UNCLAYING UNDER E	CHANGED IN SIZE IN	E DOT IS N WHICH LCD
4.	FOREIGN BLACK/WHITE/ BRIGHT LINE/ SCRATCH OF VIEWING AREA	LENGTH: L $L \le 0.3$ $0.3 < L \le 2.5$ $2.5 < L$ WIDTH: W mm, 1	WIDTH: W $W \le 0.05$ $0.05 < W \le 0.1$ $0.1 < W$	PERMISSIBLE NO. IGNORE 4 NONE	
5.	FOREIGN MATTER \ BLACK SPOTS \ WHITE SPOTS \ DENT (INCLUDING LIGHT LEAKAGE DUE TO POLARIZING PLATES PINHOLES, ETC.)	· ·		NUMBER OF PIECES IGNORE 4 NONE	

NO.	ITEM		CRITERIA		
			AVERAGE DIAMETER (mm): D	NUMBER OF PIECES PERMITTED	
		DUDDI E QUETIE	D ≤ 0.25	IGNORE	
		BUBBLE ON THE POLARIZER	$0.25 < D \le 0.5$	N ≤ 5	
		TOLARIZER	0.5 < D	NOTE	
		SURFACE STATUS	D < 0.1 mm	IGNORE	
		SORTACE STATES	$0.1 < D \le 0.3 mm$	N ≤ 3	
		CF FAIL / SPOT	D < 0.1 mm	IGNORE	
			$0.1 < D \le 0.3$ mm	N ≤ 3	
BUBBLES OF POLARIZER /DIRT/CF FAIL /SURFACE STAINS		NOTE: (1)POLARIZER BUBBLE IS DEFINED AS THE BUBBLE APPEARS ON ACTIVE DISPLAY AREA. THE DEFECT OF POLARIZER BUBBLE SHALL BE IGNORED IF THE POLARIZER BUBBLE APPEARS ON THE OUTSIDE OF ACTIVE DISPLAY AREA. (2)THE EXTRANEOUS SUBSTANCE IS DEFINED AS IT CAN BE OBSERVED WHEN THE MODULE IS POWER ON. (3)THE DEFINITION OF AVERAGE DIAMETER, D IS DEFINED AS FOLLOWING. AVERAGE DIAMETER (D)=(a+b)/2			
7.	LINE DEFECT ON DISPLAY	OBVIOUS VERTICAL OR HORIZONTAL LINE DEFECT IS NOT ALLOW			
8.	MURA ON DISPLAY	IT'S OK IF MURA IS	IT'S OK IF MURA IS SLIGHT VISIBLE THROUNG 6% ND FILTER		
9.	UNEVEN COLOR SPREAD, COLORATION	(1)TO BE DETERMINED BASED UPON THE STANDARD SAMPLE.			
10.	BEZEL APPEARANCE	(1)BEZEL MAY NOT HAVE RUST, BE DEFORMED OR HAVE FINGER PRINTS STAINS OF OTHER CONTAMINATION. (2)BEZEL MUST COMPLY WITH JOB SPECIFICATIONS.			
11	РСВ	THE SEAL AREA (THAN THREE PLA (2)NO OXIDATION O (3)PARTS ON PCB MI CHARACTERISTIC THERE SHOULD B PARTS. (4)THE JUMPER ON T CHARACTERISTIC (5)IF SOLDER GETS (R CONTAMINATION PCI UST BE THE SAME AS OF CCHART. E NO WRONG PARTS, M	E SHOULD BE NO MORE B TERMINALS. N THE PRODUCTION ISSING PARTS OR EXCESS ORM TO THE PRODUCT ED PAD, ZEBRA PAD OR	

NO. ITEM	CDITEDIA
12. SOLDERING	CRITERIA (1)NO SOLDERING FOUND ON THE SPECIFIED PLACE (2)INSUFFICENT SOLDER (a)LSI, IC A POOR WETTING OF SOLDER IS BETWEEN LOWER BEND OR "HEEL" OF LEAD AND PAD SOLDER FILLET SOLDER IS LESS THAN 50% OF SIDES AND FRONT FACE WETTING SOLDER FILLET 1/2 SOLDER FILLET SOLDER WETS 3 SIDES OF TERMINAL, BUT LESS THAN 25% OF SIDES AND FRONT SURFACE AREA ARE COVERED SOLDER S

NO.	ITEM	CRITERIA
		(b)CHIP COMPONENT COMPONENT IS OFF CENTER, AND MORE THAN 50% OF THE LEADS IS OFF THE PAD OUTLINE
12. SOLDE	RING	
		 (4)NO UNMELTED SOLDER PASTE MAY BE PRESENT ON THE PCB. (5)NO COLD SOLDER JOINTS, MISSING SOLDER CONNECTIONS, OXIDATION OR ICICLE. (6)NO RESIDUE OR SOLDER BALLS ON PCB. (7)NO SHORT CIRCUITS IN COMPONENTS ON PCB.
13. BACKL	IGHT	(1)NO LIGHT (2)FLICKERING AND OTHER ABNORMAL ILLUMINATION (3)SPOTS OR SCRATCHES THAT APPEAR WHEN LIT MUST BE JUDGED USING LCD SPOT, LINES AND CONTAMINATION STANDARDS. (4)BACKLIGHT DOESN'T LIGHT OR COLOR IS WRONG.
14. GENER APPEA	.AL RANCE	 (1)NO OXIDATION, CONTAMINATION, CURVES OR, BENDS ON INTERFACE PIN (OLB) OF TCP. (2)NO CRACKS ON INTERFACE PIN (OLB) OF TCP. (3)NO CONTAMINATION, SOLDER RESIDUE OR SOLDER BALLS ON PRODUCT. (4)THE IC ON THE TCP MAY NOT BE DAMAGED, CIRCUITS. (5)THE UPPERMOST EDGE OF THE PROTECTIVE STRIP ON THE INTERFACE PIN MUST BE PRESENT OR LOOK AS IF IT CAUSE THE INTERFACE PIN TO SEVER. (6)THE RESIDUAL ROSIN OR TIN OIL OF SOLDERING (COMPONENT OR CHIP COMPONENT) IS NOT BURNED INTO BROWN OR BLACK COLOR. (7)SEALANT ON TOP OF THE ITO CIRCUIT HAS NOT HARDENED. (8)PIN TYPE MUST MATCH TYPE IN SPECIFICATION SHEET. (9)LCD PIN LOOSE OR MISSING PINS. (10)PRODUCT PACKAGING MUST THE SAME AS SPECIFIED ON PACKAGING SPECIFICATION SHEET. (11)PRODUCT DIMENSION AND STRUCTURE MUST CONFORM TO PRODUCT SPECIFICATION SHEET. (12)THE APPEARANCE OF HEAT SEAL SHOULD NOT ADMIT ANY DIRT AND BREAK.

NO.	ITEM		CRITERIA		
		THE LCD WITH EXTENSIVE OF GENERAL GLASS CHIP:	$ \begin{array}{c c} a \\ & \leq t/2 \\ \hline & t/2 > , \leq 2t \\ \hline *W = DISTANC \\ \end{array} $	b < VIEWING AREA ≤ W/2 E BETWEEN AREA AND LO OGE E LENGTH	c ≤ 1/8X ≤ 1/8X
15. (CRACKED GLASS	CHIP ON ELECTRODE PAD	$\begin{array}{c c} & a \\ & \leq t/2 \\ & > t/2 & , \leq 2t \\ \hline *W=DISTANC! \\ & SEALANT \\ & PANEL EI \\ & X = LCD SID \\ & t = GLASS TI \\ \hline & a \\ & \leq t \\ \hline & * X=LCD SIDE \\ & t = GLASS TI \\ \hline \end{array}$	CAREA AND LODGE E LENGTH HICKNESS b <pre></pre>	c ≤1/8X ≤1/8X CD
		c a	©IF GLASS CH TERMINAL, REMAIN AN ACCORDIN TERMINAL ©IF THE PROI SEALED BY	HICKNESS DE PAD LENGT HIPPING THE IT , OVER 2/3 OF T ND BE, INSPECT G TO ELECTRO SPECIFICATIO DUCT WILL BE THE CUSTOM MENT MARK M	TO THE ITO MU TED DDE DNS THEAT ER,

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13.4 RELIABILITY TEST

13.4.1 STANDARD SPECIFICATIONS FOR RELIABILITY OF LCD MODULE

NO	ITEM	DESCRIPTION
1	High temperature operation	The sample should be allowed to stand at +70°C for 240 hrs
2	Low temperature operation	The sample should be allowed to stand at -20°C for 240 hrs
3	High temperature storage	The sample should be allowed to stand at +80°c for 240 hrs
4	Low temperature storage	The sample should be allowed to stand at -30°C for 240 hrs
5	High temp / humidity test storage	The sample should be allowed to stand at 60°C, 90% RH 240 hrs
6	Thermal shock (not operated)	The sample should be allowed to stand the following 10 cycles of operation: -40°c for 30 minutes ~ +80°c for 30 minutes
7	ESD (Electrostatic Discharge) (not operated)	AIR DISCHARGE ± 12KV CONTACT DISCHARGE ± 8KV

NOTE (1): THE TEST SAMPLES HAVE RECOVERY TIME FOR 2 HOURS AT ROOM TEMPERATURE BEFORE THE FUNCTION CHECK. IN THE STANDARD CONDITIONS, THERE IS NO DISPLAY FUNCTION NG ISSUE OCCURRED.

E M E R G I N G D I S P L A Y TECHNOLOGIES CORPORATION

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13.5 TESTING CONDITIONS AND INSPECTION CRITERIA

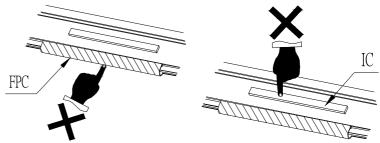
For the final test the testing sample must be stored at room temperature for 24 hours, after the tests listed in table 13.5, standard specifications for reliability have been executed in order to ensure stability.

NO	ITEM	TEST MODEL	INSPECTION CRITERIA
1	Current	Ratar La Shacitication	The current consumption should
	consumption		conform to the product specification.
			After the tests have been executed,
2	Contrast	_	the contrast must be larger than half
			of its initial value prior to the tests.
3	Appearance	Visual inspection	Defect free

13.6 OPERATION

- 13.6.1 Do not connect or disconnect modules to or from the main system while power is being supplied .
- 13.6.2 Use the module within specified temperature; lower temperature causes the retardation of blinking speed of the display; higher temperature makes overall display discolor. When the temperature returns to normality, the display will operate normally.
- 13.6.3 Adjust the LC driving voltage to obtain the optimum contrast .
- 13.6.4 Power On Sequence input signals should not be supplied to LCD module before power supply voltage is applied and reaches the specified value . If above sequence is not followed, CMOS LSIs of LCD modules may be damaged due to latch up problem .
- 13.6.5 Not allowed to inflict any external stress and to cause any mechanical interference on the bending area of FPC during the tail bending backwards!

 Do NOT stress FPC and IC on the Module!



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13.7 NOTICE

- 13.7.1 Use a grounded soldering iron when soldering connector I/O terminals. For soldering or repairing, take precaution against the temperature of the soldering iron and the soldering time to prevent peeling off the through-hole-pad.
- 13.7.2 Do not disassemble . EDT shall not be held responsible if the module is disassembled and upon the reassembly the module failed .
- 13.7.3 Do not charge static electricity, as the circuit of this module contains CMOS LSIs. A workman's body should always be static-protected by use of an ESD STRAP. Working clothes for such personnel should be of static-protected material.
- 13.7.4 Always ground the electrically-powered driver before using it to install the LCD module. While cleaning the work station by vacuum cleaner, do not bring the sucking mouth near the module; static electricity of the electrically-powered driver or the vacuum cleaner may destroy the module.
- 13.7.5 Don't give external shock.
- 13.7.6 Don't apply excessive force on the surface.
- 13.7.7 Liquid in LCD is hazardous substance. Must not lick and swallow. When the liquid is attach to your, skin, cloth etc. Wash it out thoroughly and immediately.
- 13.7.8 Don't operate it above the absolute maximum rating.
- 13.7.9 Storage in a clean environment, free from dust, active gas, and solvent.
- 13.7.10 Store without any physical load.
- 13.7.11 Rewiring: no more than 3 times.