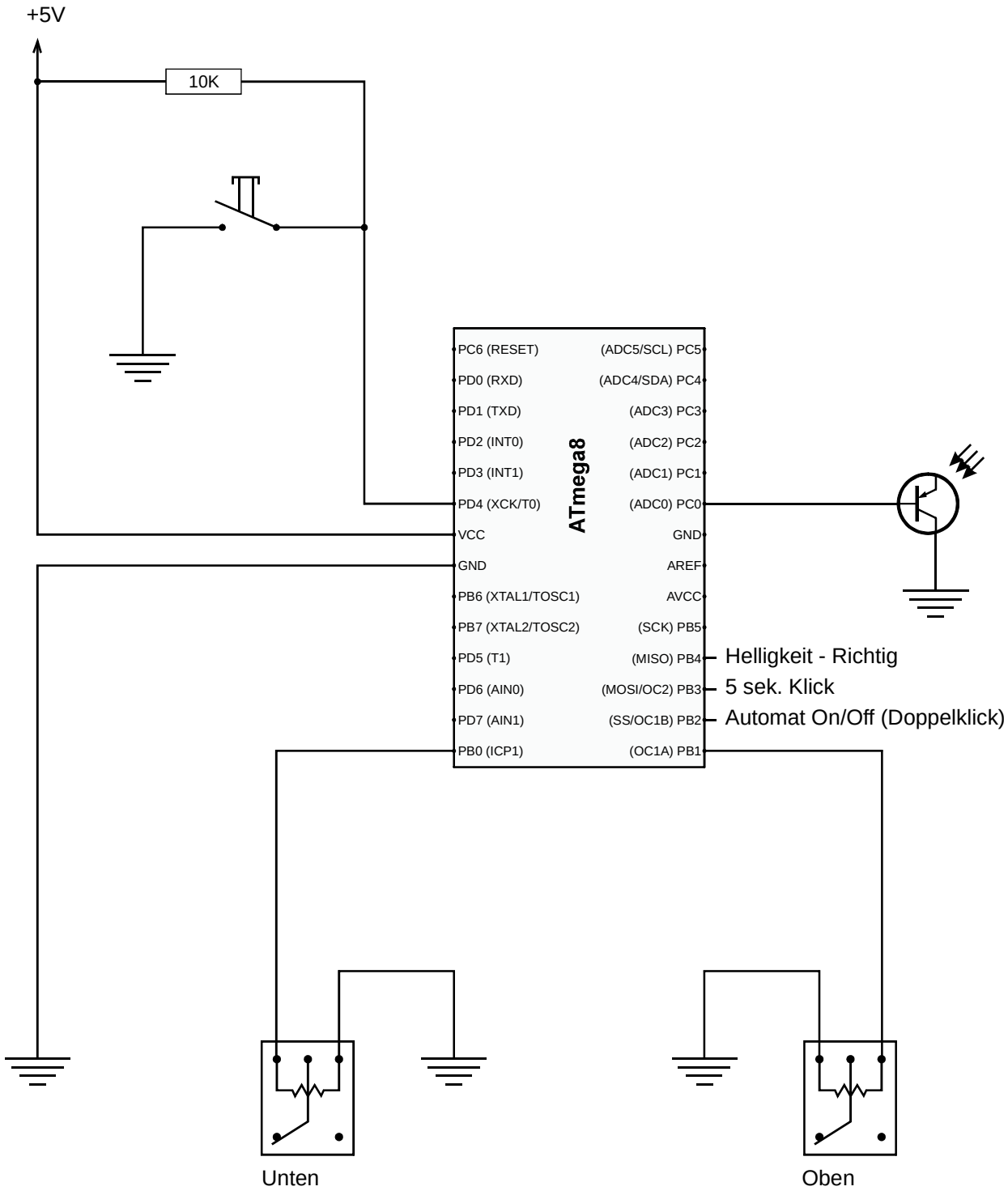
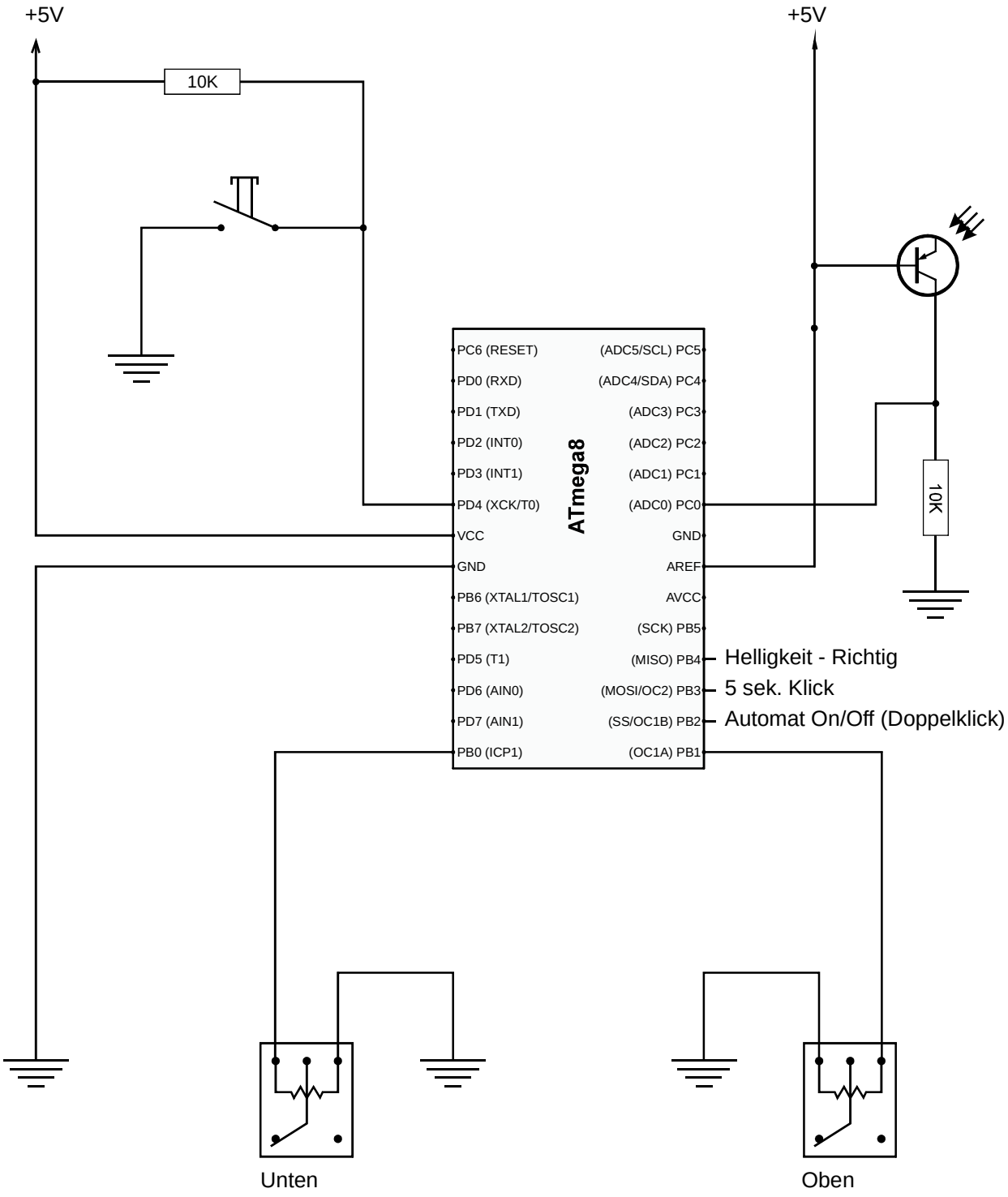
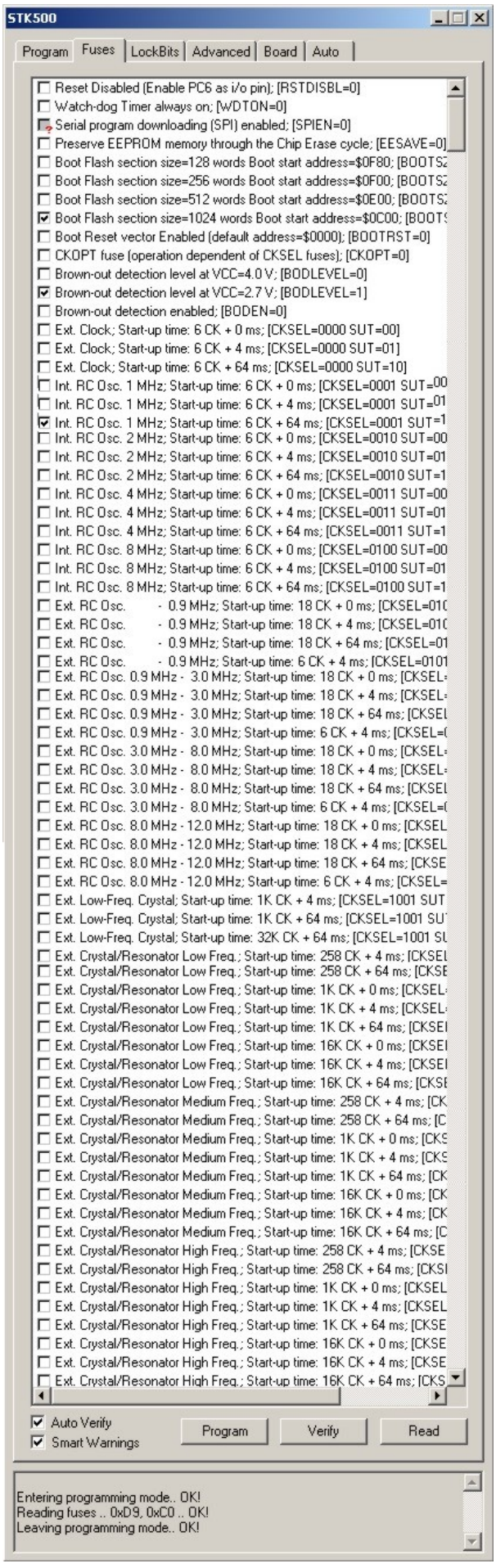
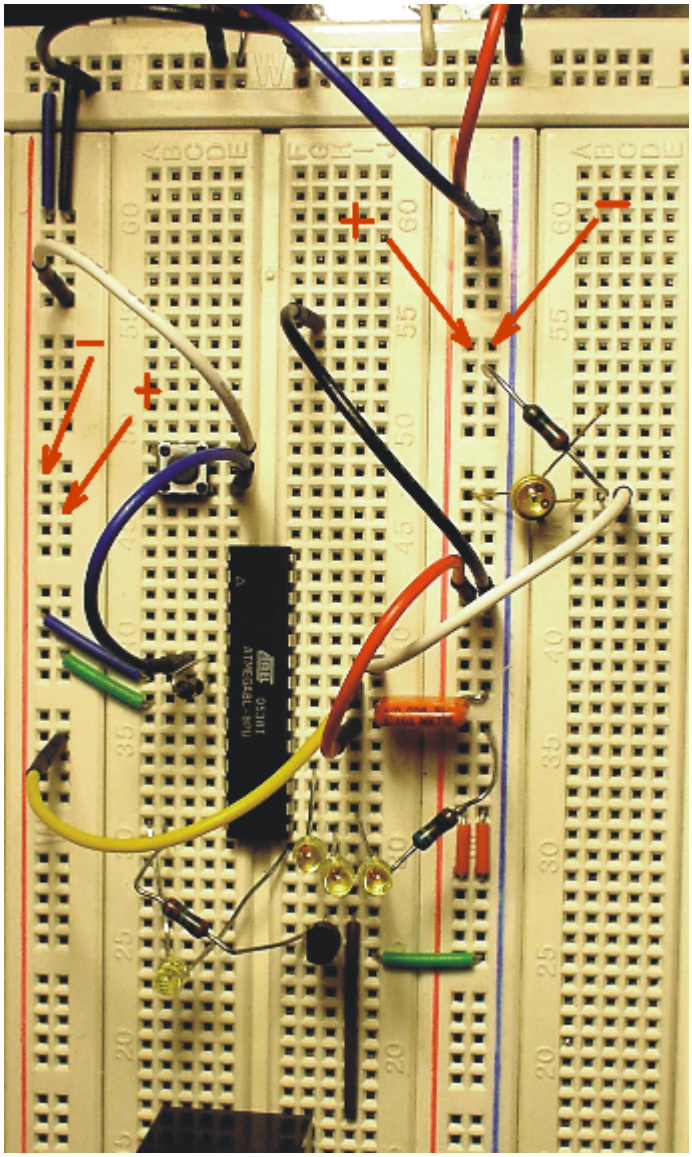


Ort:	Kempten	Revision: 2	19.04.06
Datum:	05.04.2006		
Autor:	Rafael Stolarczyk		
Titel:			



Ort:	Kempten	Revision: 2	19.04.06
Datum:	05.04.2006		
Autor:	Rafael Stolarczyk		
Titel:			


STK500
Program
Fuses
LockBits
Advanced
Board
Auto
Reset Disabled (Enable PC6 as i/o pin); [RSTDISBL=0]
Watch-dog Timer always on; [WDTON=0]
Serial program downloading (SPI) enabled; [SPIEN=0]
Preserve EEPROM memory through the Chip Erase cycle; [EESAVE=0]
Boot Flash section size=128 words Boot start address=$0F80; [BOOTS
Boot Flash section size=256 words Boot start address=$0F00; [BOOTS
Boot Flash section size=512 words Boot start address=$0E00; [BOOTS
Boot Flash section size=1024 words Boot start address=$0C00; [BOOT
Boot Reset vector Enabled (default address=$0000); [BOOTRST=0]
CKOPT fuse (operation dependent of CKSEL fuses); [CKOPT=0]
Brown-out detection level at VCC=4.0 V; [BODLEVEL=0]
Brown-out detection level at VCC=2.7 V; [BODLEVEL=1]
Brown-out detection enabled; [BODEN=0]
Ext. Clock; Start-up time: 6 CK + 0 ms; [CKSEL=0000 SUT=00]
Ext. Clock; Start-up time: 6 CK + 4 ms; [CKSEL=0000 SUT=01]
Ext. Clock; Start-up time: 6 CK + 64 ms; [CKSEL=0000 SUT=10]
Int. RC Osc. 1 MHz; Start-up time: 6 CK + 0 ms; [CKSEL=0001 SUT=00
Int. RC Osc. 1 MHz; Start-up time: 6 CK + 4 ms; [CKSEL=0001 SUT=01
Int. RC Osc. 1 MHz; Start-up time: 6 CK + 64 ms; [CKSEL=0001 SUT=1
Int. RC Osc. 2 MHz; Start-up time: 6 CK + 0 ms; [CKSEL=0010 SUT=00
Int. RC Osc. 2 MHz; Start-up time: 6 CK + 4 ms; [CKSEL=0010 SUT=01
Int. RC Osc. 2 MHz; Start-up time: 6 CK + 64 ms; [CKSEL=0010 SUT=1
Int. RC Osc. 4 MHz; Start-up time: 6 CK + 0 ms; [CKSEL=0011 SUT=00
Int. RC Osc. 4 MHz; Start-up time: 6 CK + 4 ms; [CKSEL=0011 SUT=01
Int. RC Osc. 4 MHz; Start-up time: 6 CK + 64 ms; [CKSEL=0011 SUT=1
Int. RC Osc. 8 MHz; Start-up time: 6 CK + 0 ms; [CKSEL=0100 SUT=00
Int. RC Osc. 8 MHz; Start-up time: 6 CK + 4 ms; [CKSEL=0100 SUT=01
Int. RC Osc. 8 MHz; Start-up time: 6 CK + 64 ms; [CKSEL=0100 SUT=1
Ext. RC Osc. - 0.9 MHz; Start-up time: 18 CK + 0 ms; [CKSEL=010
Ext. RC Osc. - 0.9 MHz; Start-up time: 18 CK + 4 ms; [CKSEL=010
Ext. RC Osc. - 0.9 MHz; Start-up time: 18 CK + 64 ms; [CKSEL=01
Ext. RC Osc. - 0.9 MHz; Start-up time: 6 CK + 4 ms; [CKSEL=0101
Ext. RC Osc. 0.9 MHz - 3.0 MHz; Start-up time: 18 CK + 0 ms; [CKSEL=
Ext. RC Osc. 0.9 MHz - 3.0 MHz; Start-up time: 18 CK + 4 ms; [CKSEL=
Ext. RC Osc. 0.9 MHz - 3.0 MHz; Start-up time: 18 CK + 64 ms; [CKSEL
Ext. RC Osc. 0.9 MHz - 3.0 MHz; Start-up time: 6 CK + 4 ms; [CKSEL=(
Ext. RC Osc. 3.0 MHz - 8.0 MHz; Start-up time: 18 CK + 0 ms; [CKSEL=
Ext. RC Osc. 3.0 MHz - 8.0 MHz; Start-up time: 18 CK + 4 ms; [CKSEL=
Ext. RC Osc. 3.0 MHz - 8.0 MHz; Start-up time: 18 CK + 64 ms; [CKSEL
Ext. RC Osc. 3.0 MHz - 8.0 MHz; Start-up time: 6 CK + 4 ms; [CKSEL=(
Ext. RC Osc. 8.0 MHz - 12.0 MHz; Start-up time: 18 CK + 0 ms; [CKSEL
Ext. RC Osc. 8.0 MHz - 12.0 MHz; Start-up time: 18 CK + 4 ms; [CKSEL
Ext. RC Osc. 8.0 MHz - 12.0 MHz; Start-up time: 18 CK + 64 ms; [CKSE
Ext. RC Osc. 8.0 MHz - 12.0 MHz; Start-up time: 6 CK + 4 ms; [CKSEL=
Ext. Low-Freq. Crystal; Start-up time: 1K CK + 4 ms; [CKSEL=1001 SUT
Ext. Low-Freq. Crystal; Start-up time: 1K CK + 64 ms; [CKSEL=1001 SU
Ext. Low-Freq. Crystal; Start-up time: 32K CK + 64 ms; [CKSEL=1001 SU
Ext. Crystal/Resonator Low Freq.; Start-up time: 258 CK + 4 ms; [CKSEL
Ext. Crystal/Resonator Low Freq.; Start-up time: 258 CK + 64 ms; [CKSE
Ext. Crystal/Resonator Low Freq.; Start-up time: 1K CK + 0 ms; [CKSEL=
Ext. Crystal/Resonator Low Freq.; Start-up time: 1K CK + 4 ms; [CKSEL=
Ext. Crystal/Resonator Low Freq.; Start-up time: 1K CK + 64 ms; [CKSEL
Ext. Crystal/Resonator Low Freq.; Start-up time: 16K CK + 0 ms; [CKSEL
Ext. Crystal/Resonator Low Freq.; Start-up time: 16K CK + 4 ms; [CKSEL
Ext. Crystal/Resonator Low Freq.; Start-up time: 16K CK + 64 ms; [CKSE
Ext. Crystal/Resonator Medium Freq.; Start-up time: 258 CK + 4 ms; [CK
Ext. Crystal/Resonator Medium Freq.; Start-up time: 258 CK + 64 ms; [C
Ext. Crystal/Resonator Medium Freq.; Start-up time: 1K CK + 0 ms; [CKS
Ext. Crystal/Resonator Medium Freq.; Start-up time: 1K CK + 4 ms; [CKS
Ext. Crystal/Resonator Medium Freq.; Start-up time: 1K CK + 64 ms; [CK
Ext. Crystal/Resonator Medium Freq.; Start-up time: 16K CK + 0 ms; [CK
Ext. Crystal/Resonator Medium Freq.; Start-up time: 16K CK + 4 ms; [CK
Ext. Crystal/Resonator Medium Freq.; Start-up time: 16K CK + 64 ms; [C
Ext. Crystal/Resonator High Freq.; Start-up time: 258 CK + 4 ms; [CKSE
Ext. Crystal/Resonator High Freq.; Start-up time: 258 CK + 64 ms; [CKSI
Ext. Crystal/Resonator High Freq.; Start-up time: 1K CK + 0 ms; [CKSEL
Ext. Crystal/Resonator High Freq.; Start-up time: 1K CK + 4 ms; [CKSEL
Ext. Crystal/Resonator High Freq.; Start-up time: 1K CK + 64 ms; [CKSE
Ext. Crystal/Resonator High Freq.; Start-up time: 16K CK + 0 ms; [CKSE
Ext. Crystal/Resonator High Freq.; Start-up time: 16K CK + 4 ms; [CKSE
Ext. Crystal/Resonator High Freq.; Start-up time: 16K CK + 64 ms; [CKS
Auto Verify
Smart Warnings
Program
Verify
Read
Entering programming mode.. OK!
Reading fuses .. 0xD9, 0xC0 .. OK!
Leaving programming mode.. OK!



```
'#########################################################################
'
' Title:       DROS
' Autor:       Rafael Stolarczyk
' Date:        16.04.2006
' Description: ...
'
'#########################################################################

$regfile = "m8def.dat"

Dim ...

' ONLOAD ##################################################

Config Portb = Output
Portb = &B00000000                                          ' alle LEDs aus
Config Portd = Input
Portd = 0

Config Adc = Single , Prescaler = Auto , Reference = Internal
Start Adc

Config TIMER0 = Timer , Prescale = 64
On Timer0 SekundenTakt
Enable Timer0
Enable Interrupts


' RUN #####################################################
Do
' Helligkeit auslesen.
   Start Adc
   intHellNow = Getadc(0)
   Stop Adc

...

Loop
```