Table 27. GPIO Base Address = 0xFFFF0400

Address	Name	Byte	Access Type	Cycle
0xFFFFF400	GP0CON	4	R/W	1
0xFFFFF404	GP1CON	4	R/W	1
0xFFFFF408	GP2CON	4	R/W	1
0xFFFFF40C	GP3CON	4	R/W	1
0xFFFFF410	GP4CON	4	R/W	1
0xFFFFF420	GP0DAT	4	R/W	1
0xFFFFF424	GP0SET	1	W	1
0xFFFFF428	GP0CLR	1	W	1
0xFFFFF42C	GP0PAR	4	R/W	1
0xFFFFF430	GP1DAT	4	R/W	1
0xFFFFF434	GP1SET	1	W	1
0xFFFFF438	GP1CLR	1	W	1
0xFFFFF43C	GP1PAR	4	R/W	1
0xFFFFF440	GP2DAT	4	R/W	1
0xFFFFF444	GP2SET	1	W	1
0xFFFFF448	GP2CLR	1	W	1
0xFFFFF44C	GP2PAR	4	R/W	1
0xFFFFF450	GP3DAT	4	R/W	1
0xFFFFF454	GP3SET	1	W	1
0xFFFFF458	GP3CLR	1	W	1
0xFFFFF45C	GP3PAR	4	R/W	1
0xFFFFF460	GP4DAT	4	R/W	1
0xFFFFF464	GP4SET	1	W	1
0xFFFFF468	GP4CLR	1	W	1
0xFFFFF46C	GP4PAR	4	R/W	1

Table 28. Flash/EE Block 0 Base Address = 0xFFFFF800

Address	Name	Byte	Access Type	Cycle
0xFFFFF800	FEE0STA	1	R	1
0xFFFFF804	FEE0MOD	1	R/W	1
0xFFFFF808	FEE0CON	1	R/W	1
0xFFFFF80C	FEE0DAT	2	R/W	1
0xFFFFF810	FEE0ADR	2	R/W	1
0xFFFFF818	FEE0SGN	3	R	1
0xFFFFF81C	FEE0PRO	4	R/W	1
0xFFFFF820	FEE0HID	4	R/W	1

Table 29. Flash/EE Block 1 Base Address = 0xFFFFF880

Address	Name	Byte	Access Type	Cycle
0xFFFFF880	FEE1STA	1	R	1
0xFFFFF884	FEE1MOD	1	R/W	1
0xFFFFF888	FEE1CON	1	R/W	1
0xFFFFF88C	FEE1DAT	2	R/W	1
0xFFFFF890	FEE1ADR	2	R/W	1
0xFFFFF898	FEE1SGN	3	R	1
0xFFFFF89C	FEE1PRO	4	R/W	1
0xFFFFF8A0	FEE1HID	4	R/W	1