





The C1 and C2 external capacitors should be low SR value ceramic type constructions (typ. suggested value 200 mOhm).  
 Reservoir capacitor C1 is nominally 4.7  $\mu$ F in capacitance, with the set/reset capacitor C2 nominally 0.22  $\mu$ F in capacitance.  
 The device core is supplied through the Vdd line. Power supply decoupling capacitors (C4=100 nF ceramic, C3=10  $\mu$ F Al)  
 should be placed as near as possible to the supply pin of the device (common design practice).  
 All the voltage and ground supplies must be present at the same time to have proper behavior of the IC.

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