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LIQUID CRYSTAL DISPLAY MODULE

Product Specification

CUSTOMER					
PRODUCT NUMBER		DVC40418			
CUSTOMER APPROVAL				Date	
	INT	ERNAL APPROV	ALS		
Quality Mgr	Product Mgr	Project Leader	Mech. Eng	Electr. Eng	
	Brus. Geordina				
Date:	Date: 11 Feb. 05	Date:	Date:	Date:	
 □ Approval for Specification only □ Approval for Specification and Sample 					
Sample no.:		Date:	ISIR no.:		

REV. A

REV. 2

DVC40418EW80G101WU

VGG100802-6TCLWB

Product No.



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REVISION RECORD

Date	Page	Chapt.	Comment	ECR no.
02/10/05			New DCA Release	E1838

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1 MAIN FEATURES

ITEM	CONTENTS	
Display Format	101*(RGB)*80 DOTS	
Colour	4096	
Overall Dimensions	36.7mm(W) x 62.15mm(H) x 4.1mm(D) Max	
Viewing Area	31.26mm(W) Min. x 24.4mm(H) Min.	
LCD type	CSTN-Negative	
Mode	Transmissive	
Viewing Angle	6 o'clock	
Duty ratio	1/80	
Driver IC	TL0482A	
Backlight type	LED	
Backlight colour	White	
DC/DC converter	Built-In	
Operating temperature	-20°C to +70°C	
Storage temperature	-30°C to +80°C	

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2 MECHANICAL SPECIFICATION

2.1 MECHANICAL CHARACTERISTICS

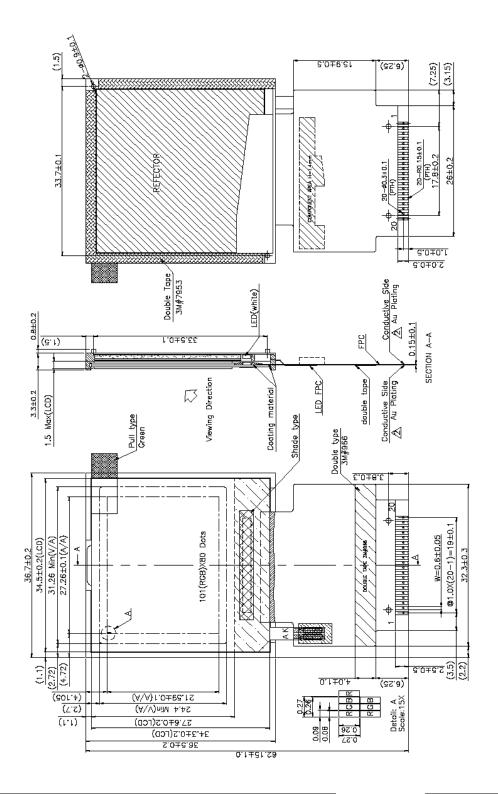
ITEM	CHARACTERISTIC	UNIT
Display Format	101*(RGB)*80 DOTS	
Overall Dimensions	36.7(W) x 62.15(H) x 4.1(D) Max	mm
Viewing Area	31.26(W) Min. x 24.4(H) Min.	mm
Active Area	27.26(W) x 21.59(H)	mm
Dot Size	0.26(W) x 0.26(H)	mm
Dot Pitch	0.27(W) x 0.27(H)	mm
IC Controller/Driver	TL0482A	

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2.2 MECHANICAL DRAWING



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3 ELECTRICAL SPECIFICATION

3.1 ABSOLUTE MAXIMUM RATINGS

VSS = 0 V, Ta = 25 °C

Item	Symbol	Min	Max	Unit	Note
Power Supply Voltage	V_{DD} - V_{SS}	-0.3	+4.0	V	
Power Supply for LCD	$V_{ m OUT}$	-0.3	+20.0	V	
Input Voltage	Vin	-0.3	V _{DD} +0.3	V	
Operating Temperature	Тор	-20	+70	°C	Note 1
Storage Temperature	Tst	-30	+80	°C	Note 2
Static Electricity	Be sure that you are grounded when handling displays.				

Note 1: Background colour changes slightly depending on ambient temperature. This phenomenon is reversible. Ta≤70 °C: 75% RH max

Note 2: Ta≤80 °C: 75% RH max.

3.2 ELECTRICAL CHARACTERISTICS

VSS = 0 V, Ta = 25 °C

					00 01, 1	a 25 C
Item	Symbol	Condition	Min	Тур	Max	Unit
Power Supply for Logic	V_{DD} - V_{SS}	Ta = 25 °C	-	2.6	-	V
Input Voltage	$V_{ m IL}$	L Level	0	ı	0.2* V _{DD}	V
	V_{IH}	H Level	0.8 *V _{DD}	-	V_{DD}	V
LCD Module Driving Voltage	V _{O-} V _{SS}	Ta = 25 °C	-	9.75	-	V
Current Consumption	* I _{DD}	V _{DD} =2.6V	-	1.0	2.0	mA

^{*} I_{DD} measurement condition is for all pattern ON

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3.3 INTERFACE PIN ASSIGNMENT

No.	Symbol	Function
1	LED+	Backlight Power Supply A.
2	LED-	Backlight Power Supply B.
3	VSS	Ground (0V)
4	VDD	Power Supply for Logic (2.6V)
5	VSS	Ground (0V)
6	NC	No Connection.
7	/CS	Chip select input pins.
8	/RESET	Reset input pins.
9	RS	Register select input pins.
10	/WR	Read / Write Execution Control Pin.
11	/RD	Read / Write Execution Control Pin.
12	D7	Data Bit 7
13	D6	Data Bit 6
14	D5	Data Bit 5
15	D4	Data Bit 4
16	D3	Data Bit 3
17	D2	Data Bit 2
18	D1	Data Bit 1
19	D0	Data Bit 0
20	VSS	Ground (0V)

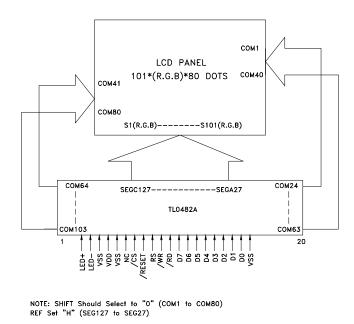
Note: Use internal oscillator. Bias set (B2, B1, B0) : (1,1,0) \rightarrow 1/11 Bias. DS SET (0110) , DSE SET "1" , Duty= 1/80

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3.4 BLOCK DIAGRAM



3.5 TIMING CHARACTERISTICS

Note: Please reference the manufacturers datasheet for the TL0482A controller.

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4 OPTICAL SPECIFICATION

4.1 OPTICAL CHARACTERISTICS

 $Ta = 25 \, ^{\circ}C$

	1					1	a = 25 °C
Item	Symbol	Condition	Min	Тур	Max	Unit	Note
	θ1	CR≥2	-	40	-	deg	1
Misseine Augla	θ2	CR≥2	-	10	-	deg	1
Viewing Angle	θ3	CR≥2	-	30	-	deg	2
	θ4	CR≥2	-	30	-	deg	2
Contrast Ratio	CR	Ta = 25 °C	-	15	-	-	3
D. T.	Tr	Ta = 25 °C	-	200	300	ms	4
Response Time	Tf	Ta = 25 °C	-	200	300		
Duissins Mades d	Duty			1/80			
Driving Method	Bias	Bias 1/11					
LCD Type	CSTN-Negative-Transflective						
Viewing Direction	6 O'CLOCK						

4.2 COLOR OF CIE COORDINATES

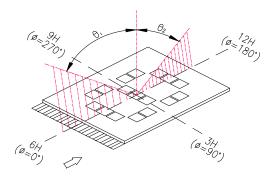
Item		Symbol	Condition	Value	Brightness
	Red	X		0.42	20 1/ 2
	Rea	Y	$\Theta = \Phi = 0_{\rm o}C$	0.31	20 cd/m^2
	C	X	LED Backlight	0.28	50 1/ 2
Chromaticity	Green	Y	Color Degree	0.41	50 cd/m^2
Coordinates	D1	X	X=0.29	0.18	15 1/ 2
	Blue	Y	Y=0.29	0.218	15 cd/m^2
	White	X	Brightness = 1500 cd/m^2	0.28	00 1/ 2
	white	Y		0.30	80 cd/m^2

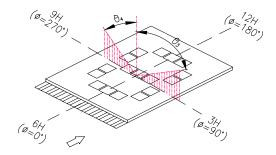
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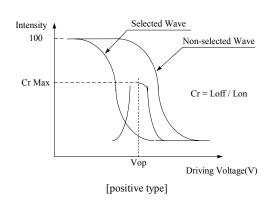


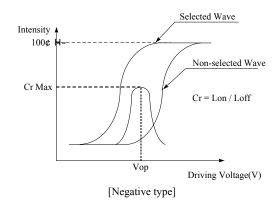
Note 1: definition of viewing angle $\theta 1 \& \theta 2$ Note 2: definition of viewing angle $\theta 3 \& \theta 4$



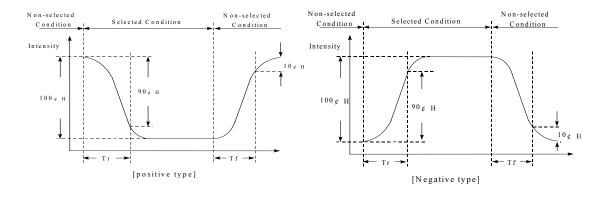


Note 3: definition of contrast ratio (CR)





Note 4: definition of response time



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5 BACKLIGHT SPECIFICATION

5.1 BACKLIGHT CHARACTERISTICS

5.1.1 Standard Lamp Styles (Edge Lighting Type):

The LED chips are distributed over the edge light area of the illumination unit, which gives the less power consumption. The main advantages of the LED Backlight are as follows: a resistor or potentiometer can simply adjust the brightness of the backlight.

PARAMETER	Symbol	Test Condition	Min	Тур	Max	Unit	Note
Supply Current	I	V=3.6V	-	20	-	mA	
Supply Voltage	V	-	-	3.6	-	V	
Reverse Voltage	V_R	-	-	-	4	V	
Luminous Intensity	I_{V}	V=3.6V	-	1500	-	cd/m²	1,2
Uniformity (For Module)	-	V=3.6V	65	-	ı	%	3
Life time		V≦4.0V	-	20000	-	hrs	
Color				White			

Note:

- 1. Backlight only.
- 2. Average Luminous Intensity of P1-P9.
- 3. Uniformity = Min/Max * 100%.

Measured Method:

10.4 Measured Method:

P1	P2	P3	
0	○	O	
P4	P5	P6	
○	○	○	
P7	P8	P9	
○	○	O	

(Effective spatial Distribution)

Hole Diameter: ±1Ø; 1 to 9 per Position Measured Luminous

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6 LABELLING & MARKING

DENSITRON DVC40418EW80G101WU TAIWAN YYMM

7 QUALITY ASSURANCE SPECIFICATION

7.1 CONFORMITY

The performance, function and reliability of the shipped products conform to the Product Specification.

7.2 DELIVERY ASSURANCE

7.2.1 Delivery inspection standards

- MIL-STD-105E, general inspection level II, single sampling level;
- IPC-AA610 rev. C, class 2 electronic assemblies standard

The quality assurance levels are shown below:

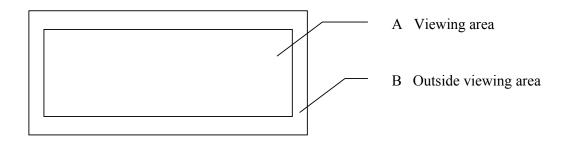
Rank	Item Inspected	Defect type	AQL	Remark
		Non display		Fit/Function defect
		Over current		
		Missing segment		
	Display	Wrong viewing direction		
Major defect		Incorrect operating	0.65%	
		Backlight OFF		
		Backlight flashing		
	Dimension	PCB and bezel out of		
	Difficusion	specification		
	LCD	Black and white spot		Appearance defect
		Black and white lines		
		Polariser scratch		
		Bubbles in polariser	2.5%	
Minor defect		Segment deformation, pin hole		
		Colour uniformity		
		Glass chip		
	PCB	Dust, solder ball on PCB		
	1 CD	Pad scratch		
		Total	2.5%	

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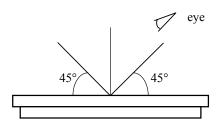


7.2.2 Zone definition



7.2.3 Visual inspection

- Inspect under 2x20W or 40W fluorescent lamp (approximately 3000 lux) leaving 25 to 30 cm between the module and the lamp and 30 cm between the module and the eye (measuring position).
- Appearance is inspected at the best contrast voltage (best contrast is adjusted considering clearness and crosstalk on screen).
- Inspect the module at 45° right and left, top and bottom.
- Use the optimum viewing angle during the contrast inspection.



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7.2.3.1 Standard of appearance inspection

Units: mm

Om	s: mm				
No	Item	Criteria			
1	Black spot,	Round type: as per following drawing			
	white spot, dust	$\emptyset = (X+Y)/2$			
		Acceptable quantity			
			Size	Zone A	Zone B
		*	Ø<0.1	Any number	
		Y	0.1<Ø<0.2	2	Any number
		→ √ ← ↑	0.2<Ø<0.25	1	I my number
		X	0.25<Ø	0	
		Line type: as per following	ng drawing		
		The state of the s		ole quantity	
		W Length	Width	Zone A	Zone B
			W≤0.02	Any number	
		L≤3.0	0.02 <w≤0.03< td=""><td>2</td><td>Any number</td></w≤0.03<>	2	Any number
		L≤2.5	0.03 <w≤0.05< td=""><td></td><td>7 tily liullioci</td></w≤0.05<>		7 tily liullioci
		L	0.05 <w< td=""><td>As round type</td><td></td></w<>	As round type	
2	Polariser scratch Polariser bubble	Scratch on protective film is permitted Scratch on polariser: same as No. 1 $\emptyset = (X+Y)/2$			
		Acceptable quantity			
			Size	Zone A	Zone B
		*	Ø<0.2	Any number	
		Y	0.2<Ø<0.5	2	Any number
		* * * *	0.5<Ø<1.0	1	Any number
		X	1.0<Ø	0	
			Total acceptable	quantity: 3	
4	Segment deformation	1.a. Pin hole on segment	ed display		
	deformation	W: segment width			
		W: segment width $\varnothing = (A+B)/2$ Acceptable quantity			
		Width Ø			
			W≤0.4	Ø≤0.2 and	
			W>0.4		
		$W>0.4$ $\varnothing \le 0.25$ and $\varnothing \le 1/3W$ Total acceptable quantity: 1 defect per segment			
		V W		y under 0.10 mm a	
		THE STATE OF THE S			

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No	Item	Criteria		
5	Segment deformation	1b. Pin hole on dot matrix display	Acceptable Size $a,b<0.1$ $(a+b)/2\le0.1$ $0.5<\varnothing<1.0$ Total acceptable	Any number Any number 3
		2. Segments / dots with different width	Accept a≥b a <b< td=""><td>table a/b≤4/3 a/b>4/3</td></b<>	table a/b≤4/3 a/b>4/3
		3. Alignment layer defect $\emptyset = (a+b)/2$	Acceptable Size $\emptyset \le 0.4$ $0.4 < \emptyset \le 1.0$ $1.0 < \emptyset \le 1.5$ $1.5 < \emptyset \le 2.0$ Total acceptable	Any number 5 3 2
6	Colour uniformity	Level of sample for approval set as limit sa	ample	
7	Backlight	The backlight colour should correspond to Flashing and or unlit backlight is not allow Dust larger than 0.25 mm is not allowed		cation

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7.3 DEALING WITH CUSTOMER COMPLAINTS

7.3.1 Non-conforming analysis

Purchaser should supply Densitron with detailed data of non-conforming sample. After accepting it, Densitron should complete the analysis in two weeks from receiving the sample.

If the analysis cannot be completed on time, Densitron must inform the purchaser.

7.3.2 Handling of non-conforming displays

If any non-conforming displays are found during customer acceptance inspection which Densitron is clearly responsible for, return them to Densitron.

Both Densitron and customer should analyse the reason and discuss the handling of non-conforming displays when the reason is not clear.

Equally, both sides should discuss and come to agreement for issues pertaining to modification of Densitron quality assurance standard.

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8 RELIABILITY SPECIFICATION

8.1 RELIABILITY TESTS

Test Item	Test Condition	Evaluation and assessment
Operation at High	40°C±2°C, 90%RH for 240	No abnormalities in
Temperature and Humidity	hours	function* and appearance**
Operation at High	70°C±2°C for 240 hours	No abnormalities in
Temperature	70 C12 C 101 240 Hours	function* and appearance**
	-20~+70C. Left for 30	
Heat Shock	minutes at each temperature,	No abnormalities in
Treat Shock	transition time 5 min,	function* and appearance**
	repeated 10 times.	
Low Temperature	-20°C+2°C for 240 hours	No abnormalities in
Low Temperature	-20 C±2 C 101 240 Hours	function* and appearance**
	Sweep for 1 minute at 10Hz,	
Vibration	55Hz, 10Hz, amplitude	No abnormalities in
Violation	1.5mm, 2 hours each in the	function* and appearance**
	X,Y and Z directions.	
Drop Shock	Drop Shock	No abnormalities in
Diop Shock	Diop Shock	function* and appearance**

8.2 LIFE TIME

Item	Description
1	Function, performance, appearance, etc. shall be free from remarkable deterioration within 50,000 hours under ordinary operating and storage conditions of room temperature (25±10 °C), normal humidity (45±20% RH), and in area not exposed to direct sunlight.

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9 PART NUMBER DESCRIPTIONS FOR AVAILABLE OPTIONS

DVC40418①②<u>80G101</u>④⑤ ③

- ① POLARISER OPTION
 - E = Transmissive negative mode.
- ② LED BACKLIGHT COLOUR
 W = White
- 3 ROW X COLUMN DOT FORMAT
- 4 TEMPERATURE RANGE AND POWER SUPPLY
 W = Wide temperature range; on-board negative voltage regulator.
- **5** FLUID TYPE AND COMPENSATION CIRCUIT U = CSTN.

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10 HANDLING PRECAUTIONS

Safety

If the LCD panel breaks, be careful not to get the liquid crystal fluid in your mouth or in your eyes. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and plenty of water

Mounting and Design

Place a transparent plate (e.g. acrylic, polycarbonate or glass) on the display surface to protect the display from external pressure. Leave a small gap between the transparent plate and the display surface.

When assembling with a zebra connector, clean the surface of the pads with alcohol and keep the surrounding air very clean.

Design the system so that no input signal is given unless the power supply voltage is applied.

Caution during LCD cleaning

Lightly wipe the display surface with a soft cloth soaked with Isopropyl alcohol, Ethyl alcohol or Trichlorotriflorothane.

Do not wipe the display surface with dry or hard materials that will damage the polariser surface. Do not use aromatic solvents (toluene and xylene), or ketonic solvents (ketone and acetone).

Caution against static charge

As the display uses C-MOS LSI drivers, connect any unused input terminal to VDD or VSS. Do not input any signals before power is turned on.

Also, ground your body, work/assembly table and assembly equipment to protect against static electricity.

Packaging

Displays use LCD elements, and must be treated as such. Avoid strong shock and drop from a height. To prevent displays from degradation, do not operate or store them exposed directly to sunshine or high temperature/humidity.

Caution during operation

It is indispensable to drive the display within the specified voltage limit since excessive voltage shortens its life.

Direct current causes an electrochemical reaction with remarkable deterioration of the display quality. Give careful consideration to prevent direct current during ON/OFF timing and during operation. Response time is extremely delayed at temperatures lower than the operating temperature range while, at high temperatures, displays become dark. However, this phenomenon is reversible and does not mean a malfunction or a display that has been permanently damaged.

If the display area is pushed on hard during operation, some graphics will be abnormally displayed but returns to a normal condition after turning off the display once.

Even a small amount of condensation on the contact pads (terminals) can cause an electro-chemical reaction which causes missing rows and columns. Give careful attention to avoid condensation.

Storage

Store the display in a dark place where the temperature is $25^{\circ}\text{C} \pm 10^{\circ}\text{C}$ and the humidity below 50%RH

Store the display in a clean environment, free from dust, organic solvents and corrosive gases. Do not crash, shake or jolt the display (including accessories).

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