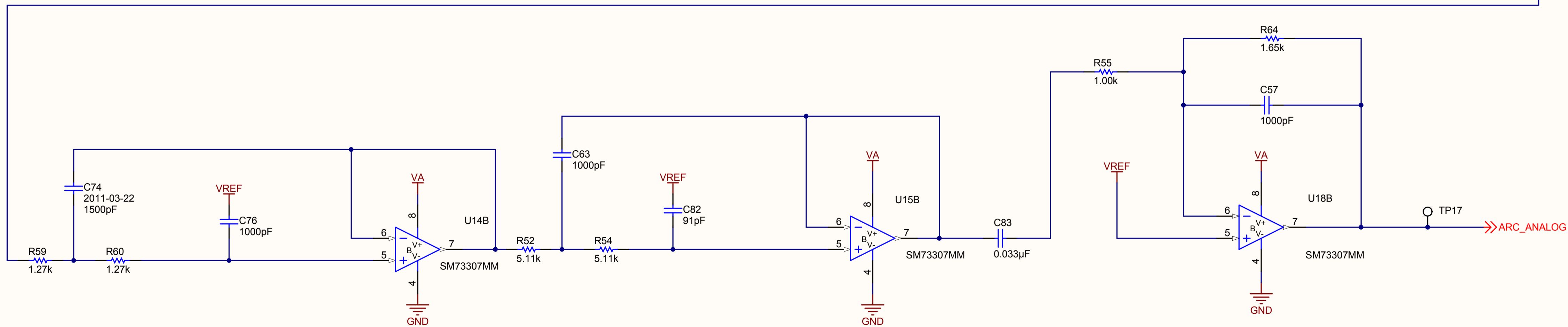


CH1 and CH2 are slots to increase the creepage distance between the primary and secondary transformer pins that may be needed for safety certification (the creepage requirement is voltage dependent)

12.5mm creepage on reference board design



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Designed for: [National Semiconductor](#) Mod. Date: 6/18/2012
 Project: [SM73201 - ARCTest](#)
 Sheet Title: [Arc Detect Analog Front End](#) Sheet: 4 of 7
 Size: B Schematic: [870600620](#) Rev: 005
 Assembly Variant: [\[No Variations\]](#)
 File: [SM73201-ARC QFN-56 Detect AFE.SchDoc](#)
 Contact: <http://www.national.com/support>



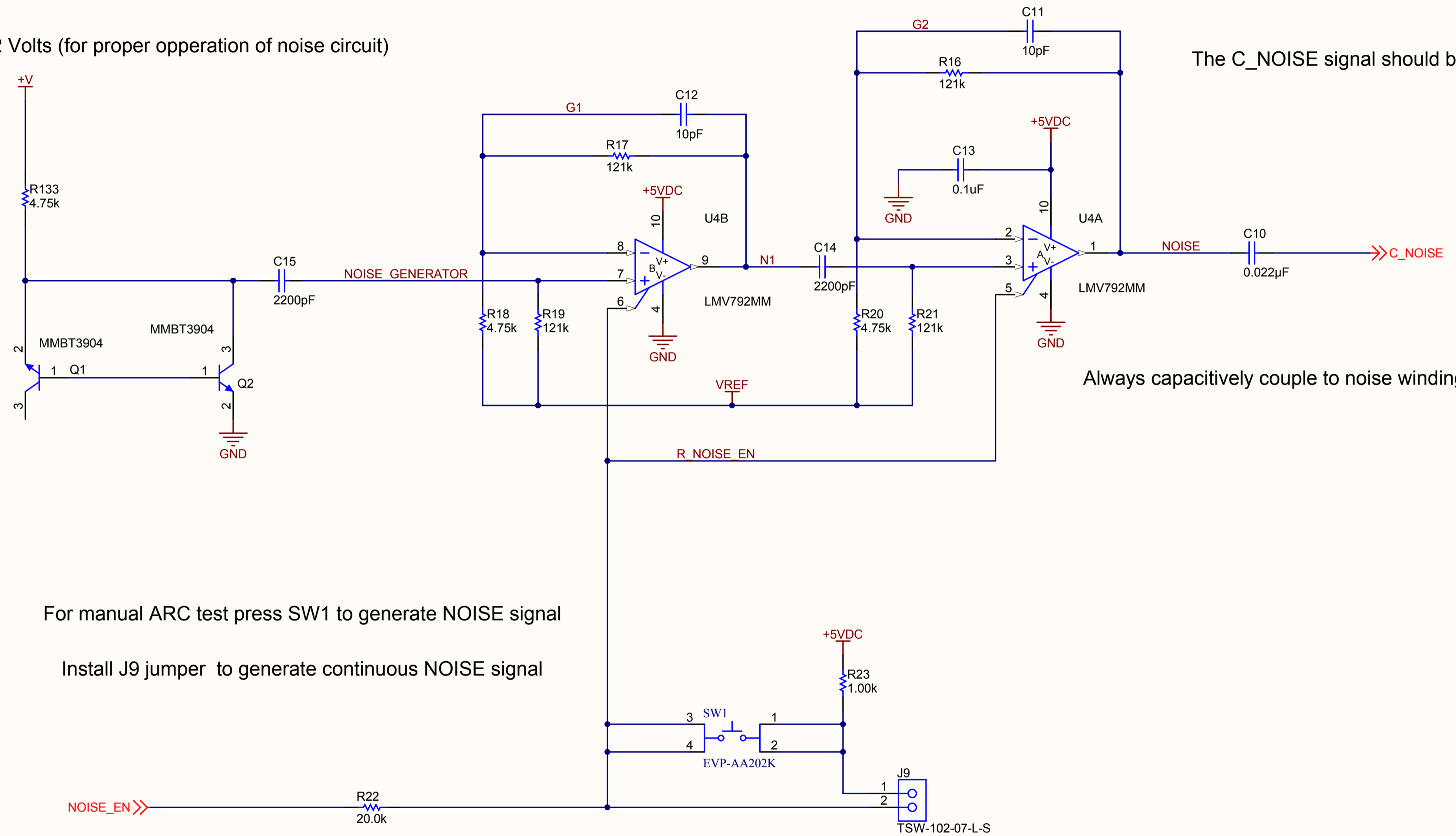
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When using the PA3655NL transformer the NOISE signal should be at least 300 mV p-p for reliable operation

+V should be between 8 and 12 Volts (for proper operation of noise circuit)

The C_NOISE signal should be about 100 mV p-p



Always capacitively couple to noise winding on transformer

For manual ARC test press SW1 to generate NOISE signal

Install J9 jumper to generate continuous NOISE signal

For CPU ARC test J9 must not be installed then drive NOISE_EN signal (GPIO6) high to generate NOISE signal

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Designed for: National Semiconductor | Mod. Date: 6/18/2012
 Project: SM73201 - ARCTest
 Sheet Title: Arc Detect Filter | Sheet: 7 of 7
 Size: B | Schematic: 870600620 | Rev: 005
 Assembly Variant: [No Variations]
 File: SM73201 - ARC QFN-56 Test.SchDoc
 Contact: <http://www.national.com/support>



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