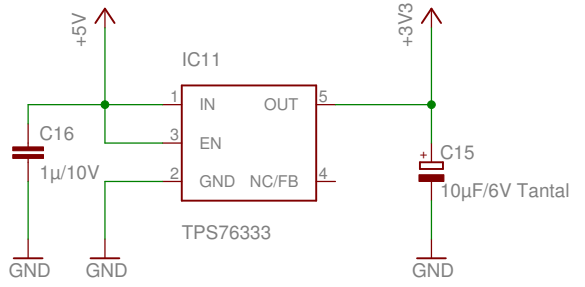


Value for R3 (1/fsw - 83ns)/77pF
 12k ~ 1MHz
 25k ~ 500kHz
 129k ~ 100kHz

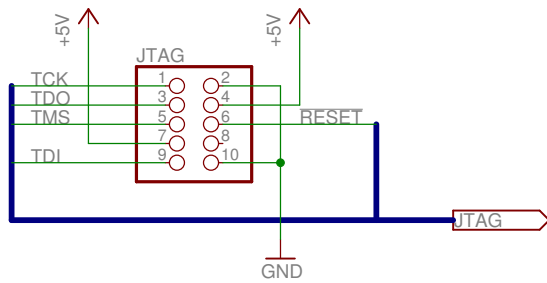


Alternative:
 IC11 = TLV70033DDCT/DDCR, C15 = C16 = 1µ/10V Ceramic

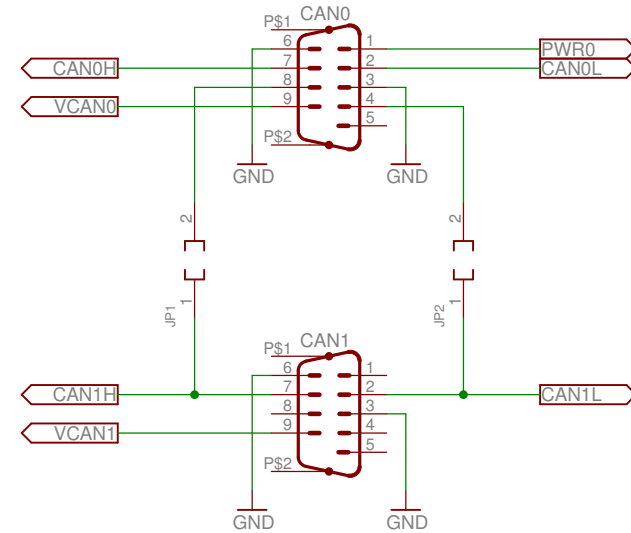
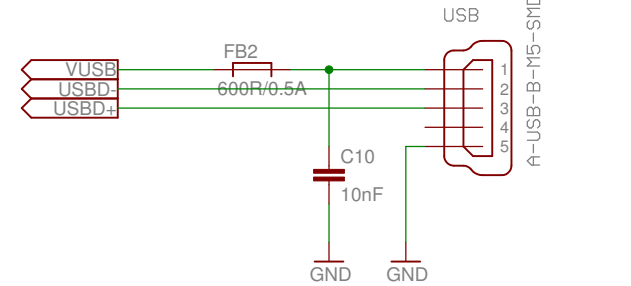
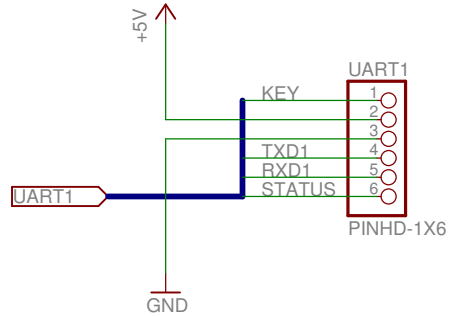
Layer mark

Power_Reg	
at90can-v1.1	
3/31/13 3:39 PM	
Sheet: 1/8	

* +5V to pin 7 new in V1.1



JTAG (Atmel Standard)

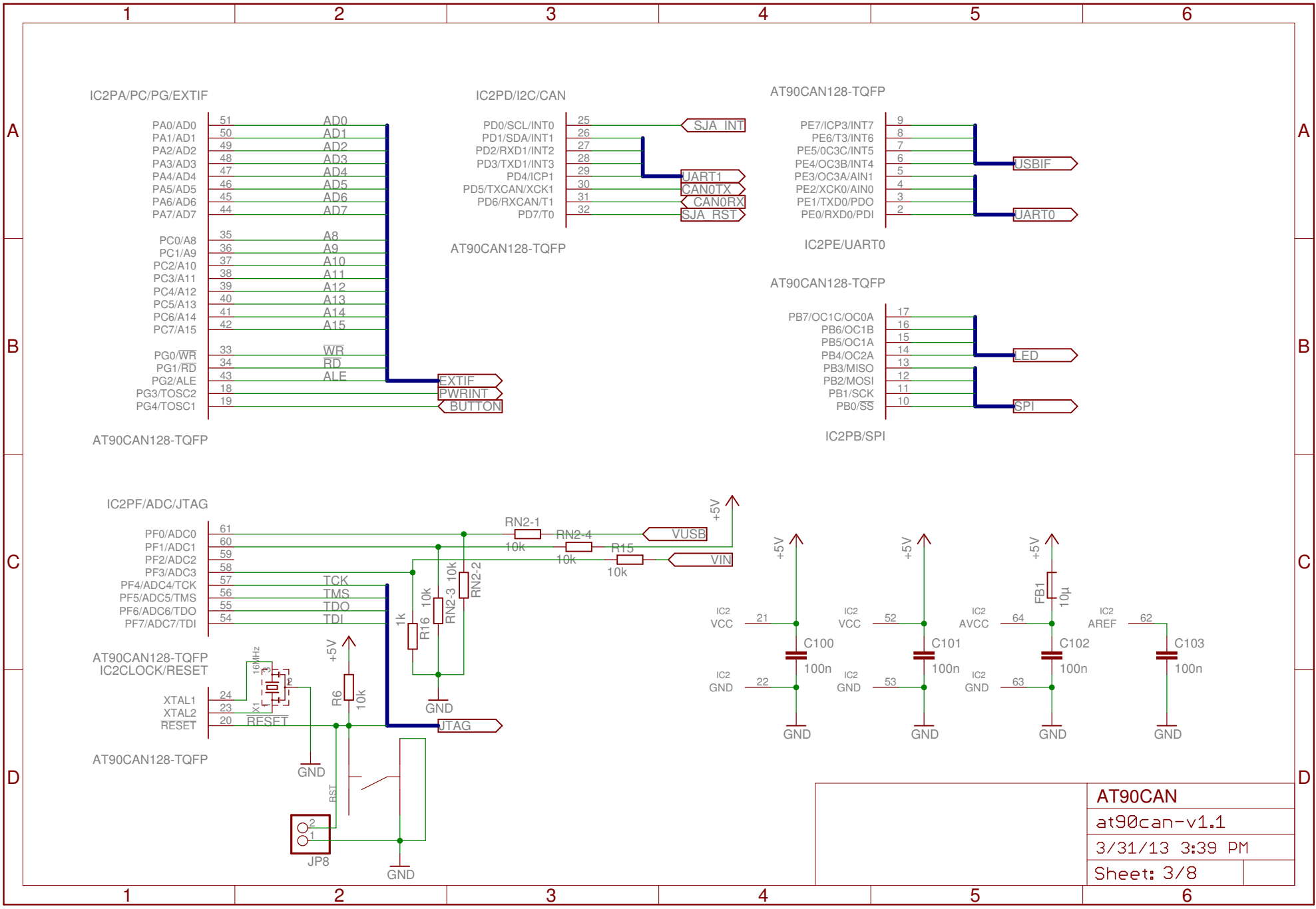


Connectors

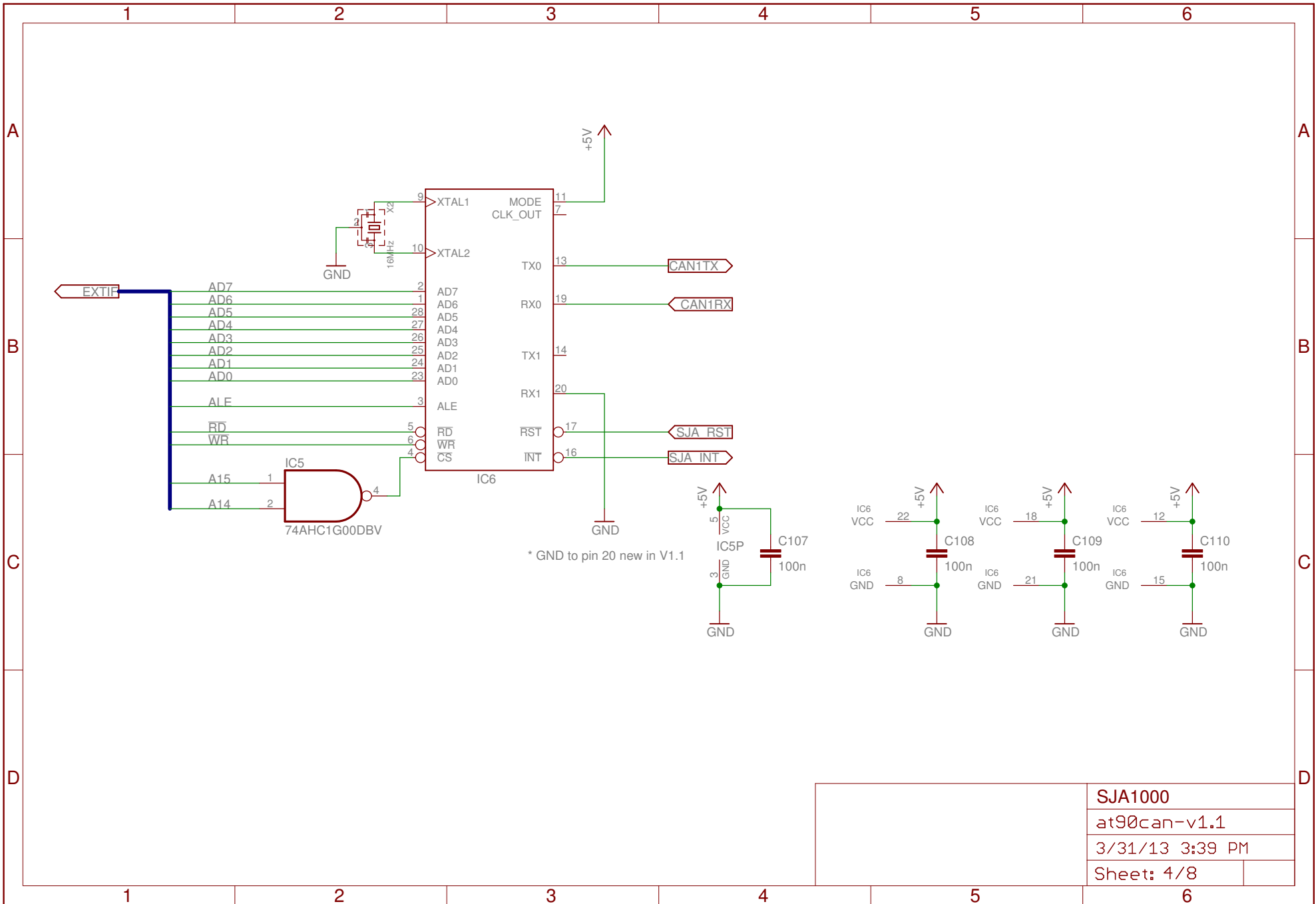
at90can-v1.1

3/31/13 3:39 PM

Sheet: 2/8

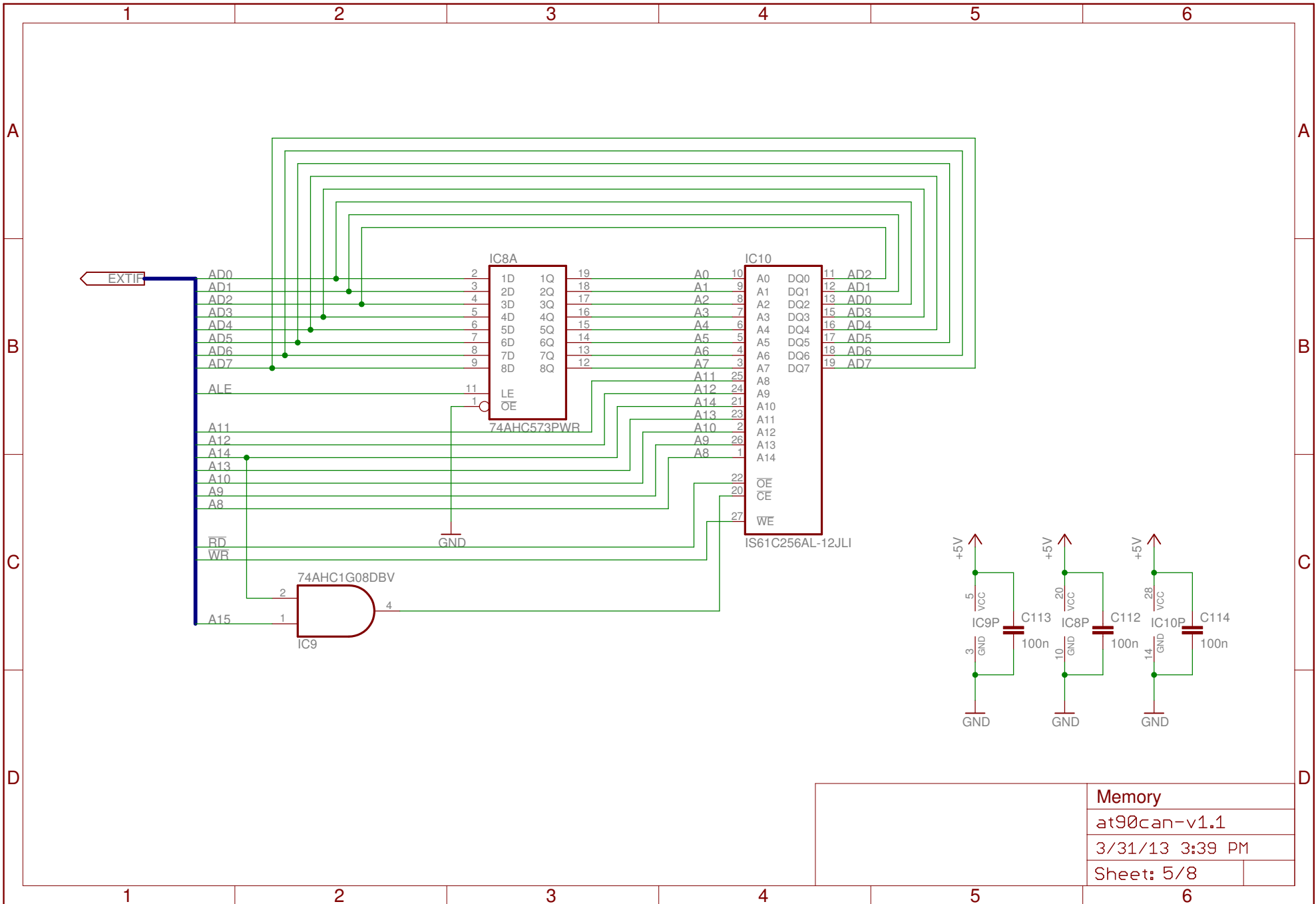


AT90CAN	
at90can-v1.1	
3/31/13 3:39 PM	
Sheet: 3/8	

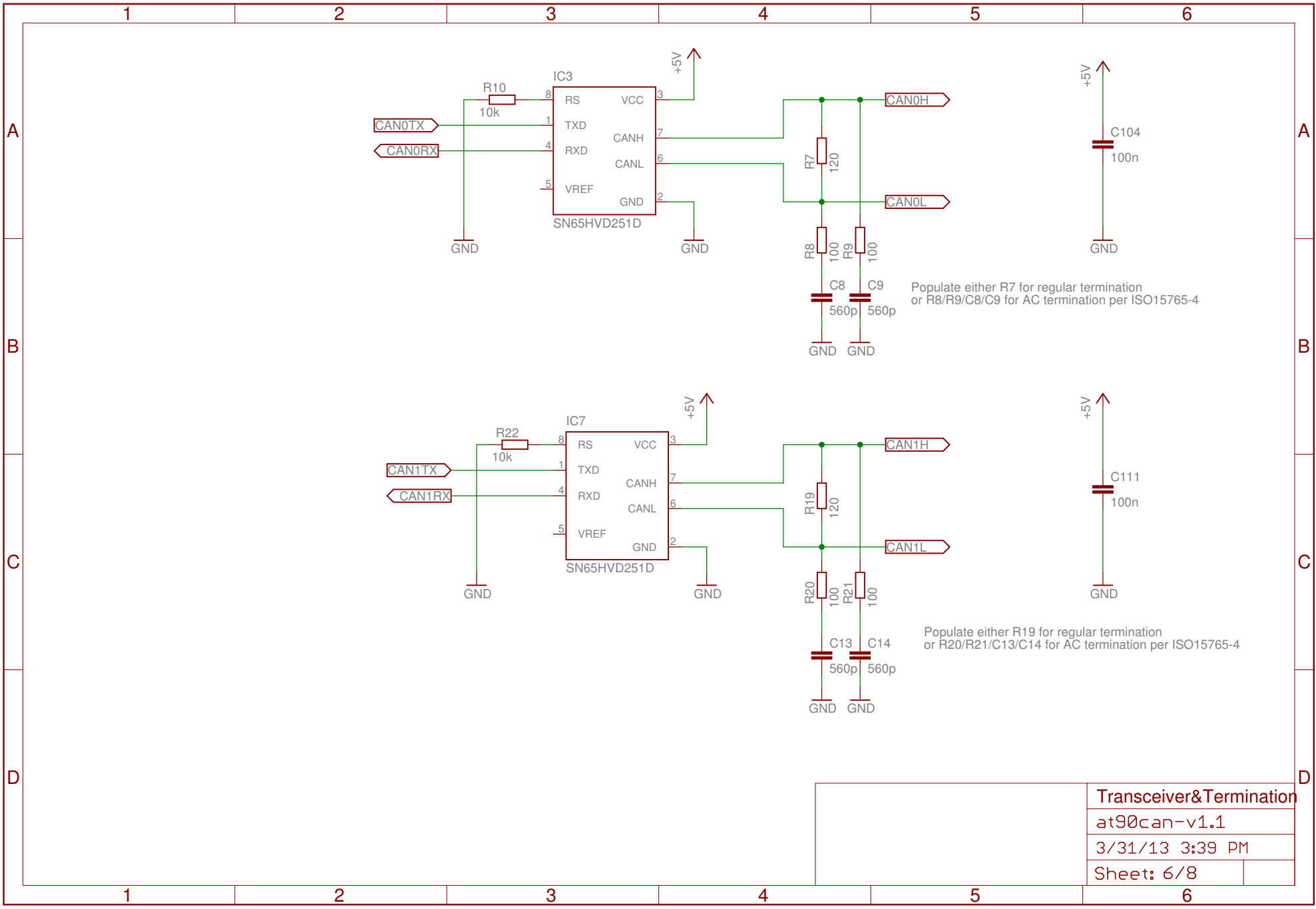


* GND to pin 20 new in V1.1

SJA1000	
at90can-v1.1	
3/31/13 3:39 PM	
Sheet: 4/8	



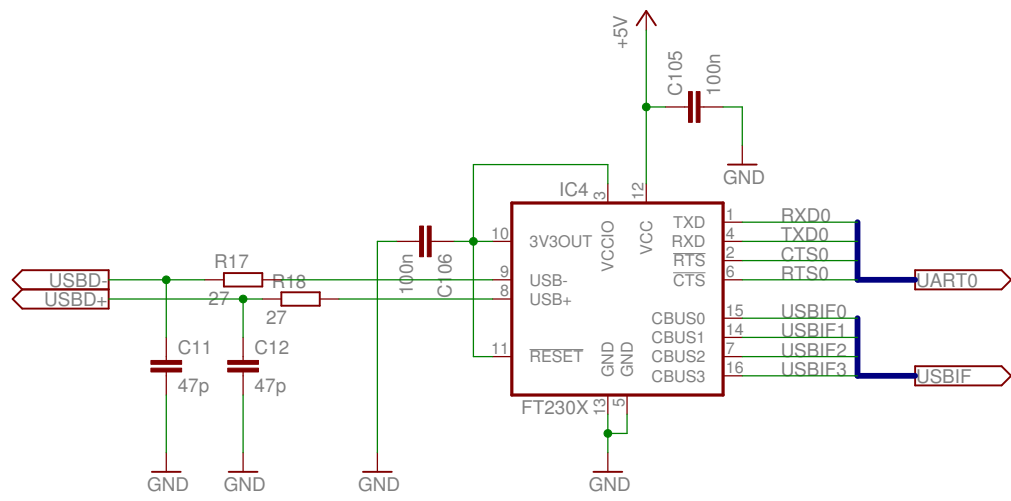
Memory	
at90can-v1.1	
3/31/13 3:39 PM	
Sheet: 5/8	



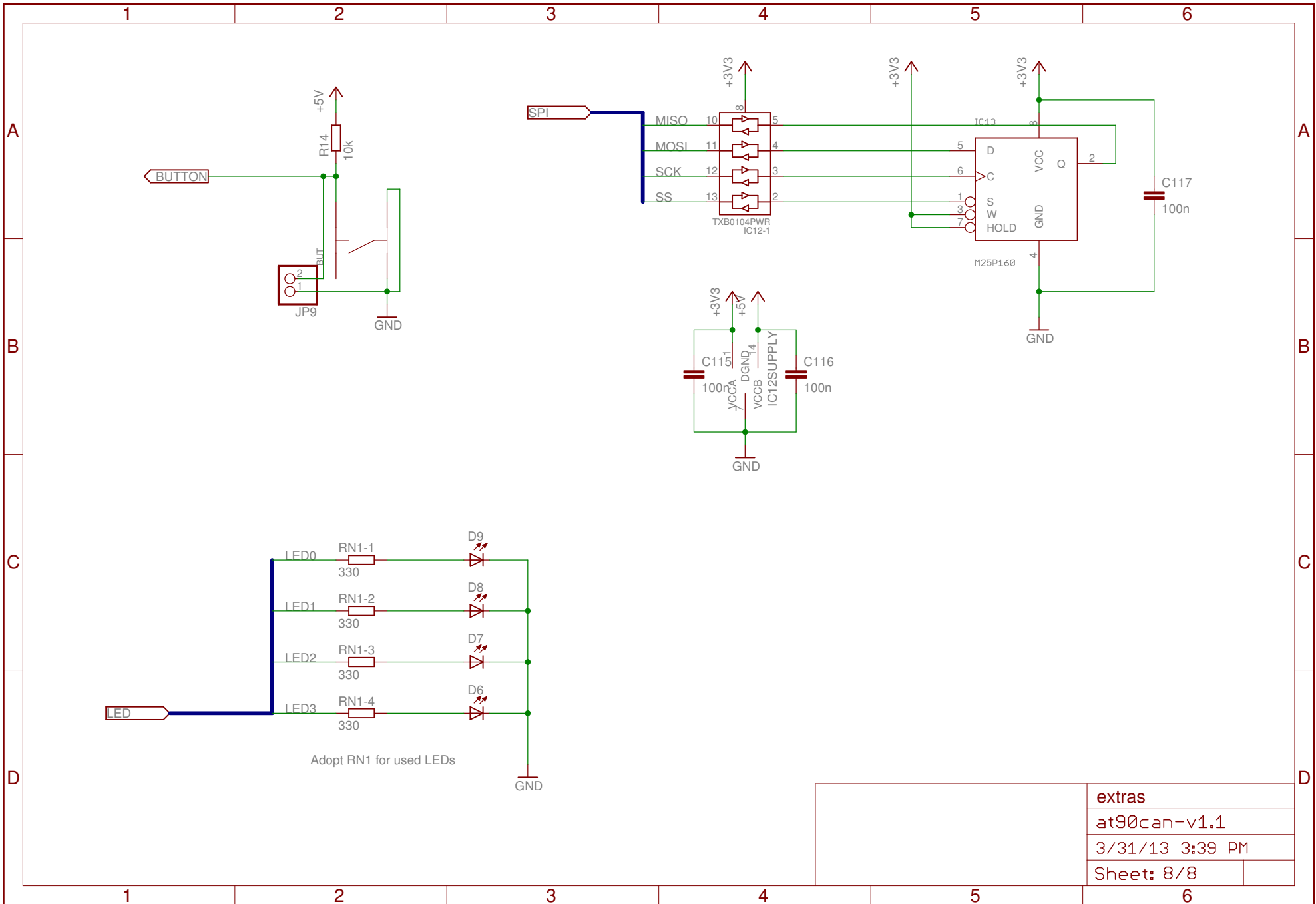
Populate either R7 for regular termination
or R8/R9/C8/C9 for AC termination per ISO15765-4

Populate either R19 for regular termination
or R20/R21/C13/C14 for AC termination per ISO15765-4

Transceiver&Termination	
at90can-v1.1	
3/31/13 3:39 PM	
Sheet: 6/8	



USB	
at90can-v1.1	
3/31/13 3:39 PM	
Sheet: 7/8	



extras	
at90can-v1.1	
3/31/13 3:39 PM	
Sheet: 8/8	