

Morphing the Circuit

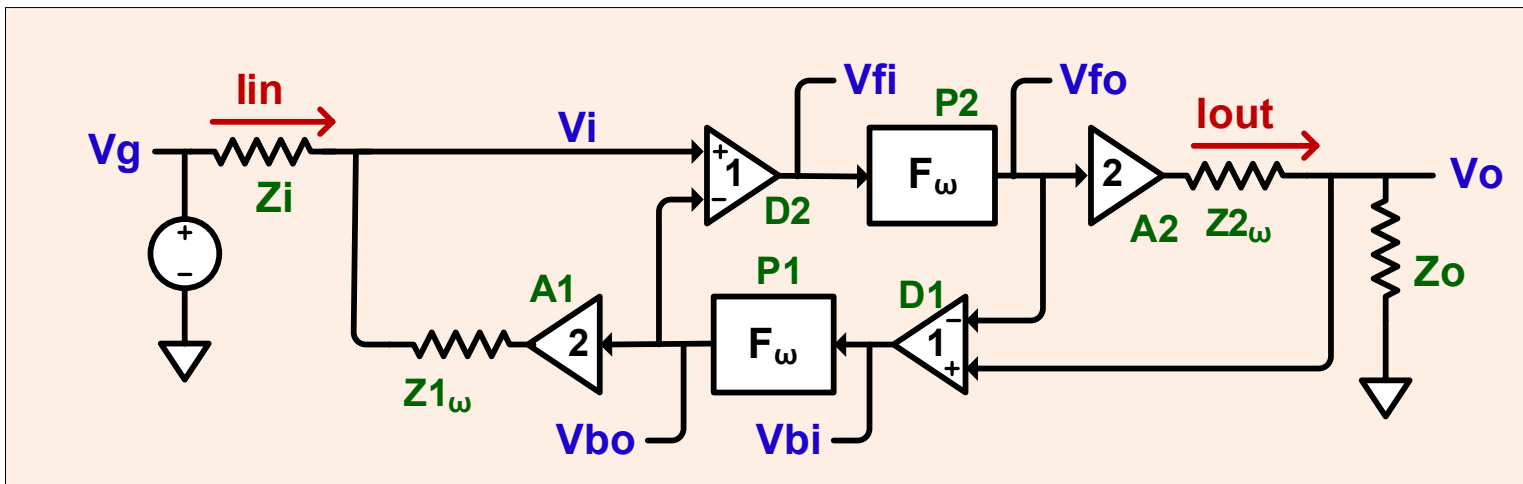


Figure 1:

While we could use the circuit shown in figure 1 directly, it is not optimal for SPICE. SPICE has some idiosyncrasies:

- Current sources are better than voltage sources.

- Laplace transform elements work better if they approach zero at high frequencies.

- An inductor with an internal series resistance works better than a separate inductor and resistor.

- SPICE works better at simulating balanced circuits if the simulated circuit is balanced.

The first three are documented, the last is not, but has been observed. We speculate that certain elements in the sparse matrix cancel exactly in one case and not in the other.

Currents are proportional to voltages. Voltage sources can be replaced with current sources. Voltage dependent sources can be replaced with current dependent sources. By applying some circuit theory transformations, the circuit in figure 1 can be converted to the circuit in fig. 12.

Step 1: redraw the circuit into the more symmetric form shown in fig. 2.

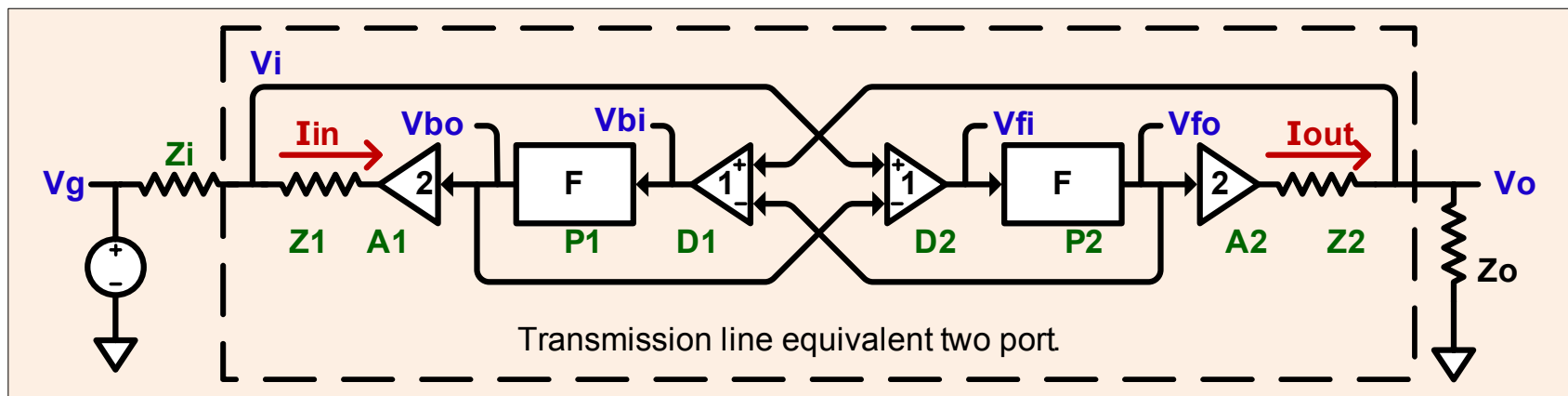


Figure 2: The circuit redrawn to emphasize the symmetry.

Step 2: replace voltage doubler A1 and series impedance Z1 with double current sources G3 and G5 and Z1 in shunt. Likewise A2 and Z2 in series have been replacing by G4 and G6 and Z2 in shunt. The trans-conductance of G3 through G6 is $1/Z$. The result is shown in fig 3. The reason for using double current sources instead of one current source with twice the gain will become apparent in a few steps.

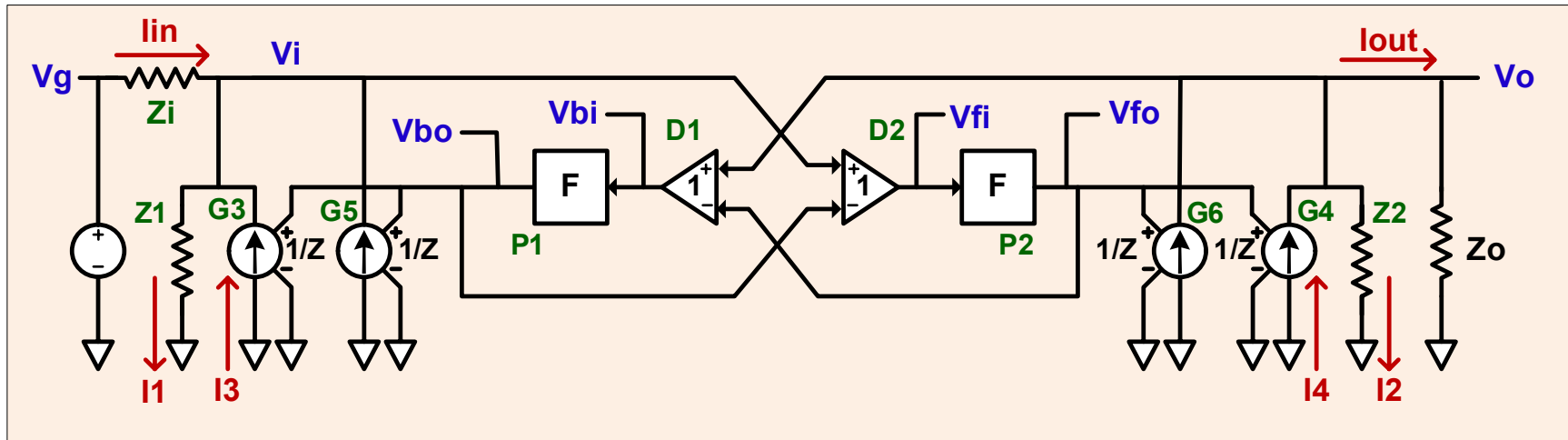


Figure 3. Voltage doublers replaced by transconductance amplifiers.

Step 3: replace the difference amplifier D2 by transimpedance amplifiers H2 and H4 in series. D2 produced the output

$$V_{fi} = V_i - V_{bo}.$$

It should be obvious that

$$I_1 = V_i / Z$$

$$I_3 = V_{bo} / Z.$$

Solving for the voltages yields

$$V_i = (Z) I_1$$

$$V_{bo} = (Z) I_3$$

The series combination of H2 and H4 simply produces $V_{fi} = V_i - V_{bo}$, which is the same as the output of D2. Likewise, D1 has been replaced by the series combination of H1 and H3. The result is shown in fig 4.

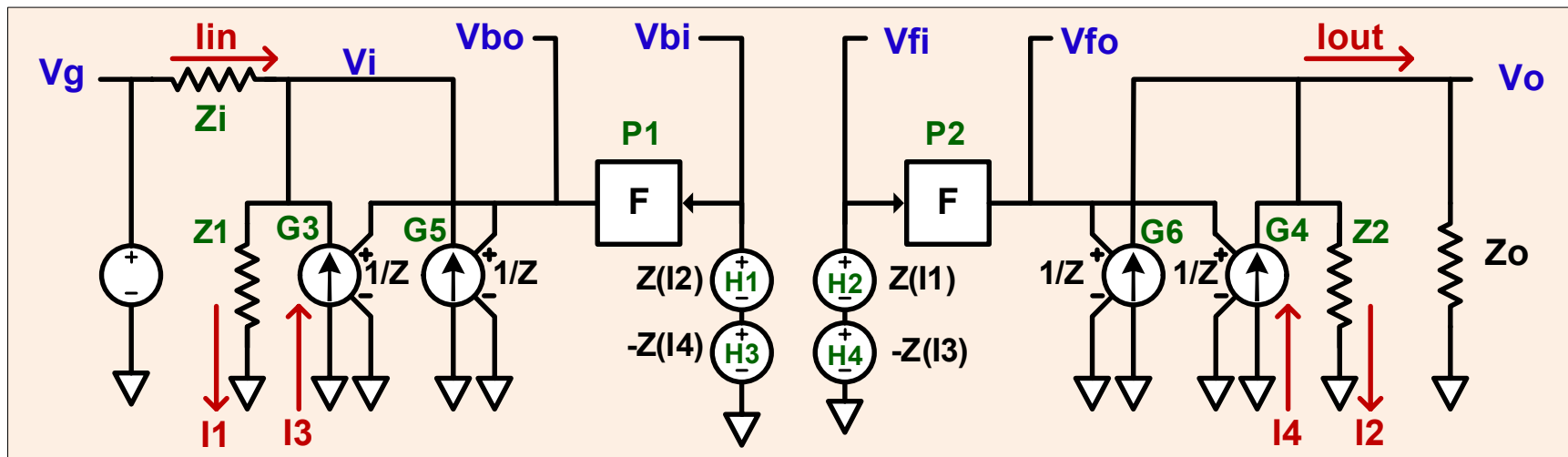


Figure 4. Difference amplifiers replaced by transimpedance amplifiers.

Step 4: insert a zero volt source, V_6 , into a circuit branch that happens to carry the current $I_2 - I_4$. The series combination of H_1 and H_3 produces the signal $I_2 - I_4$. A single transimpedance amplifier, H_6 , controlled by I_6 , the current through V_6 , can replace the series combination of H_1 and H_3 . Similarly, H_5 replaces H_2 and H_4 . The reason for the doubled current sources instead of a current source with twice the gain is so that the branch currents $I_1 - I_3$ and $I_2 - I_4$ exist. The voltages V_{bo} , V_{bi} , V_{fi} and V_{fo} are no longer used in any expression and will be suppressed.

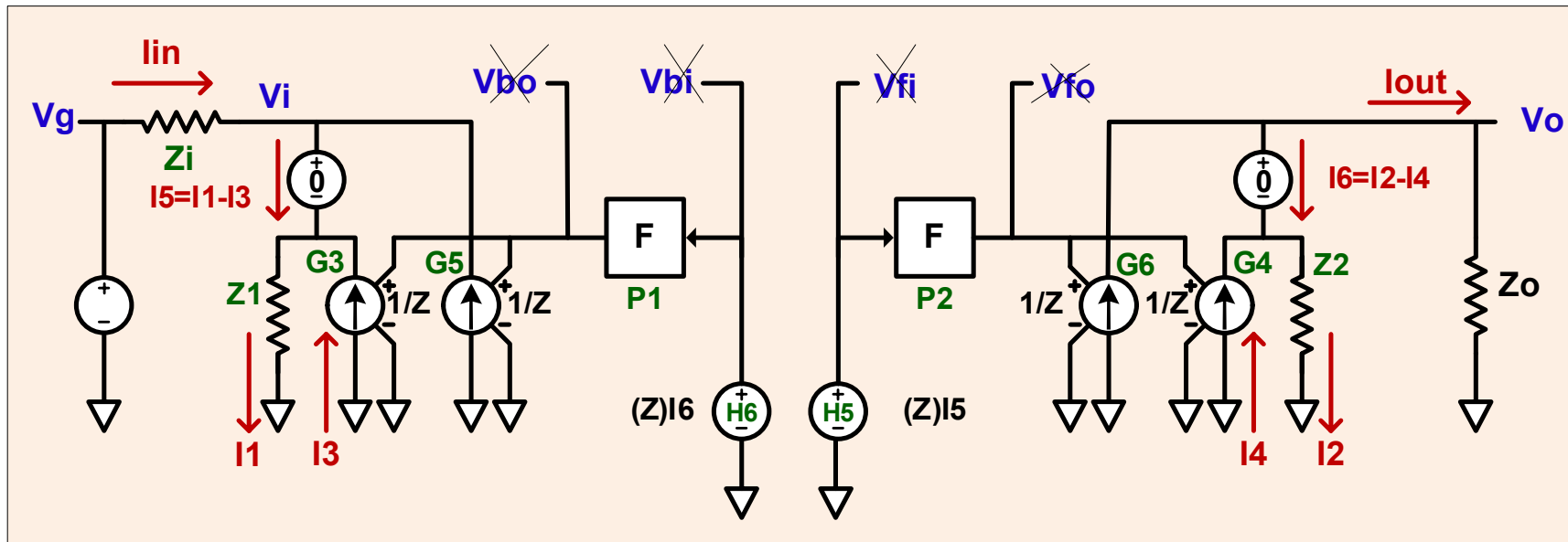


Figure 5. Voltage difference replaced with current difference.

Step 5: decrease the transimpedance gain of H1, H2, H3 and H4 by Z to 1 ohm and increase the transconductance gain of G3, G4, G5 and G6 by Z to 1/ohm. The gain from H6 to V_i is unchanged as is the gain from H5 to V_o . The gain of G3, G4, G5 and G6, which is now unity, will be suppressed in the following figures. The result is shown in fig 6.

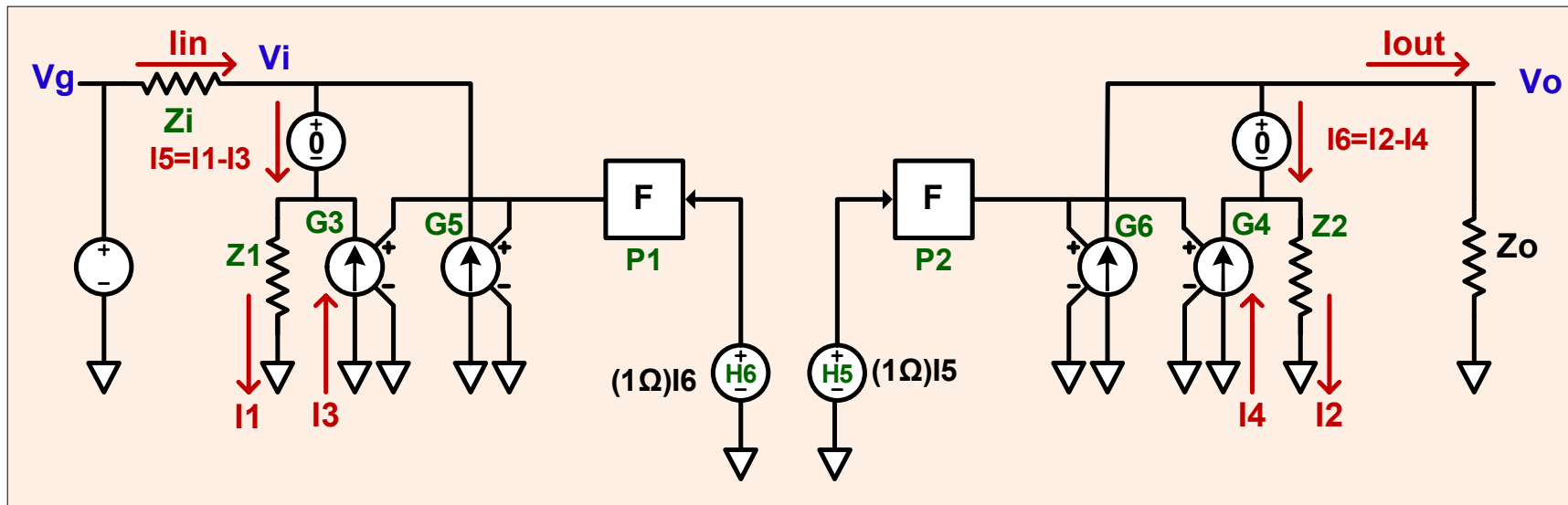


Figure 6. Gains normalized.

Step 6: replace the propagation function P1 by transconductance amplifier G7 driving a 1 ohm resistor. Replace P2 by G8 and a 1 ohm resistor. The result is shown in figure 7.

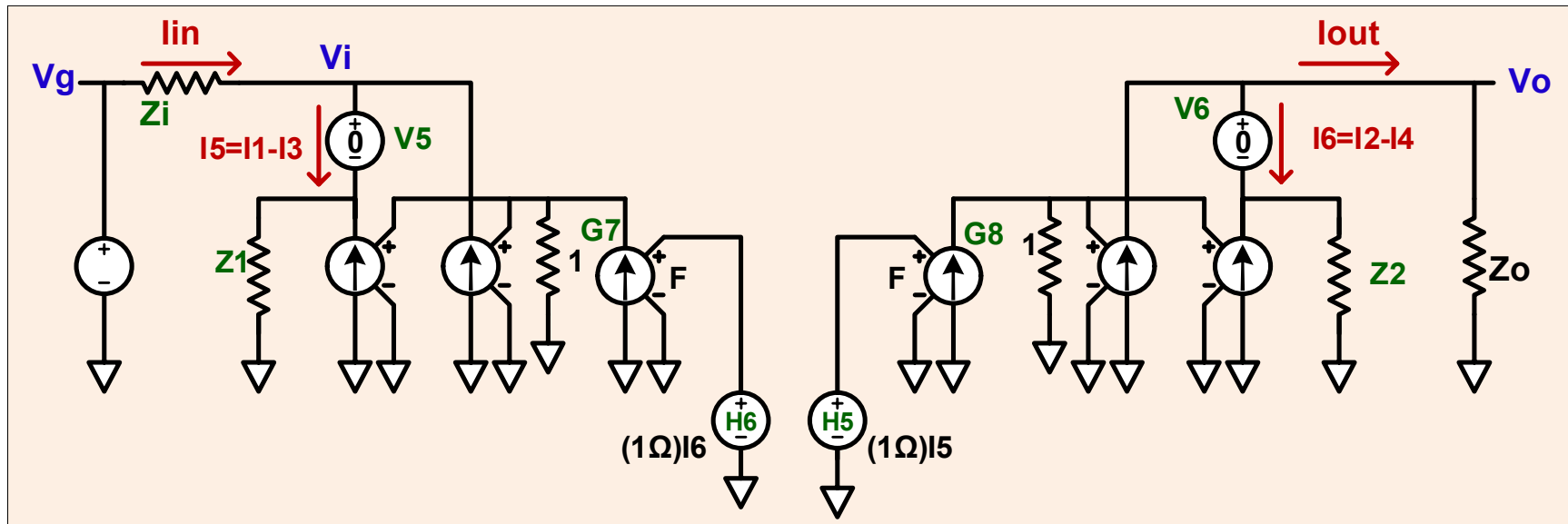


Figure 7. Propagation functions replaced with transconductance amplifiers.

Step 7: replace the impedance Z_1 by transconductance amplifier G_1 . Likewise replace Z_2 G_2 .

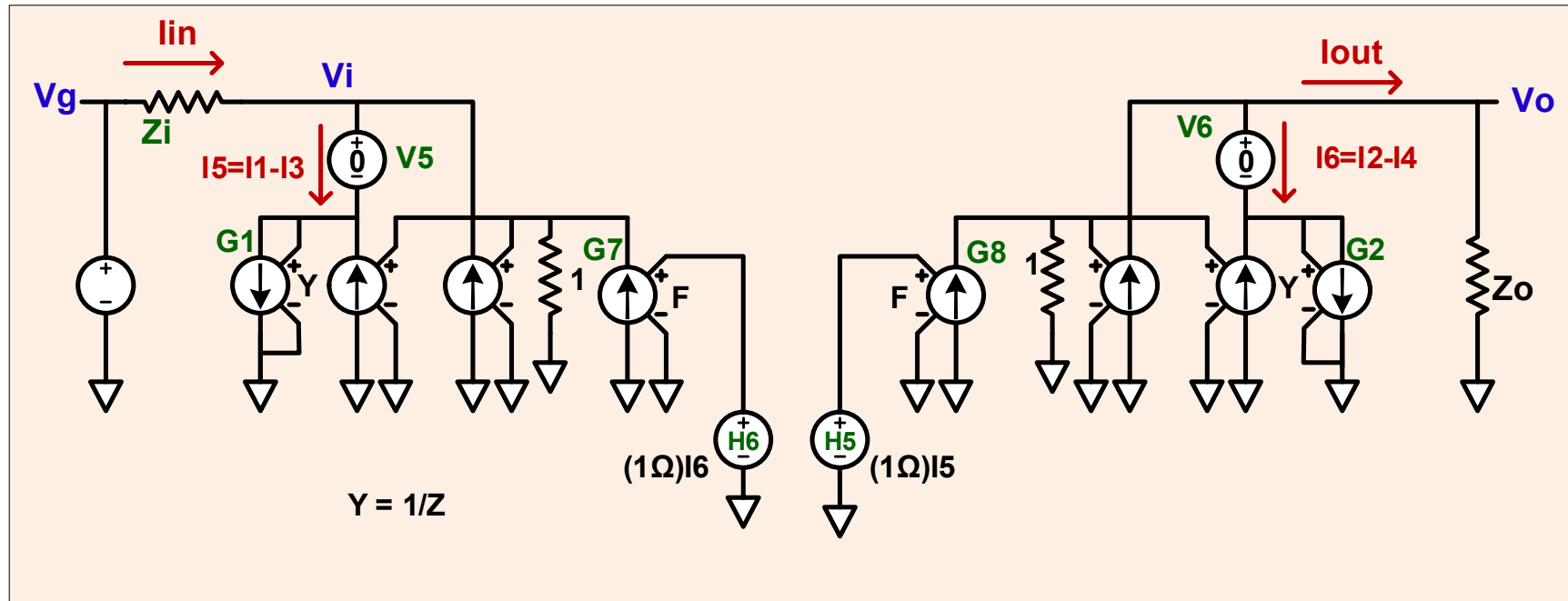


Figure 8. Characteristic impedances replaced with transconductance amplifiers.

There are two problems with the circuit of fig. 8. SPICE requires that Laplace elements roll off to zero at high frequency. G_1 and G_2 approach a constant. G_7 and G_8 do go to zero, but not quickly enough.

Step 8 Increase the roll-off of G7 by dividing its Laplace transform by $(1 + s L_{con})$. Restore the frequency response by adding an inductor, L_{con} , in series with the 1 ohm resistor that G7 drives. Do the same thing to G8 and the 1 ohm resistor that it drives.

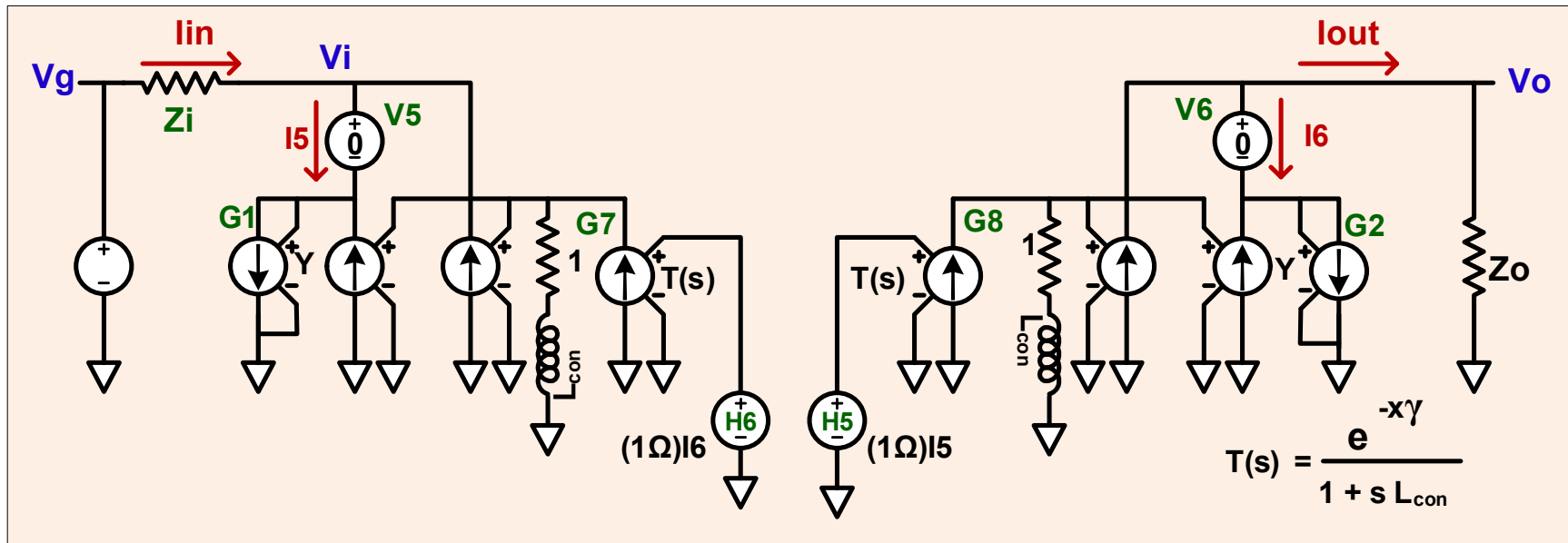


Figure 9.

Step 9: Since, G_1 in the previous figure approaches a constant, subtract that constant out of G_1 . What is left will go to zero at high frequency. The constant removed happens to be a real valued conductance. Add that conductance back by adding a simple resistor in shunt with G_1 . Likewise, subtract the same constant out of G_2 and add it back as a shunt resistor.

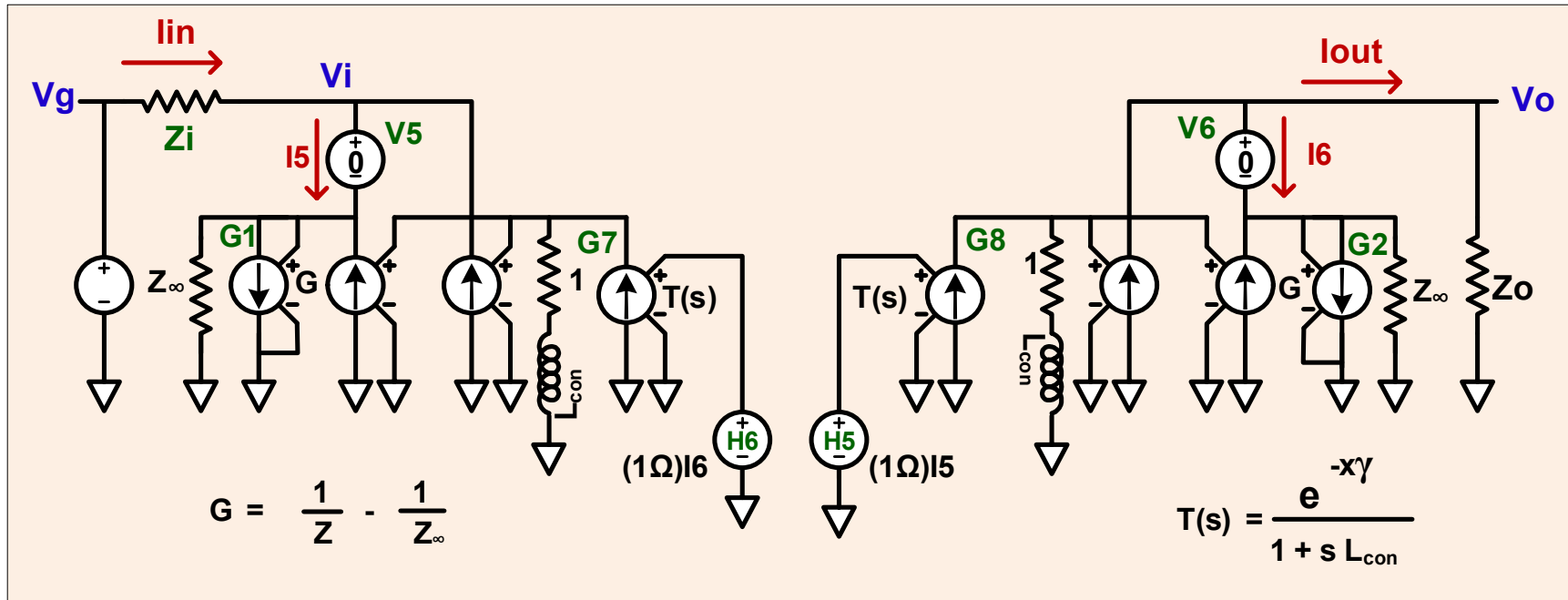


Figure 10.

Step 10: absorb the 1Ω resistor that was in series with L_{con} into L_{con} as an internal series resistance.

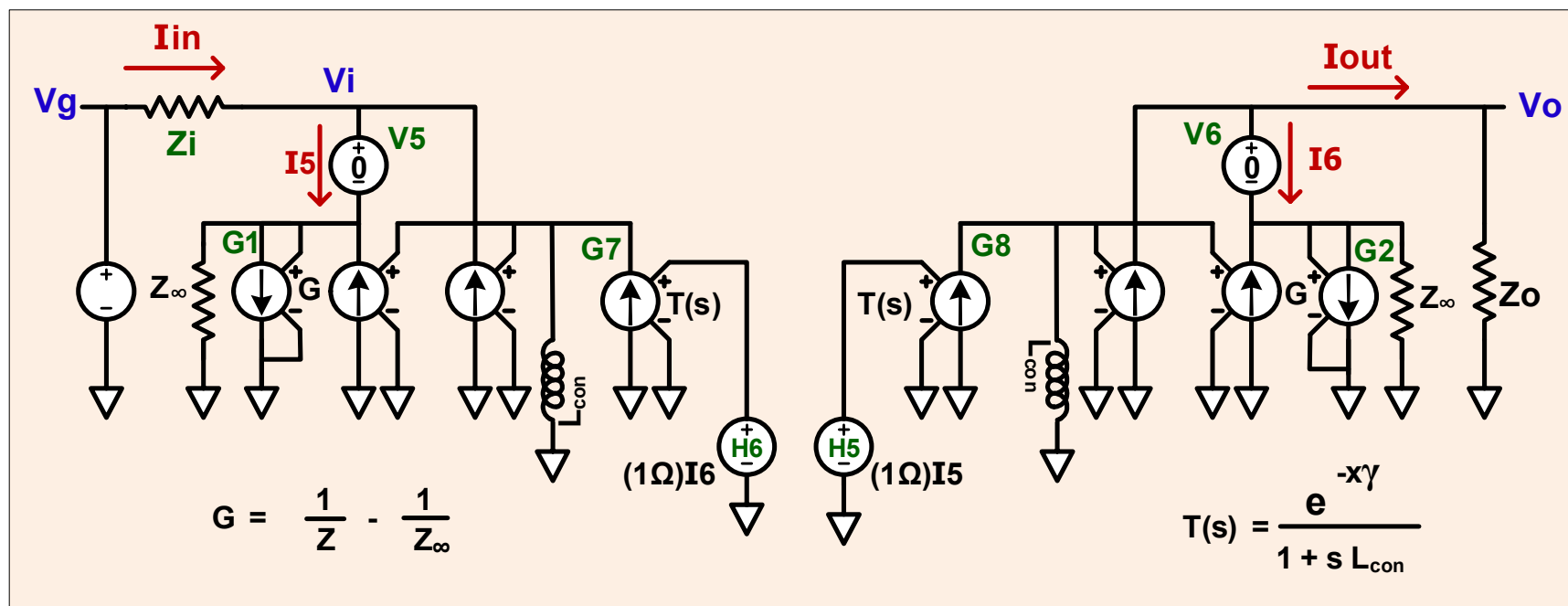


Figure 11

This circuit is now SPICE friendly and can be simulated directly, but it is not balanced. If you are simulating an unbalanced transission line, like a coaxial cable, then you are done. If you simulating a balanced transmission line like a twisted pair, there are a few more steps.

Step 11: change the input source from an unbalanced voltage source to a balanced current source driving a split resistor with the center point grounded. The load resistance has also been split with the center point grounded. The load and source have been grounded because SPICE requires a path to ground for all nodes.

G8 gets its entire signal from V5 which does not occupy a balanced place in the circuit. G7 gets its entire signal from V6 which likewise does not occupy a balanced position in the circuit. Add V7 as a mirror image to V5 and get half of the signal for G8 from V5 and half from V7. Likewise add V8 as a mirror to V6 and get half the signal for G7 from V6 and half from V8. (Dear reader, we haven't dreamed this up as an academic exercise; we had a real problem that would not simulate properly until we made this modification).

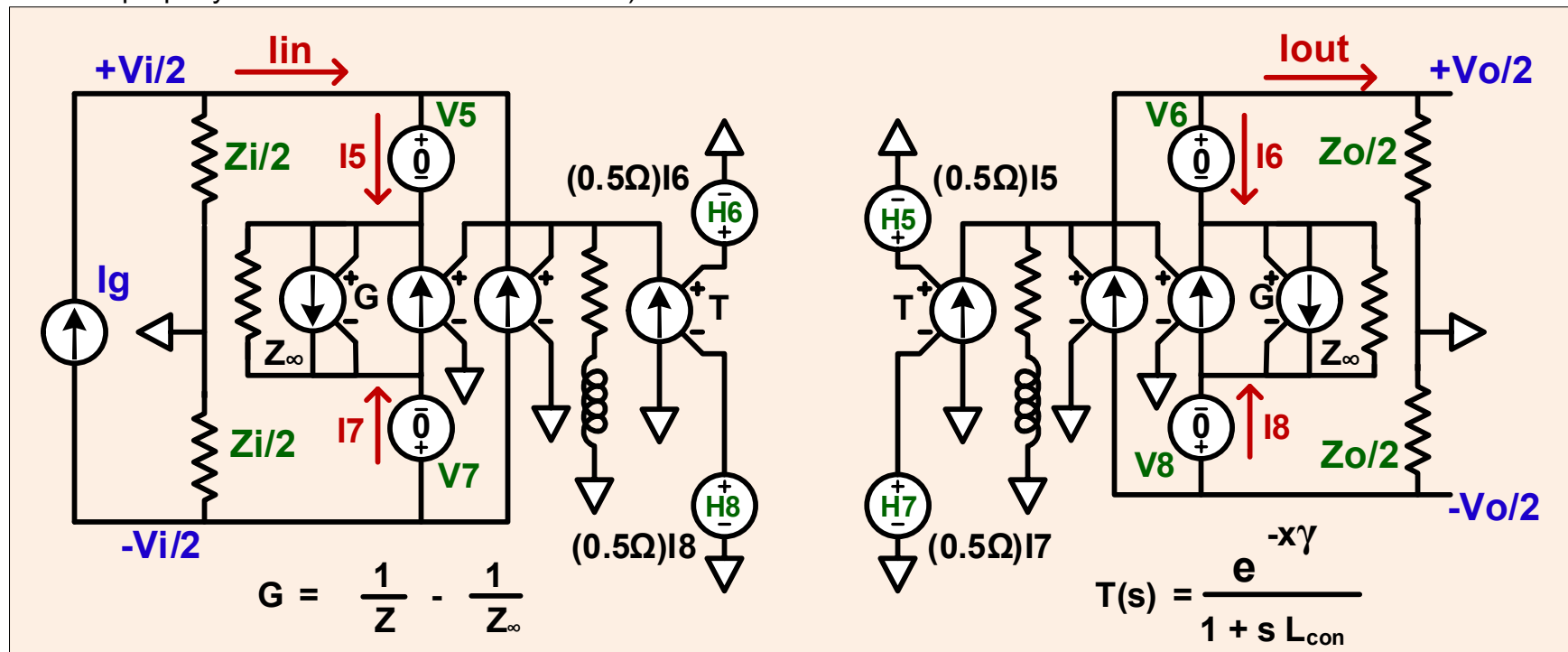


Figure 12

The circuit is now in its final, balanced SPICE ready form. But SPICE doesn't use the mathematical symbols that we have been using. Here then, are the parameter statements and Laplace expressions in their actual SPICE form.

This is the parameter statement that establishes the constants. Note, in a .param statement, ** means exponentiation.

```
.param
+ Kft=1 ; 1000 feet
+ Lcon=1n
+ C=15.72e-9 Gdc=0 Rdc=52.50 Ldc=0.1868e-3 ; dc values
+ F2=5e6 G2=36u Rac=304.62 W2=6.28318*F2 ; highest frequency values
+ F1=3e6 G1=23u ; second highest frequency values
+ WR=W2*(Rdc**2)/(((Rac**4)-(Rdc**4))**0.5) ; resistance term
+ k=Log(G2/G1)/Log(F2/F1)/2 ; conductance exponent
+ Zinf=(Linf/C)**0.5 Yinf=1/Zinf
+ Ldel=(Ldc-Linf)
+ Linf= 0.133e-3 A=1.6 wL=6.28318*161000 ; inductance terms
```

This is the Laplace expression for G7 and G8. Note, in a Laplace expression, ^ means exponentiation.

```
Laplace=Exp(-Kft*(((Rdc*(1-(s/wR)^2)^.25)+s*(Linf+Ldel/(1+A*((-s/wL)^2)^.5)-(s/wL)^2)^.25))*(Gdc+G2*(-(s/w2)^2)^k+s*C))^0.5)/(s*Lcon+1)
```

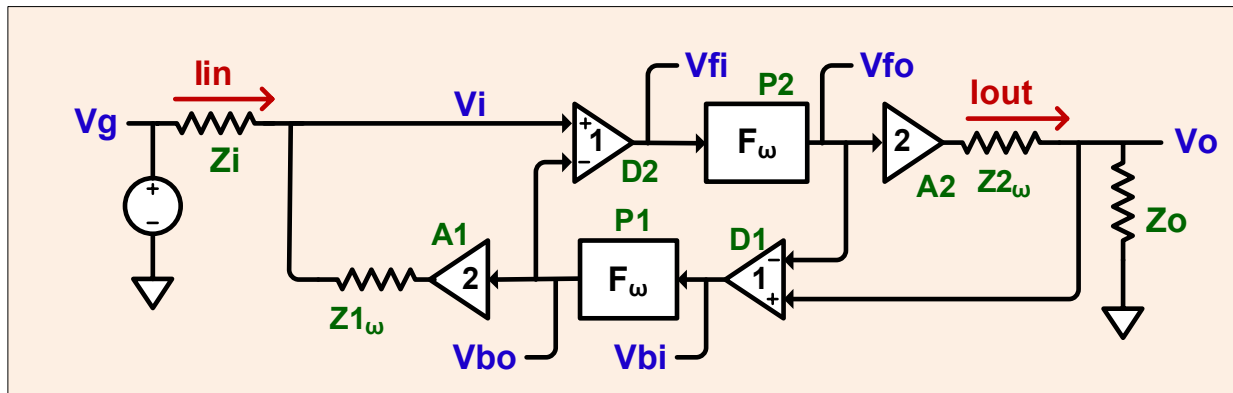
This is the Laplace expression for G1 and G2.

```
Laplace=((((Gdc+G2*(-(s/w2)^2)^k)+s*C)/((Rdc*(1-(s/wR)^2)^0.25)+s*(Linf+Ldel/(1+A*((-s/wL)^2)^0.5)-(s/wL)^2)^0.25)))^0.5) -Yinf
```

The Transfer Function of a Transmission Line with Arbitrary Load and Source Impedances

Solving a circuit from the inside out

Usually, we assume an input voltage and then solve for the other node voltages. After that linearity assures us that all the voltages are proportional to the input voltage. But there is nothing special about input node. You can start with any node and assume a voltage on it and then solve for all the other node voltages. This is especially useful when there is a loop in the signal path. If you work from the input you usually wind up with a set of simultaneous linear equations to solve. But sometimes you can pick a node on the loop and the problem simplifies.



Canonical two port equivalent of a transmission line.

For the circuit in fig. 1 we will select V_{fi} , the input to the P2 block, as the independent variable. From that we can directly compute all the node voltages and branch currents. If that doesn't seem mathematically sound, think of it this way: you have a variable source and a voltmeter. Your voltmeter is connected to V_{fi} and the variable source is connected at V_g .

The source has no voltage readout and the voltmeter can only readout V_{fi} . So, you vary the input source until the voltmeter reads 1 volt. You then apply circuit theory and you discover that in this case you can then compute all the node voltages and branch currents.

Once we have an equation for V_g as a function of V_{fi} , we can solve that equation for V_{fi} as a function of V_g . We can substitute that expression for V_{fi} in all the other equations for node voltage or branch current and we will have the traditional solutions without ever setting up or solving any simultaneous equations.

Definitions:

$$\begin{aligned}\Gamma_1 &\equiv (Z_i - Z_1) / (Z_1 + Z_i) \\ \Gamma_2 &\equiv (Z_o - Z_2) / (Z_2 + Z_o) \\ \delta &\equiv (1 - \Gamma_1 \Gamma_2 F^2) \\ Y_1 &\equiv 1/Z_1 \\ Y_2 &\equiv 1/Z_2\end{aligned}$$

useful combinations

$$\begin{aligned}(1 + \Gamma_1) &= (2)(Z_i) / (Z_i + Z_1) \\ (1 + \Gamma_2) &= (2)(Z_o) / (Z_o + Z_2) \\ (1 - \Gamma_2) &= (2)(Z_2) / (Z_2 + Z_o) \\ (1 - \Gamma_1) &= (2)(Z_1) / (Z_1 + Z_i) \\ Z_i Y_1 &= (1 + \Gamma_1) / (1 - \Gamma_1)\end{aligned}$$

The output of P2 is just F times its input.

$$V_{fo} = (F)V_{fi}$$

The output, V_o , is determined by the gain of A_2 and the voltage divider created by Z_2 and Z_o .

$$\begin{aligned}V_o &= (2 V_{fo}) [Z_o / (Z_o + Z_2)] \\ &= (1 + \Gamma_2)(F) V_{fi}\end{aligned}$$

The output current, I_{out} , can be determined by the voltage drop across Z_2 .

$$\begin{aligned} I_{out} &= [(2V_{fo}) - V_o] / Z_2 \\ &= [2(F)V_{fi} - (1 + \Gamma_2)(F)V_{fi}] Y_2 \\ &= (F) [2 - (1 + \Gamma_2)] Y_2 V_{fi} \\ &= (F) (1 - \Gamma_2) Y_2 V_{fi} \end{aligned}$$

The output of D1 is determined from its inputs V_o and V_{fo} .

$$\begin{aligned} V_{bi} &= V_o - V_{fo} \\ &= (1 + \Gamma_2)(F)V_{fi} - (F)V_{fi} \\ &= (\Gamma_2 F)V_{fi} \end{aligned}$$

The output of P1 is just F times its input.

$$\begin{aligned} V_{bo} &= (F) V_{bi} \\ &= (\Gamma_2 F^2)V_{fi} \end{aligned}$$

The output of D2 is determined from V_i and V_{bo} .

$$V_{fi} = V_i - V_{bo}$$

This can be solved for V_i .

$$\begin{aligned} V_i &= V_{fi} + V_{bo} \\ &= V_{fi} + (\Gamma_2)(F^2)V_{fi} \\ &= (1 + \Gamma_2 F^2) V_{fi} \end{aligned}$$

The input current, I_{in} , can be determined by the voltage drop across Z_1 .

$$\begin{aligned}
I_{in} &= (V_i - 2V_{bo})/Z_1 \\
&= [(1 + \Gamma_2 F^2) V_{fi} - 2(\Gamma_2 F^2) V_{fi}] Y_1 \\
&= Y_1 V_{fi} (1 - \Gamma_2 F^2)
\end{aligned}$$

V_g can be determined from V_i and I_{in} .

$$\begin{aligned}
V_g &= V_i + (Z_i) I_{in} \\
&= (1 + \Gamma_2 F^2) V_{fi} + (1 - \Gamma_2 F^2) Z_i Y_1 (V_{fi}) \\
&= [(1 + \Gamma_2 F^2) + (1 - \Gamma_2 F^2)(1 + \Gamma_1) / (1 - \Gamma_1)] (V_{fi}) \\
&= 2(V_{fi})(1 - \Gamma_2 F^2) / (1 - \Gamma_1)
\end{aligned}$$

This can be solved for V_{fi}

$$\begin{aligned}
V_{fi} &= (V_g/2)(1 - \Gamma_1) / (1 - \Gamma_1 \Gamma_2 F^2) \\
&= (V_g/2)(1 - \Gamma_1) / \delta
\end{aligned}$$

Substituting for V_{fi} in selected equations gives:

$$\begin{aligned}
V_i &= (V_g/2) (1 + \Gamma_2 F^2) (1 - \Gamma_1) / \delta \\
V_o &= (V_g/2) (1 + \Gamma_2)(F)(1 - \Gamma_1) / \delta \\
V_o/V_i &= (F)(1 + \Gamma_2) / (1 + \Gamma_2 F^2) \\
I_{out} &= (Y_2 V_g/2) (F) (1 - \Gamma_2) (1 - \Gamma_1) / \delta \\
I_{in} &= (Y_1 V_g/2) (1 - \Gamma_1) (1 - \Gamma_2 F^2) / \delta
\end{aligned}$$

We have now solved the circuit without ever solving or even creating simultaneous equations.

Interpretation

Let's see what these equations tell us in the context of transmitting brief pulses. We will assume that the transmission line is matched at source so $\Gamma_1 \equiv 0$ and $\delta \equiv 1$ and the equations of interest simplify to

$$\begin{aligned} V_i &= (V_g/2) (1 + F^2 \Gamma_2) \\ V_o &= (V_g/2) (F) (1 + \Gamma_2) \\ I_{out} &= (Y_2 V_g/2) (F) (1 - \Gamma_2) \end{aligned}$$

We will assume a 50 ohm transmission line that has a delay of 150 ns and reduces pulse amplitude to 80% in a one way transit. We will also assume V_g produces a 2 volt pulse. In this context:

$$\begin{aligned} F &\equiv \text{a gain of 0.80 and a delay of 150 ns} \\ F^2 &\equiv \text{a gain of 0.64 and a delay of 300 ns} \\ V_g/2 &\equiv \text{a 1 volt pulse} \\ Y_2 V_g &\equiv \text{a 40 ma current pulse} \end{aligned}$$

Case 1. Coax is matched at the load, hence $\Gamma_2 = 0$

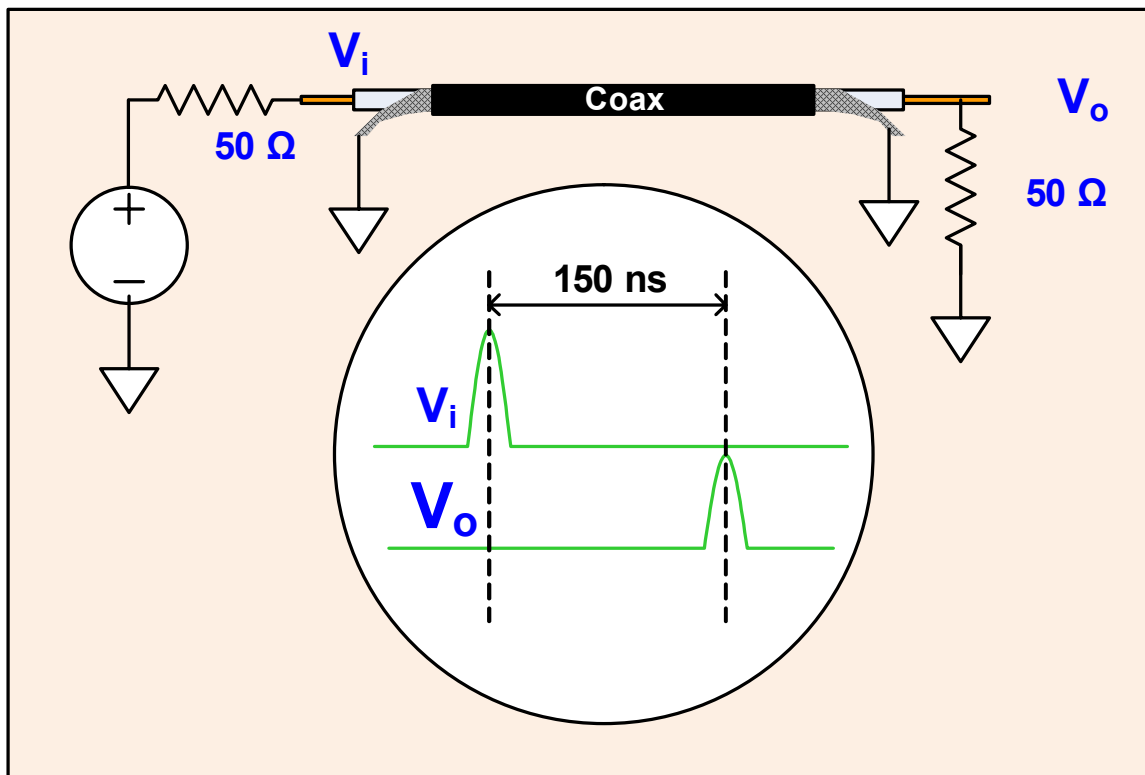
$$\begin{aligned} V_i &= (V_g/2) \\ V_o &= (V_g/2) (F) \\ I_{out} &= (Y_2 V_g / 2)(F) \end{aligned}$$

Input voltage, V_i , is a 1v pulse.

Output voltage, V_o , is a 0.8v pulse delayed 150 ns.

Output current, I_{out} , is a 16 ma pulse delayed by 150 ns.

There are no further pulses.



Case 2. Coax is shorted at the load, hence $\Gamma_2 = -1$

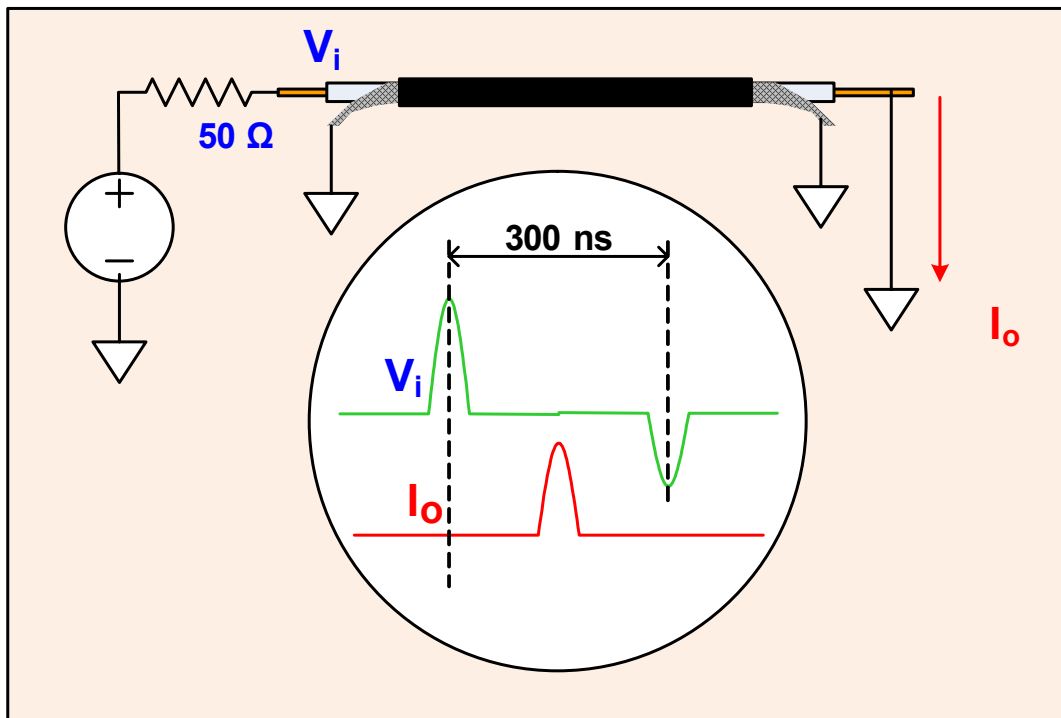
$$\begin{aligned} V_o &= 0 \\ I_{out} &= (Y_2 V_g)(F) \\ V_i &= (V_g/2)(1-F^2) \end{aligned}$$

Output voltage, V_o , into a short circuit is zero.

Output current, I_{out} , is a 32 ma pulse delayed by 150 ns.

Input voltage, V_i , is a 1v pulse and then 300 ns later, a -0.64 v reflected pulse.

There are no further pulses.



Case 3. Coax is open at the load hence $\Gamma_2 = 1$

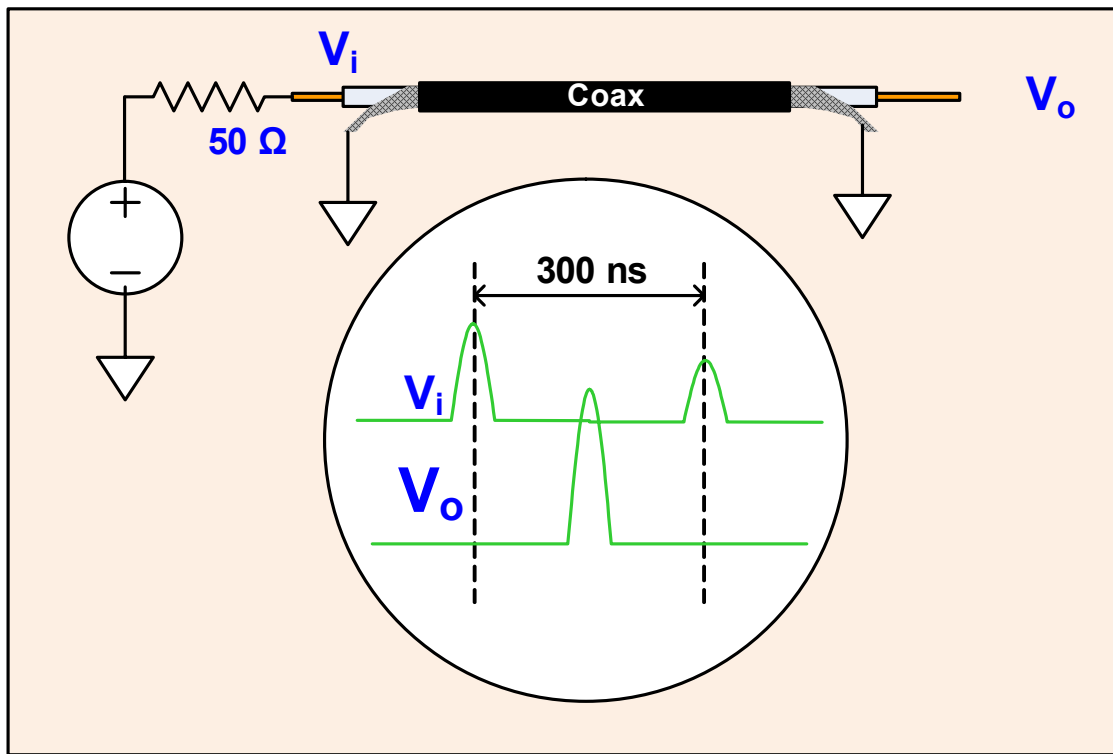
$$\begin{aligned} V_o &= (V_g)(F) \\ I_{out} &= 0 \\ V_i &= (V_g/2)(1+F^2) \end{aligned}$$

Output voltage, V_o , is a 1.6v pulse delayed 150 ns.

Output current, I_{out} , into the open load is zero.

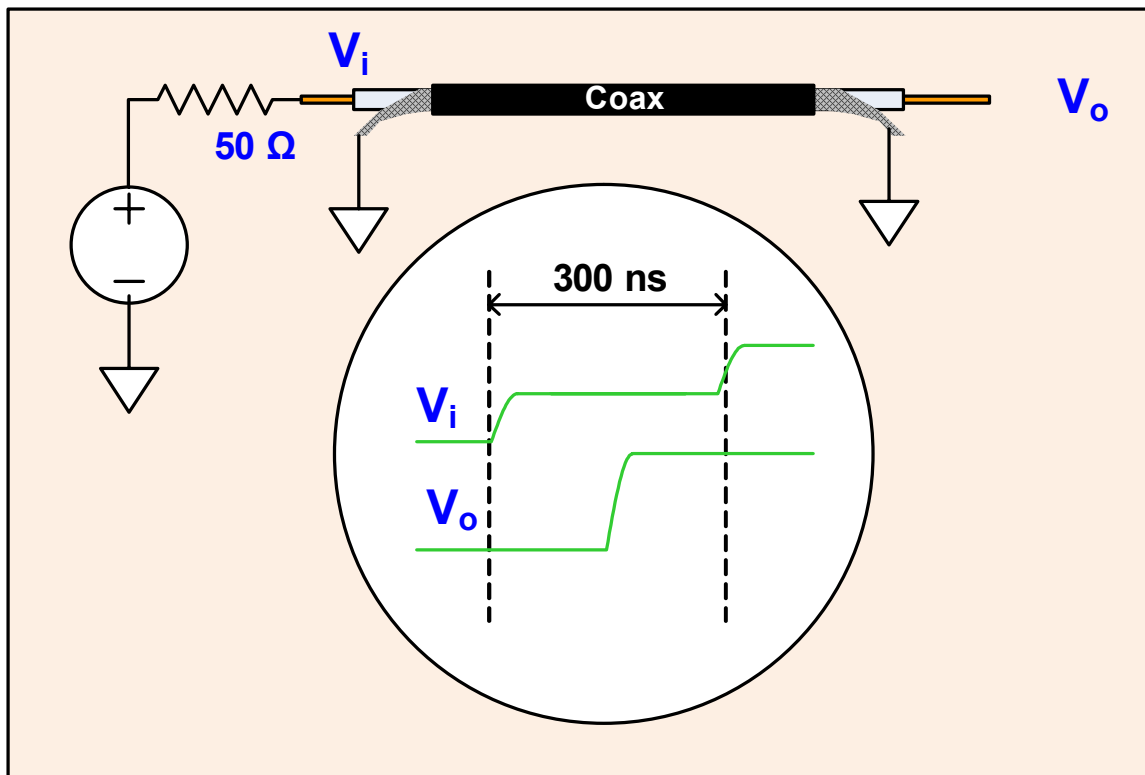
Input voltage, V_i , is a 1v pulse and then 300 ns later, a 0.64 v reflected pulse.

There are no further pulses.



Case 4. Coax is open at the load hence $\Gamma_2 = 1$ and the input is a step

$$\begin{aligned} V_o &= (V_g)(F) \\ I_{out} &= 0 \\ V_i &= (V_g/2)(1+F^2) \end{aligned}$$



A Closer Look at Telegrapher's Solutions

The behavior of Z and F have three distinct regions for ordinary transmission lines.

At high frequency, since we know the general form of the primary parameters, we can state that:

$$\begin{array}{llll}
 \omega L_{\omega} >> & R_{\omega} & & \\
 \omega C_{\omega} >> & G_{\omega} & & \\
 Z_{\infty} = & \sqrt{L_{\infty} / C_{\infty}} & \text{a purely real constant} & \\
 \text{delay} = & \beta / \omega = \sqrt{L_{\infty} C_{\infty}} & \text{a constant} & \\
 \alpha = & R_{\omega} / (2Z_{\omega}) + Z_{\omega} G_{\omega} / 2 & \text{loss increases with } \omega \text{ because both } R_{\omega} \text{ and } G_{\omega} \text{ increase with frequency.} &
 \end{array}$$

At intermediate frequencies:

$$\begin{array}{l}
 \omega L_{\omega} \ll R_{\omega} \\
 \omega C_{\omega} \gg G_{\omega} \\
 Z = \sqrt{R_{dc} / (j\omega C_{dc})} \text{ which has a phase angle of } -45^{\circ} \text{ and increases inversely with } \omega \\
 \gamma = \sqrt{j\omega R_{dc} C_{dc}} = \sqrt{\omega R_{dc} C_{dc} / 2} + (j) \sqrt{\omega R_{dc} C_{dc} / 2} \text{ decreases with } \omega
 \end{array}$$

At dc and very low frequencies:

$$\begin{array}{l}
 \omega L_{\omega} = 0 \\
 \omega C_{\omega} = 0 \\
 Z_{dc} = \sqrt{R_{dc} / G_{dc}} = \text{a large real number} \\
 \gamma = \sqrt{R_{dc} G_{dc}} = R_{dc} / Z_{dc} = \text{loss} = \text{a small real number.}
 \end{array}$$

The DC Case

Does our model work at dc? It is not obvious from the unfamiliar forms that the solution takes. The characteristic impedance is very high. How, you ask, can you get any signal into it at all? The answer is that the line is mismatched at both ends and the reflections from the far end diminish the impedance at the near end and the reflections at the near end diminish the impedance at the far end. The two ends swap geometrically decreasing reflections that with a little smoothing look just like an RC charging curve. If you account for all the reflections in the end you wind up with exactly what you see on your oscilloscope when you connect a dc source to a transmission line with a dc load.

The derivation and solution of the Telegrapher's Equations makes no assumption about the frequency of the signals and does not exclude dc. It ought to work. And in fact it does. In the zero frequency case, some terms disappear and others can be replaced by approximations in the form of simple monomials. At the end all that is left is what you would expect from dc circuit theory: if the line is not too long, the wire resistance and the load form a voltage divider and the dc voltage at the load is a less than at the source by just the right amount. And how long is too long? It depends on G_{dc} . If the line is long enough that the shunt conductance becomes significant. And what happens in that case? Nothing much. The telegraphers equations and solutions still work. The model still works. The only thing that doesn't work is dc circuit theory using lumped components.

How about SPICE. Does it work with this model at dc? The answer is yes for transient analysis, a qualified yes for AC analysis and a conditional yes for DC operating point analysis.

In the transient analysis, you put a step function (from zero to one for example) and you see the wave form settle down to a sustained dc level. The model definitely works in the transient analysis.

In the AC analysis, you cannot specify 0 Hz, but you can make the low frequency as low as you want (even one cycle per year ~ 31 nHz). The model works in the AC analysis with the understanding that the frequency must be positive.

What about DC operating point? If the equations work and the model works and the transient and AC analysis work then the operating point analysis ought to work. Right? Yes it ought to work, but in the examples given in this article it does not. But we can make it work, by increasing the inductance of L_{con} .

But, how do we feel about this? We feel disquieted. We are of the opinion that this guiding principal should hold: if the model is a sufficiently accurate model of the physical system, the simulator ought to produce accurate results. Thus, we should expend our effort on accurate modeling and not on appeasing the simulator. Once we get accurate simulation, we would be willing to tweak the model to run faster.

We are not bothered by documented, universal principals such as current sources are preferred to voltage sources or that Laplace functions should go to zero for infinite frequency. We are not bothered by undocumented, universal principals that we have discovered such as balanced circuits should be modeled by balanced models (which also is in accordance with our guiding principal of attempting to accurately model the physical system). We are a little bothered if we have to have a tweak such as L_{con} that may need to be adjusted depending on rise times and transmission line length. But we are greatly bothered by having to change the value of the tweak for different types of analysis and especially if we have to change a reactance to get the dc operating point. Reactance should have no effect on the DC operating point.

So, what to do? We offer the following suggestion. If you can find a range of values for L_{con} of say 100 to one where the outcome of a particular analysis does not change significantly, then set L_{con} for the geometric mean of the range and use that value for that analysis. Certainly, when you do run any analysis you should try both larger and smaller values for L_{con} .

Doing the Math for Zero Hertz

Let's examine the equation $V_o/V_i = (F)(1+\Gamma_2)/(1+F^2\Gamma_2)$ at dc.

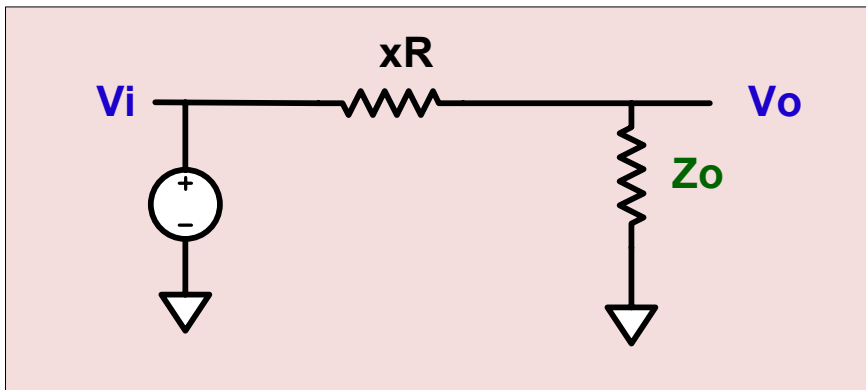


Figure 1. The equivalent circuit of a transmission line at DC.

Fig. 1 shows what we would expect from dc circuit theory. R is ohms per kft and x is the length in kft. So xR is just the total resistance of the wire between the input and the output.

$$\begin{aligned}
 \Gamma_2 &\equiv (Z_o - Z_2) / (Z_2 + Z_o) \\
 Z_1 &= \text{Sqrt} \{ (R_\omega + sL_\omega) / (G_\omega + sC_\omega) \} . \\
 Z_2 &= Z_1 \\
 \gamma(s) &= \text{Sqrt} \{ (R_\omega + sL_\omega) (G_\omega + sC_\omega) \} \\
 F(s) &= e^{-x\gamma(s)}
 \end{aligned}$$

We will need some approximations. If m and n are very small numbers then:

$$\begin{aligned}
 1/(1+m) &\approx 1-m \\
 e^m &\approx 1+m
 \end{aligned}$$

$$\begin{aligned}(1+m)(1+n) &\approx 1 + m + n \\ (1+m)^2 &\approx 1 + 2m\end{aligned}$$

First, we set $s=0$ and $\omega=0$ in the equations for $Z1$, $Z2$, $F(s)$ and $\gamma(s)$.

$$\begin{aligned}Z1 &= \text{Sqrt} \{ (R_{dc}) / (G_{dc}) \} \\ Z2 &= Z1 \\ \gamma(0) &= \text{Sqrt} \{ (R_{dc}) (G_{dc}) \} \\ F(0) &= e^{-x\gamma(0)}\end{aligned}$$

For the rest of this section we will suppress the "dc" subscripts.

The term $\text{Sqrt} \{ G / R \}$ is a very small conductance per unit length that will show up often. We'll assign it the symbol " ϵ ".

$$\begin{aligned}\epsilon &= \text{Sqrt} \{ G / R \} \\ Z1 &= \text{Sqrt} \{ R/G \} \\ &= 1/\epsilon \\ \Gamma_2 &= (Z_0 - Z2) / (Z_0 + Z2) \\ &= -(1 - \epsilon Z_0) / (1 + \epsilon Z_0) \\ &= -(1 - 2\epsilon^2 Z_0^2 + \epsilon^2 Z_0^2) / (1 - \epsilon^2 Z_0^2) \\ &\approx (-1 + 2\epsilon Z_0) \\ \gamma &= \text{Sqrt} \{ R G \} \\ &= (R) \text{Sqrt} \{ G/R \} \\ &= R\epsilon \\ F &= e^{-x\gamma} \\ &= e^{-xR\epsilon}\end{aligned}$$

$$\begin{aligned}
&\approx 1 - xR\epsilon \\
F^2 &\approx (1 - xR\epsilon)^2 \\
&\approx 1 - 2xR\epsilon \\
F^2\Gamma_2 &\approx (1 - 2xR\epsilon)(-1 - 2\epsilon Z_0) \\
&\approx -(1 - 2xR\epsilon - 2\epsilon Z_0) \\
&= -1 + 2xR\epsilon + 2\epsilon Z_0 \\
V_o/V_i &= (F) [1 + \Gamma_2] / [1 + F^2\Gamma_2] \\
&\approx (1 - xR\epsilon) [1 + (-1 + 2\epsilon Z_0)] / [1 + (-1 + 2xR\epsilon + 2\epsilon Z_0)] \\
&= (1 - xR\epsilon) (2\epsilon Z_0) / (2xR\epsilon + 2\epsilon Z_0) \\
&= (1 - xR\epsilon) (Z_0) / (xR + Z_0) \\
&\approx (Z_0) / (xR + Z_0)
\end{aligned}$$

This says that the total wire resistance, xR , and the output load, Z_0 , form a voltage divider exactly as if the total wire resistance were a single lumped component. In other words, we get exactly what we would get from circuit theory. There is caveat. The term $xR\epsilon$ may not be small because x , the length, is unlimited and could be very long. The telegrapher's equations still work. The two port model still works. But the distributed shunt conductance has become large enough that the approximations are not sufficiently accurate to apply the theory of circuits constructed from lumped elements.