

A

B

C

D

E

F

TO EV2300
SMB
Ports

1
Not populated

Charger section

1
TP1 PGND
CONN_DC_JPD1131-DB371-7F
J8
VIN

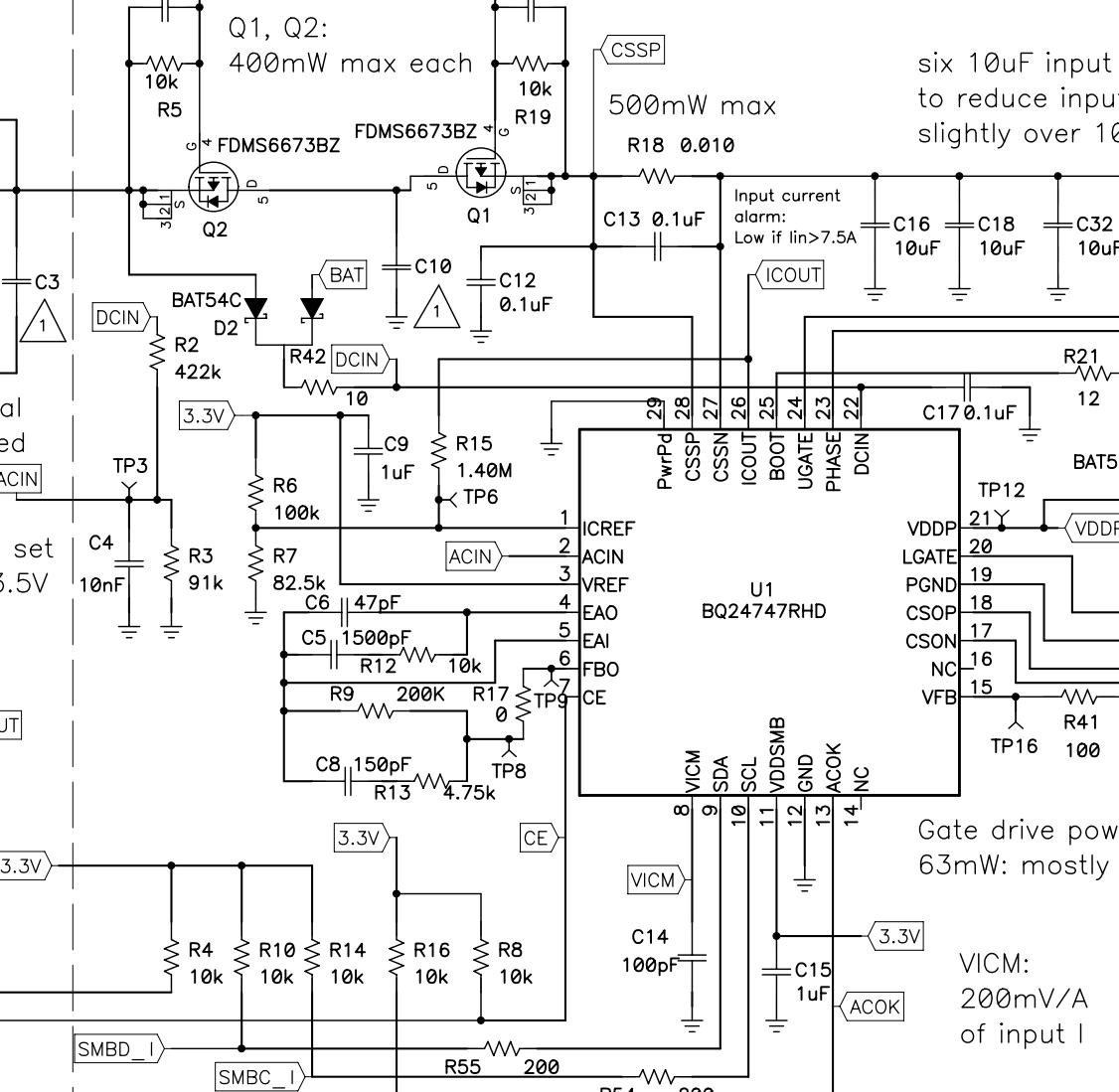
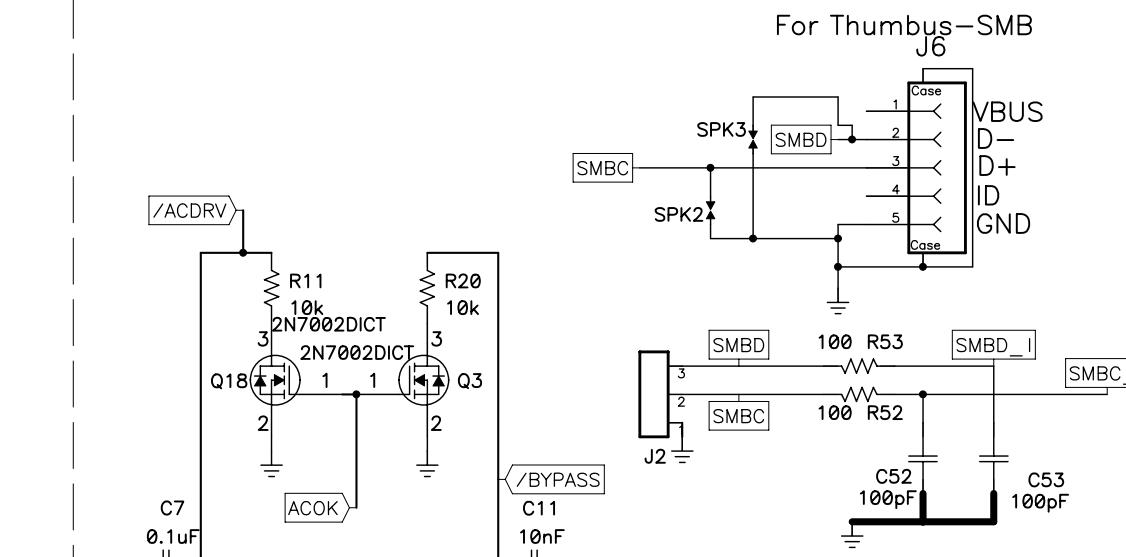
Adapter Input

Vin = 19.5V

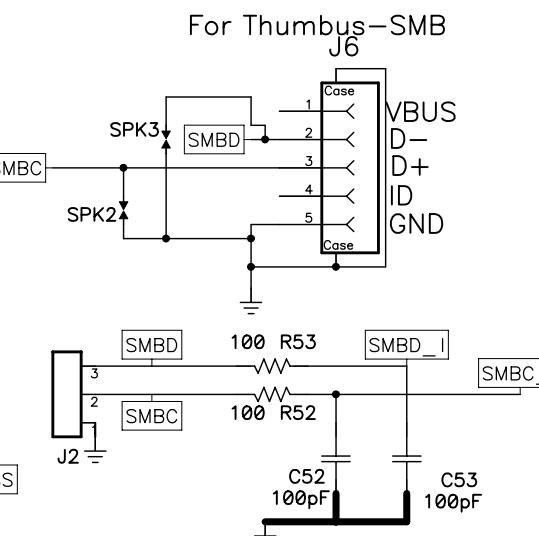
ACPWR GND
J1
R1 3.9
R40 3.9
C1 2.2uF
C2 1
provisions for additional input damping if needed

UVLO set at 13.5V

ACOK
ICOUT
VICM
VREF
J4
CE
J7
DIS CHG

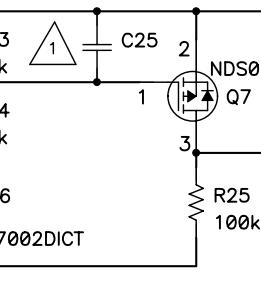


For Thumbs-SMB
J6

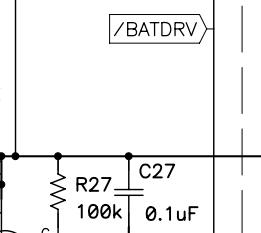


six 10uF input caps
to reduce input ripple to
slightly over 100mV p-p

1000mW max



600mW max
for 8A load



BAT

SYS

TP32

SYS

BAT

TP33

SYS

BAT

TP34

SYS

BAT

TP35

SYS

BAT

TP36

SYS

BAT

TP37

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BAT

TP38

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TP39

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TP78

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TP81

SYS

BAT

A

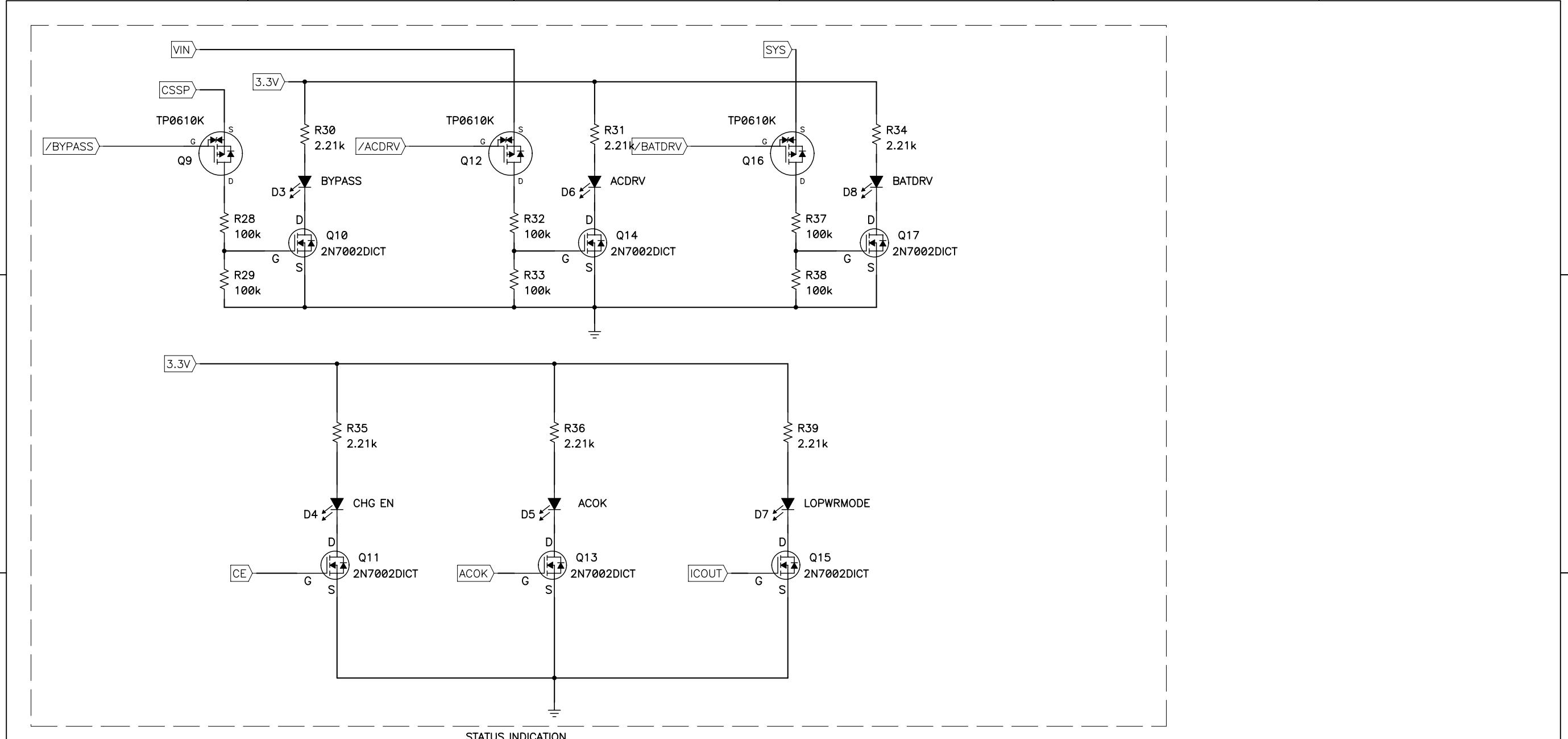
B

C

D

E

F



LED display
of charging
status
(optional)

display

Texas Instruments

Title		
3 cell 4A off 15Vin BQ24745, BQ3060		
Size	Number	Rev
C	PMP5214	A
Date	June 14, 2010	Drawn by Josh Mandelcorn
Engineer	Josh Mandelcorn	Filename PMP5214_revA.sch
		Sheet 2 of 3

A

B

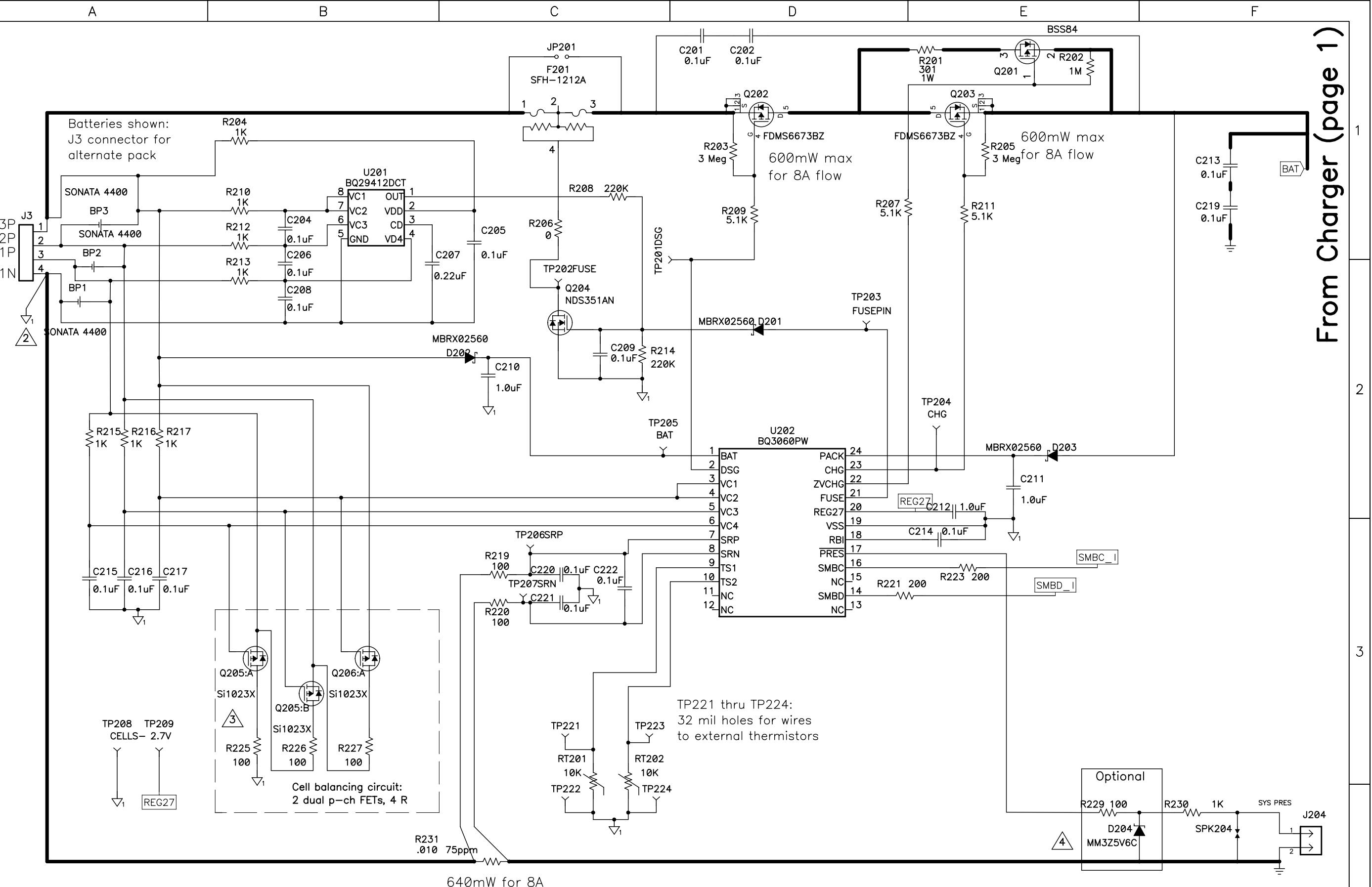
C

D

E

F

17:18:23



From Charger (page 1)

2 IC ground should be connected to the 1N cell tab.

3 R225 – R227: now size 1206: If smaller resistors are used, user should make sure the power rating of the resistor can handle the desired cell balance current

4 Optional – Only required if PRES has a chance to short PACK+ (NOT AVAILABLE IN REV.A EVM)

This page taken from
BQ3060 EVM HPA328 rev B
by S. Wen

Texas Instruments

Title		Rev
	3 cell 4A off 15Vin BQ24745, BQ3060	
Size	Number	
C	PMP5214	A
Date	Drawn by	
June 14, 2010	Josh Mandelcorn	
Filename	Sheet	
PMP5214_revA.sch	3 of 3	

Monitoring

Engineer Josh Mandelcorn

Filename PMP5214_revA.sch

Sheet 3 of 3

17:18:23

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