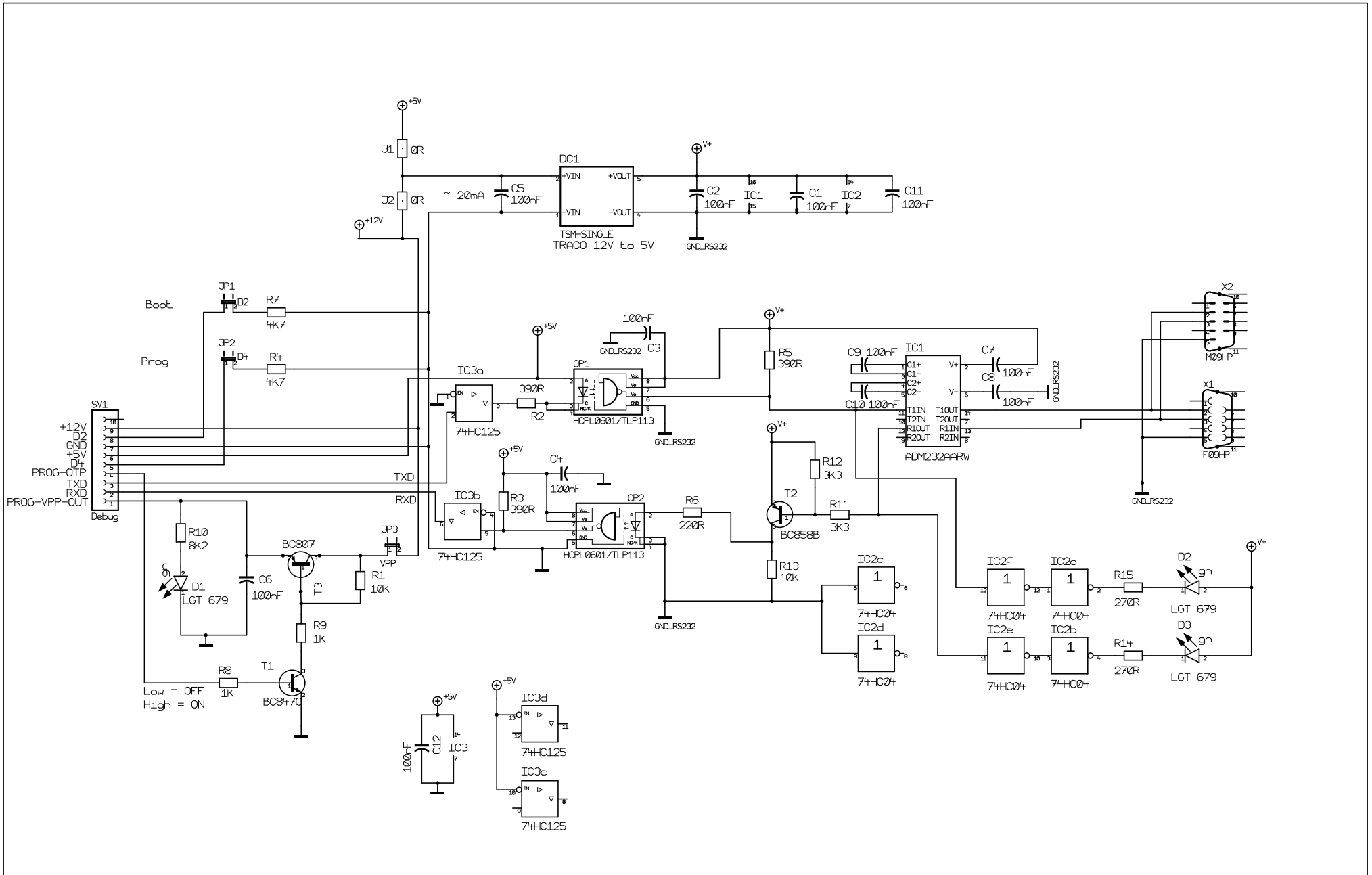
+5V
J1 0R
J2 0R
+12V
~ 20mA
C5 100nF
DC1
+VIN
-VIN
+VOUT
-VOUT
TSM-SINGLE
TRACO 12V to 5V
V+
C2 100nF
IC1
C1 100nF
IC2
C11 100nF
GND_RS232
Boot
Prog
JP1
D2
R7 4K7
JP2
D+
R4 4K7
SV1
+12V
D2
GND
+5V
D+
PROG-OTP
TXD
RXD
PROG-VPP-OUT
Debug
IC3a
74HC125
390R
R2
OP1
HCPL0601/TLP113
100nF
C3
R5 390R
C9 100nF
C10 100nF
IC1
C1+
C1-
C2+
C2-
V+
V-
T1IN
T2IN
R1OUT
R2OUT
T1OUT
T2OUT
R1IN
R2IN
ADM232AARW
C7 100nF
C8 100nF
X2
M09HP
X1
F09HP
C4 100nF
IC3b
R3 390R
OP2
R6 220R
R12 3K3
T2
BC858B
R11 3K3
R13 10K
R10 8K2
D1
LGT 679
C6 100nF
BC807
T3
JP3
VPP
R1 10K
R9 1K
R8 1K
T1
BC847C
Low = OFF
High = ON
IC2c
IC2d
74HC04
IC2f
IC2e
IC2a
IC2b
R15 270R
R14 270R
D2
D3
LGT 679
C12 100nF
IC3
IC3d
IC3c
max - speed = 200KB/s
(c) 2002 S. Hofmann
Maßstab 0.70%
Datei DEBUG
Zeichner S. Hofmann
Blatt
Änderung 02.10.03 13:26
Ausgabe 02.10.03 13:31
Titel Debug - Interface + Prog. OPTO - C16x
Firma Eppendorf Instr.
Projekt C16x - Debug - Opto