

ROM Configuration Register

The ROM Configuration Register is masked and is not accessible by the MCU. It contains fuse bits which enable the product configuration.

Table 113. ROM Configuration Register for AT8xC5122

7	6	5	4	3	2	1	0
	BLJRB				LB2	LB1	LB0
Bit Number	Bit Mnemonic	Description					
7	-	Reserved					
6	BLJRB	Bootloader Jump Rom Bit Set to configure User Code in ROM with a reset@0000h Clear to configure in CRAM/ROM (Bootloader mode with a reset@F800h)					
5 - 3	-	Reserved					
2 - 0	LB2:0	Program Lock Bits The program lock bits protects the on-chip program against software piracy. The AT8xC5122 products are delivered with the lowest protection level as detailed in the following table:					
		LB2	LB1	LB0	Security Level	Protection Description	
		1	1	1	1	No program lock features enabled.	
		1	0	1	2	Read test mode function is disabled. High pin count package MOVC instruction executed from external program memory are disabled from fetching code bytes from internal memory. \overline{EA} is sampled and latched on reset. External execution is enabled. Checksum control is enabled. Low pin count package Checksum control is enabled.	