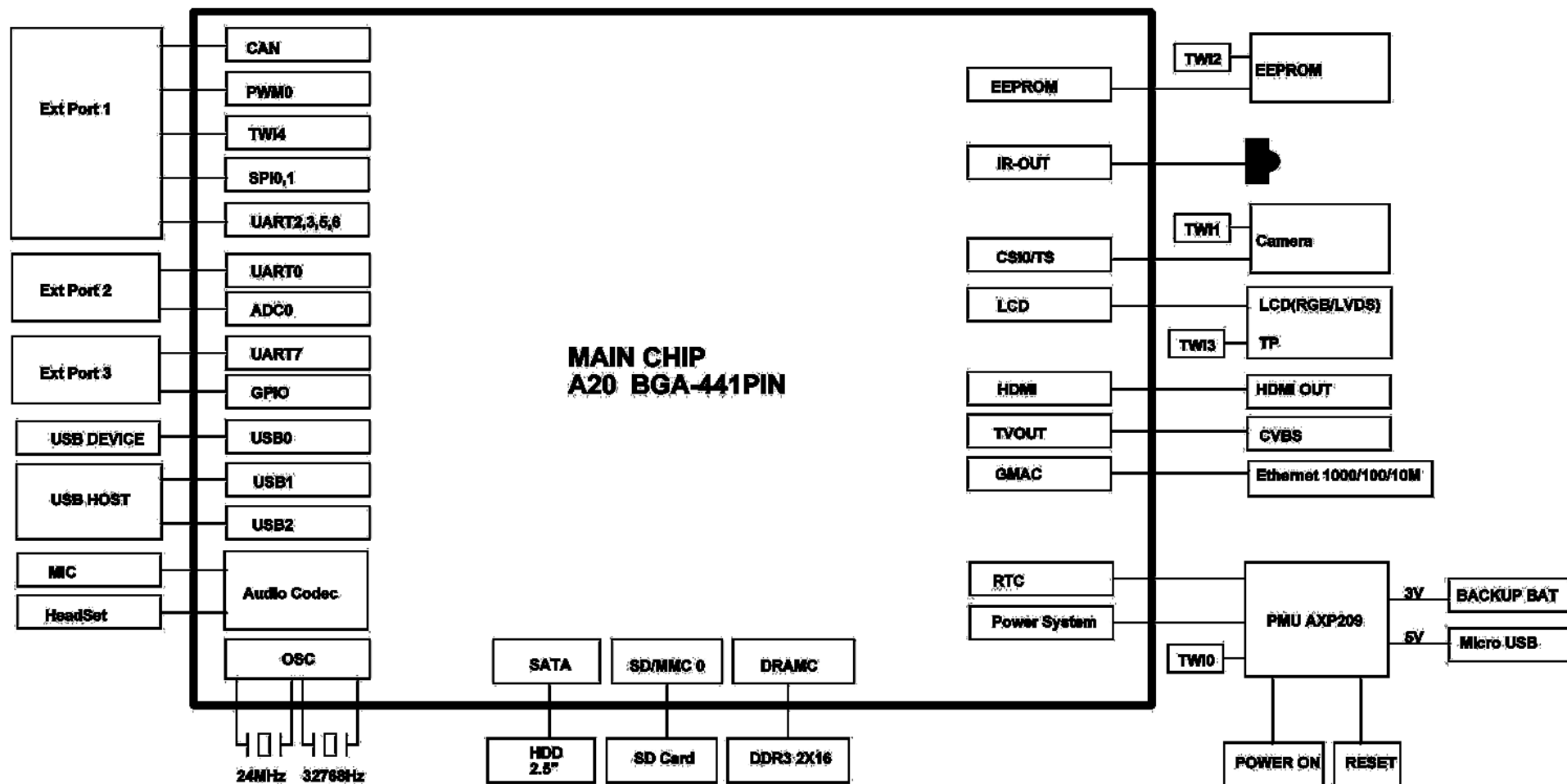


BLOCK



Differential pairs
Z0= 100ohm +/-5 ohm



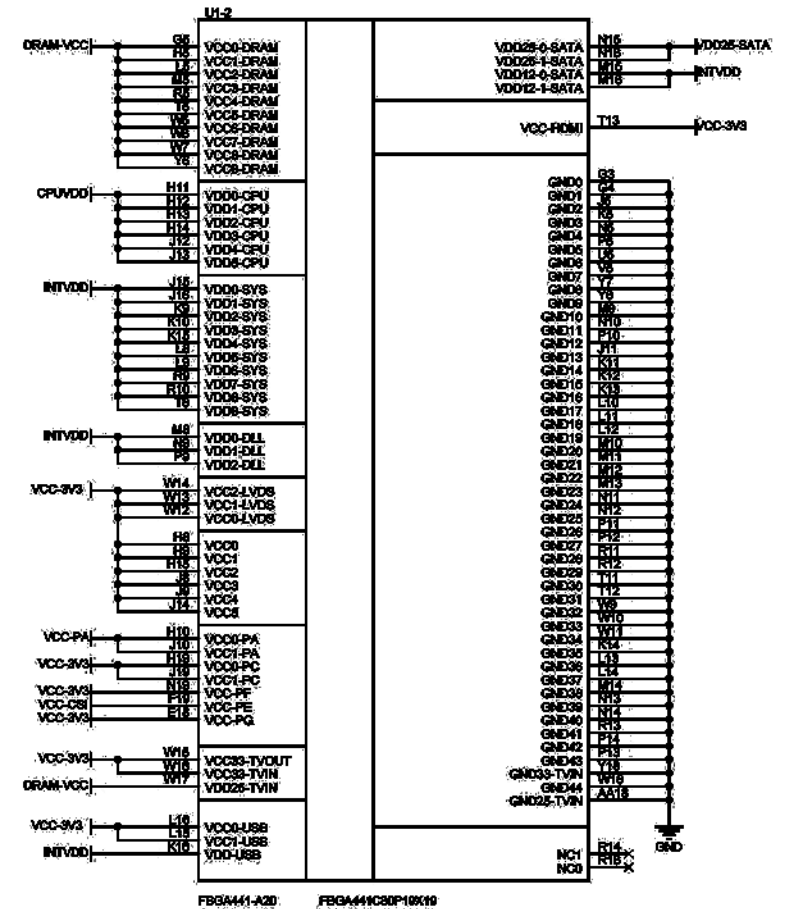
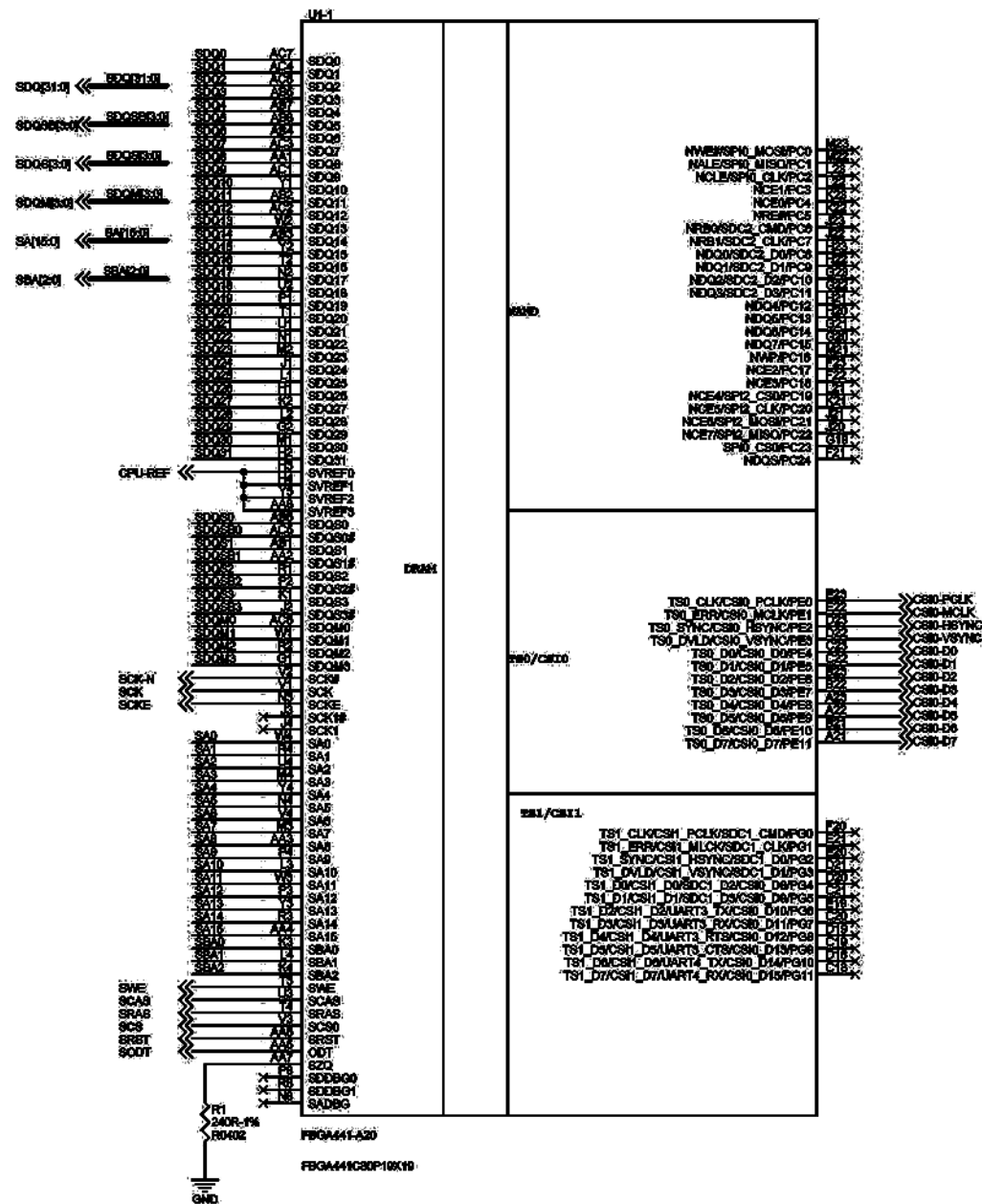
Differential pairs
Z0= 90 ohm +/-5 ohm



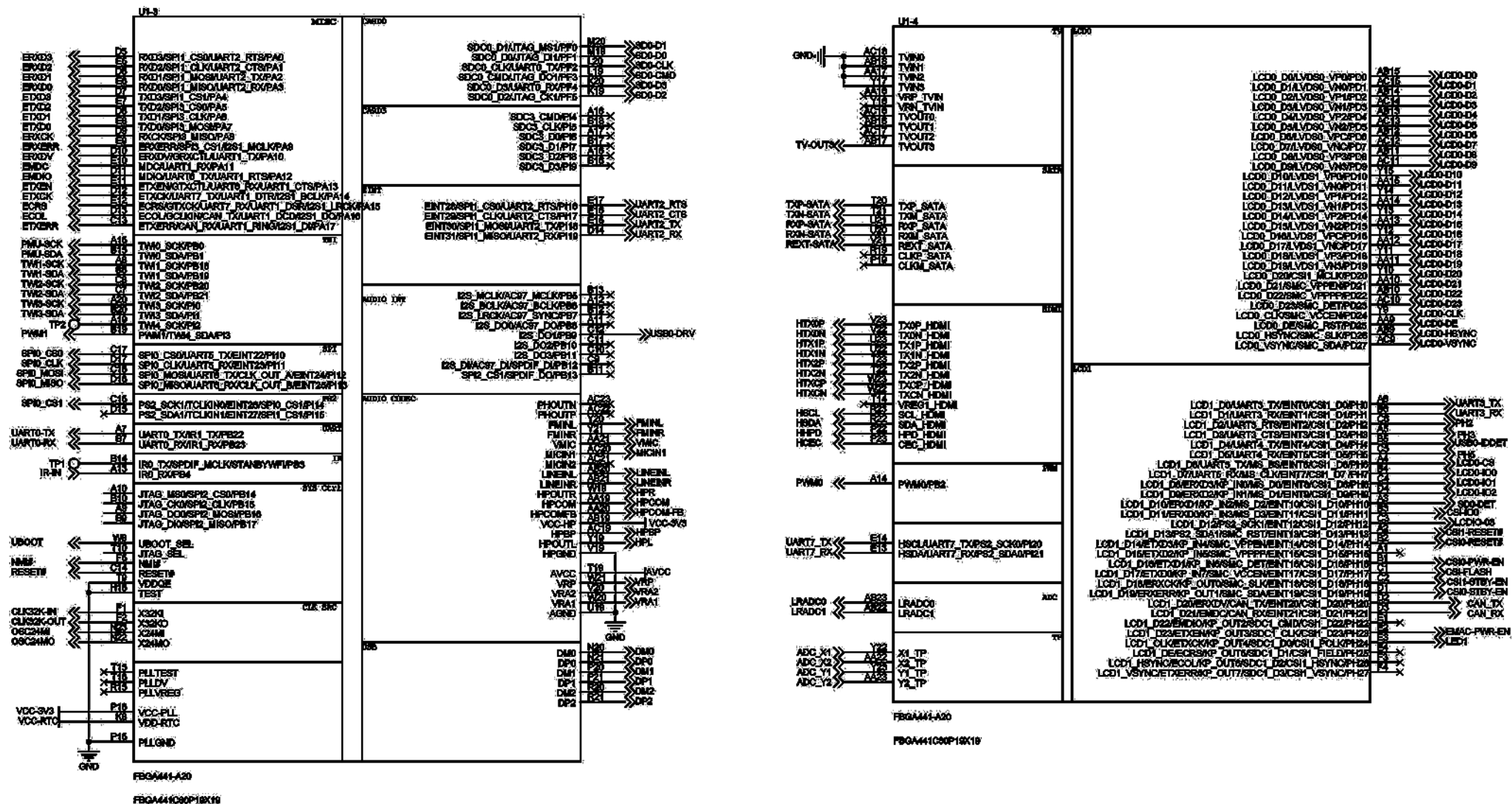
Z0= 50ohm +/-5 ohm



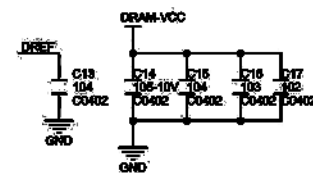
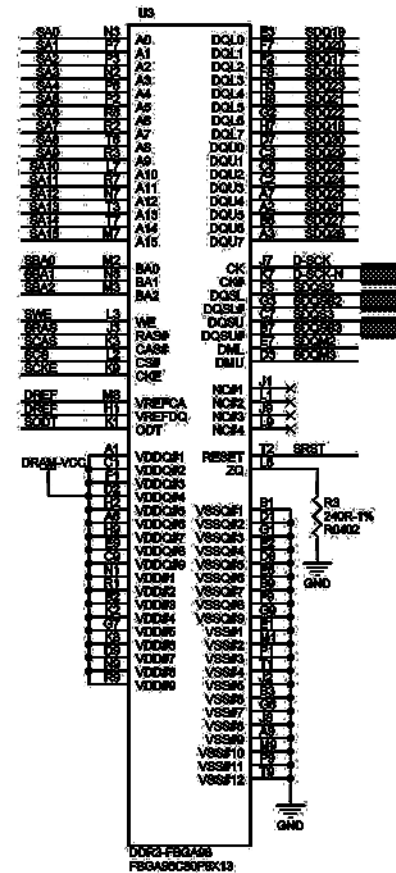
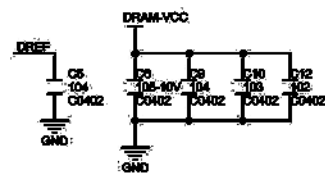
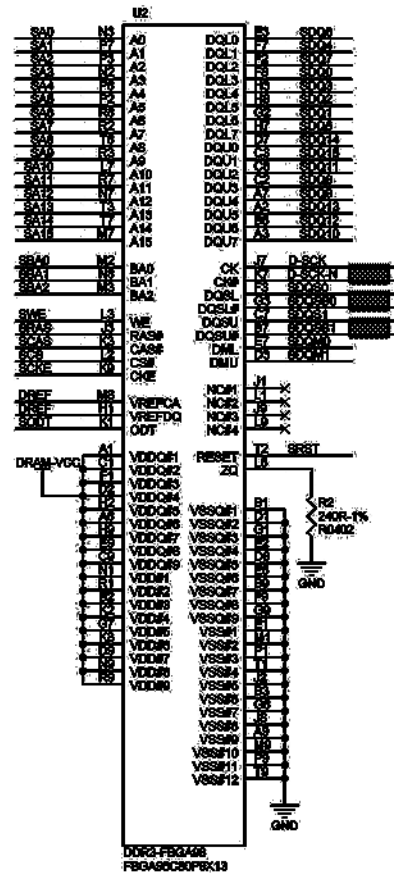
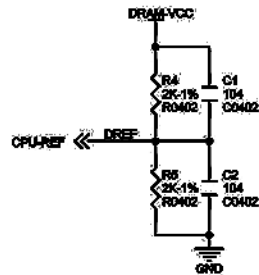
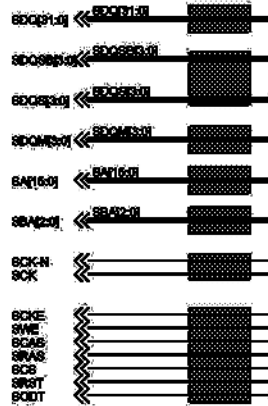
CPU1



CPU2

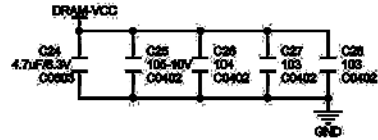


DDR3-16BITX2



BESIDE CPU

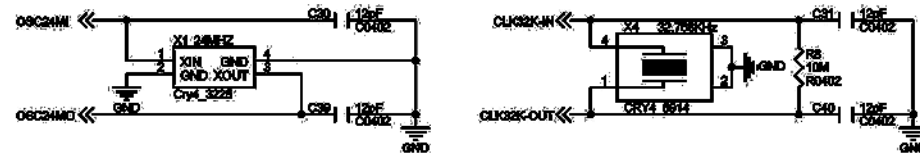
DRAM



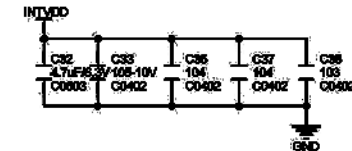
PLL



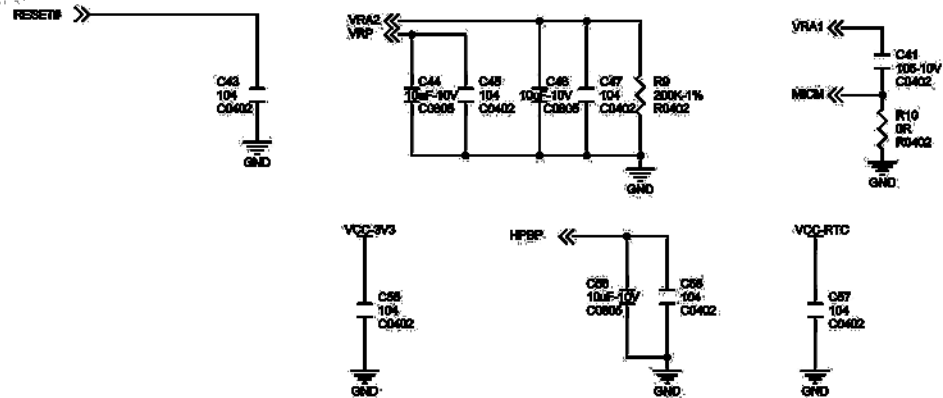
Crystal



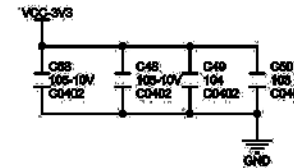
CORE



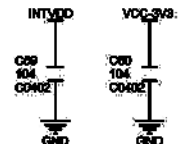
AUDIO&SYS



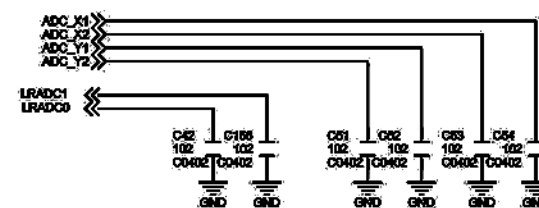
PIO-INTFACE



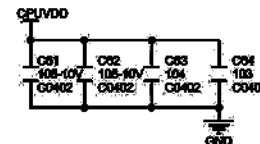
USB



ADC



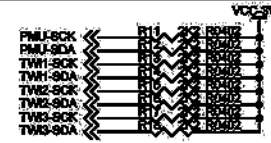
CPU&TV



SATA

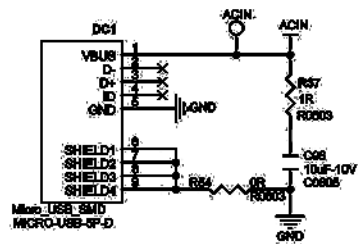


I2C-PULLUP

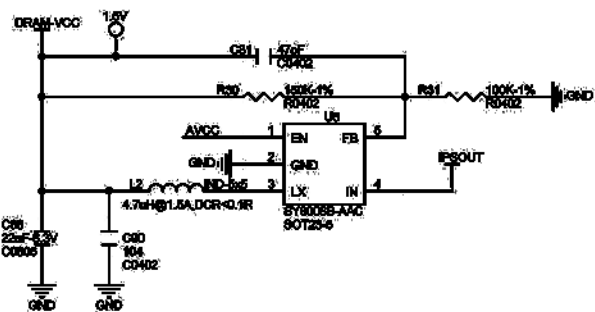


Power

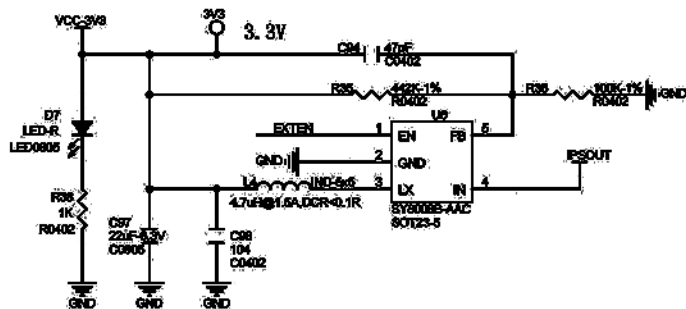
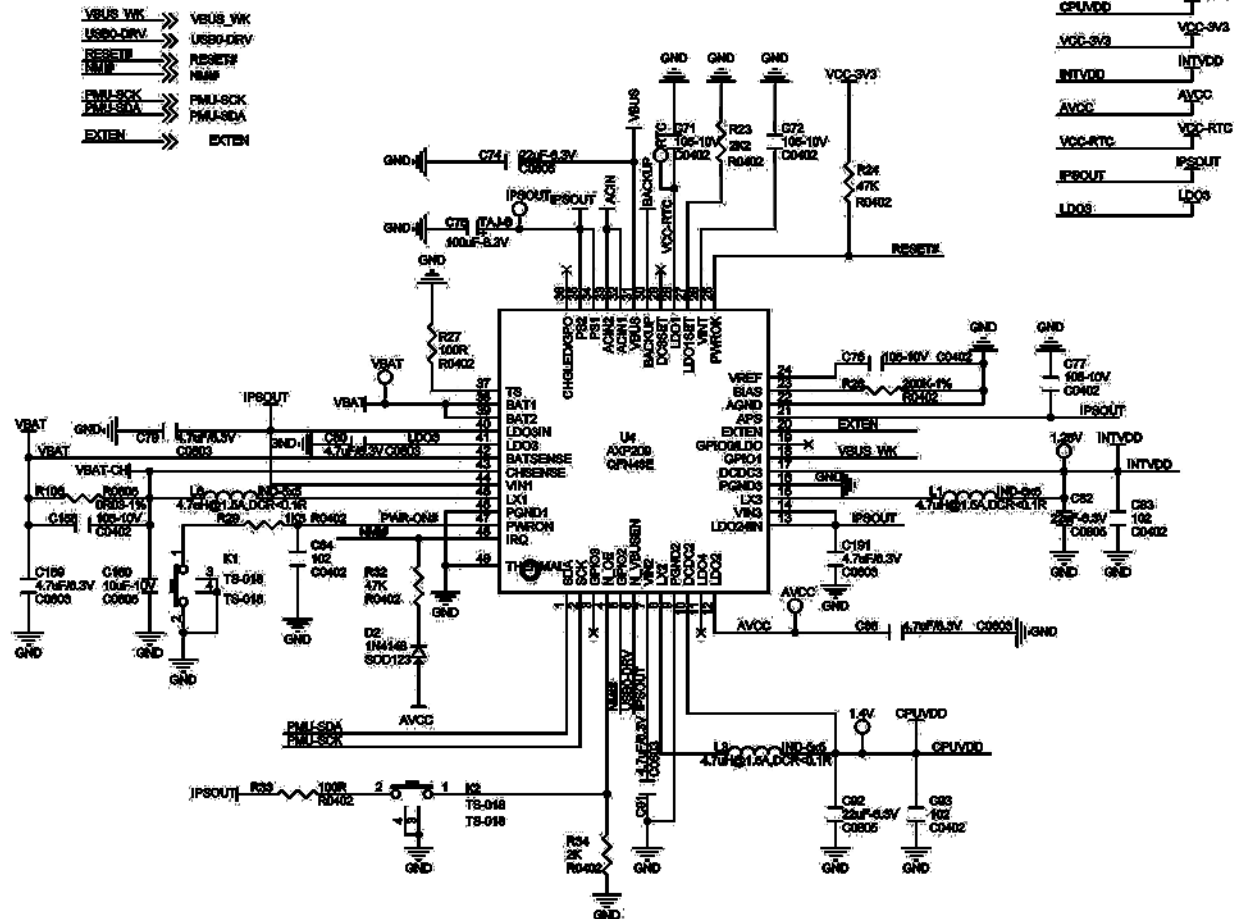
DC-IN



DRAM-VCC

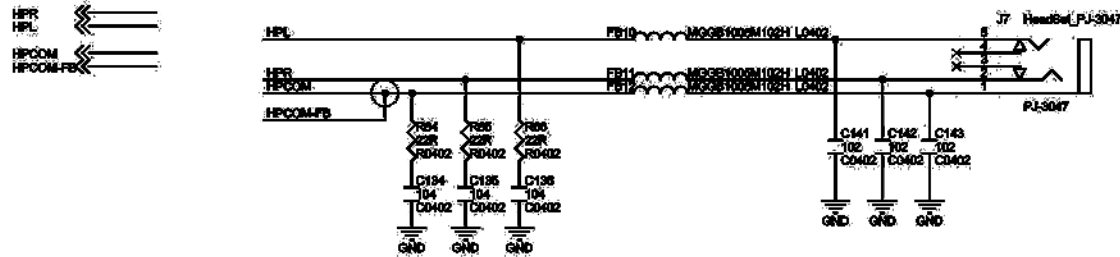


VCC-3V3

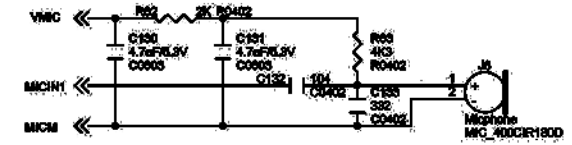
**PMU-AXP209**

AUDIO-LCD-EEPROM

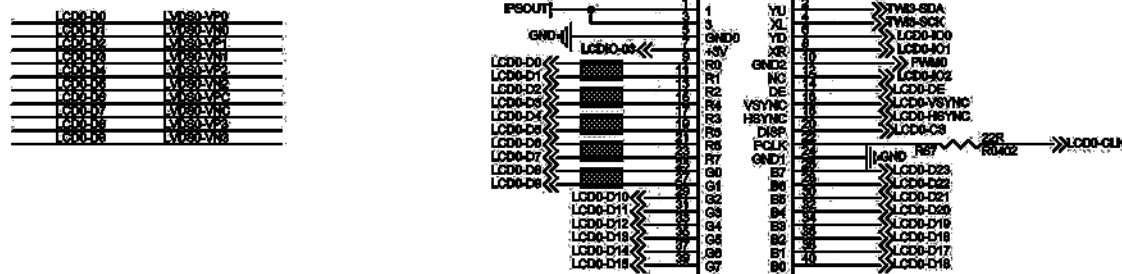
HeadSet



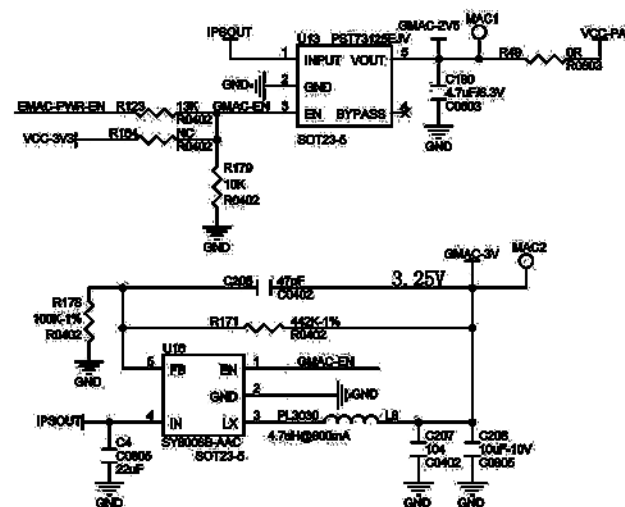
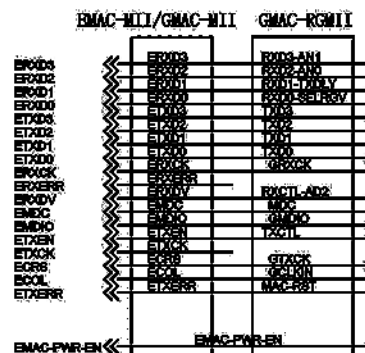
Microphone



LCD



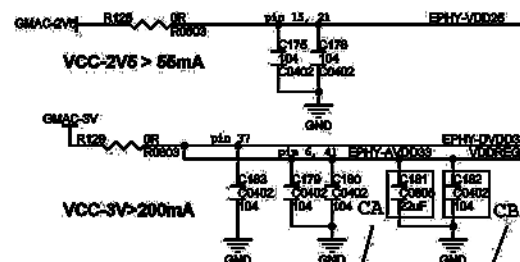
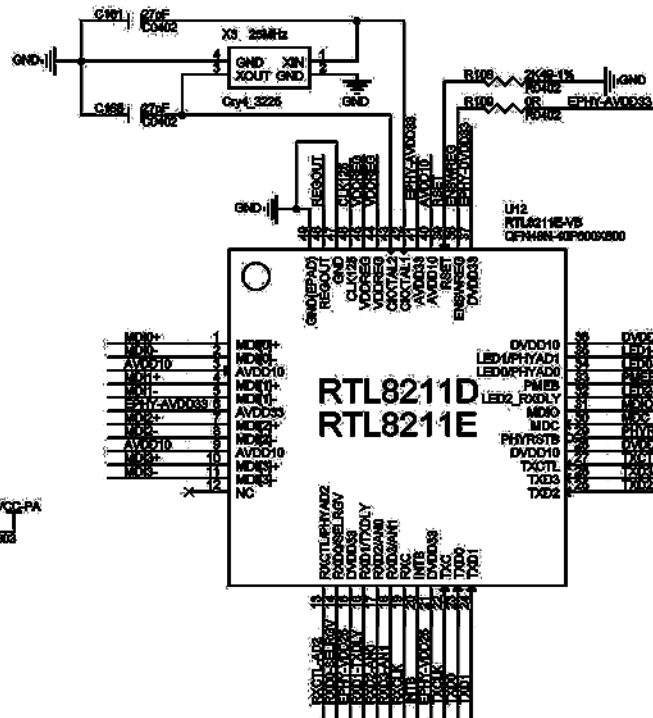
10/100/1000 RGMII Ethernet PHY



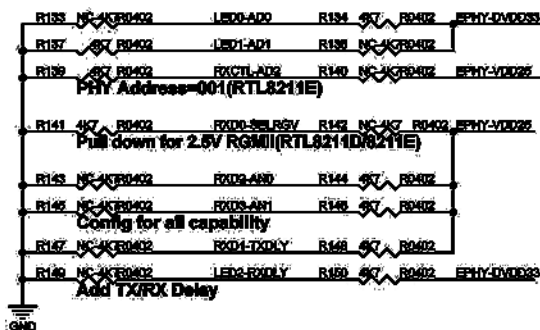
Place filter network close to CLK125.
Reserved for EMI



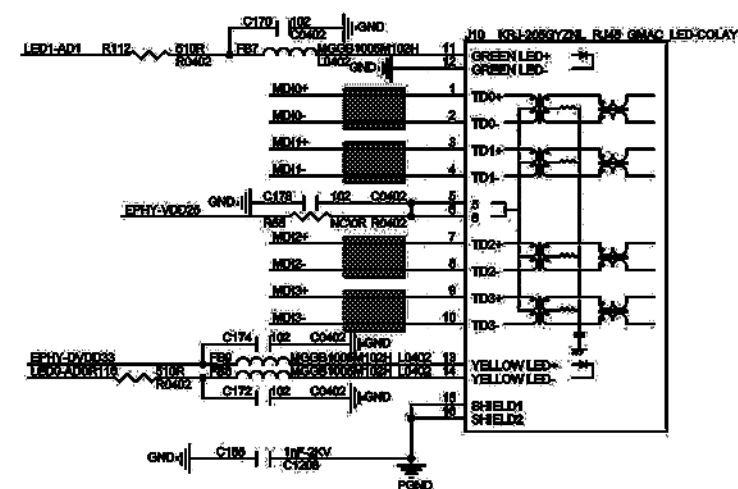
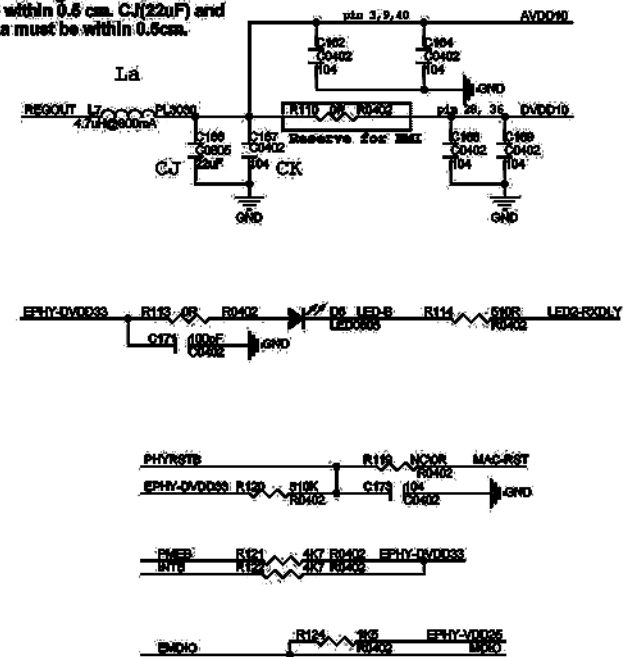
Place filter network close to RX_CLK
Reserved for EMI



Note 2: The Trace length from CA(22uF),CB(0.1uF) to Pin 44,45(VDDREG) must be within 0.5 cm. The trace width from AVDD33 to Pin 44,45 should >40mil.

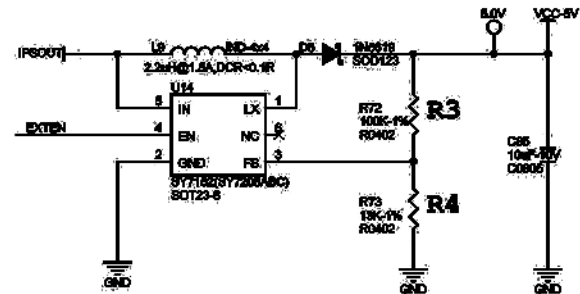
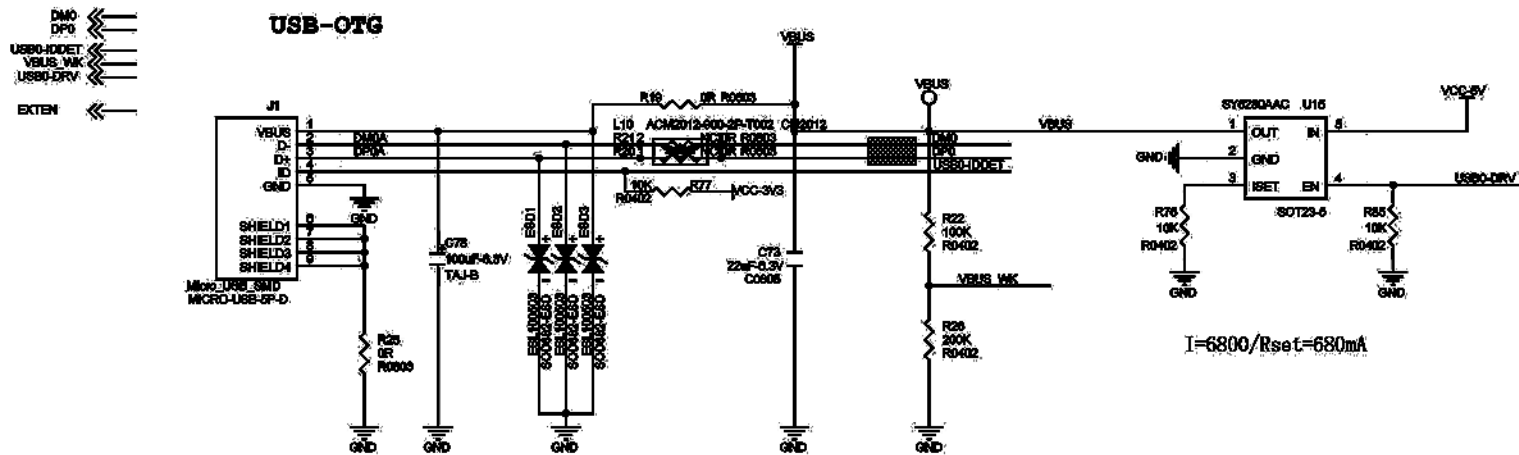


Note 1: The Trace length between La and PHY's Pin48 must be within 0.5 cm. C/J(22uF) and CK(0.1uF) to La must be within 0.5cm.



LED0: Blinking Transmitting or Receiving.
LED1: Link Up/Down.

USB_OTG



$$V_{out} = 0.6 * (1 + R3/R4)$$

A20	PIN	Use	Not use
HDMI	T13	VCC-3V3	VCC-3V3
SATA 2.5V	N15, N16	2.5V	DRAM-VCC(1.5V)
CSIO	F19 (VCC-PE)	VCC-2.8	VCC-2.8
CSI1	E18 (VCC-PG)	VCC-2.8	VCC-3V3
TVIN 2.5V	W17	2.5V	DRAM-VCC(1.5V)
TVIN 3.3V	W16	VCC-3V3	VCC-3V3
TVOUT 3.3V	W15	VCC-3V3	VCC-3V3
LVDS IO	W12, W13, W14	VCC-3V3	VCC-3V3
EMAC IO	J10, H10 (VCC-PA)	3.0V/2.5V	VCC-3V3
NAND IO	J19, H19 (VCC-PC)	VCC-3V3	VCC-3V3
SD IO	N19 (VCC-PF)	VCC-3V3	VCC-3V3
USB VDD	K16	INTVDD(1.2V)	VCC-3V3
USB VCC	L15, L16	VCC-3V3	VCC-3V3