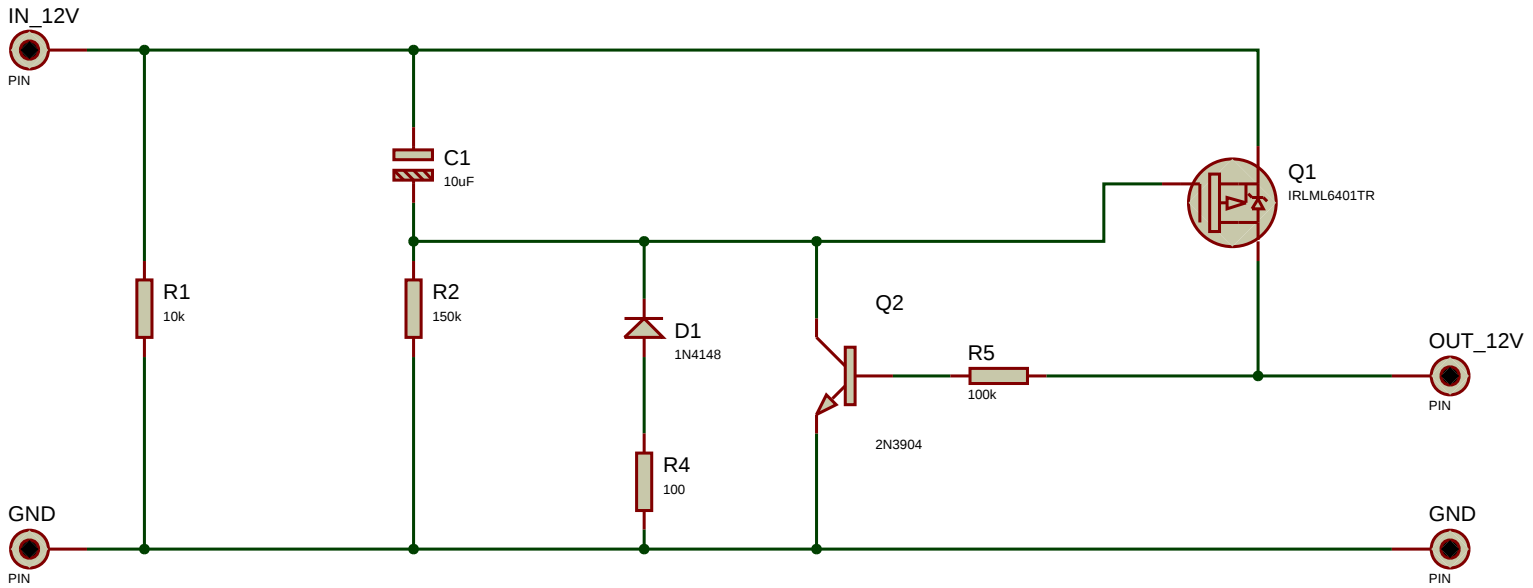




FILE NAME:	Delay	DATE:	14.11.2014
DESIGN TITLE:	12V Signal Delay 300ms	PAGE:	1 of 1
PATH:	C:\Users\Mirki\Documents\Delay	TIME:	02:30:34
BY: Buelent	REV: 0.1		