
MCP23S17 Rev. A Silicon Errata

The MCP23S17 parts you have received conform functionally to the Device Data Sheet (DS21952B), except for the anomalies described below.

All of the issues listed here will be addressed in future revisions of the MCP23S17 silicon.

1. Module: SPI Module

When IOCON.HAEN = 0 (hardware addressing disabled): If the A2 pin is high, then the device must be addressed as A2, A1, A0, = 1xx (i.e., OP CODE = b"0100 1XX').

Work around

- None

Clarifications/Corrections to the Data Sheet:

In the Device Data Sheet (DS21952B), the following clarifications and corrections should be noted.

- None

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APPENDIX A: REVISION HISTORY

Revision A (March 2007)

- Initial release of this document, silicon issue (SPI Module).

APPENDIX B: SILICON REVISION HISTORY

The following table and package marking information indicates how to determine the revision of the DSTEMP device. The revision information can be determined by the Year and Week Code of the manufacturer printed on the device.

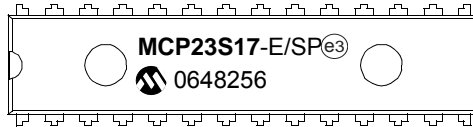
TABLE B-1: SILICON REVISION/DEVICE MARKING

Silicon Revision	YYWWNNN		Comments
	Start Date	End Date	
Rev A2	XXXXNNN	XXXXNNN	In Production
Rev A0	—	XXXXNNN	

Legend: "N" is any alphanumeric character.

Package Marking Information

28-Lead PDIP (Skinny DIP)



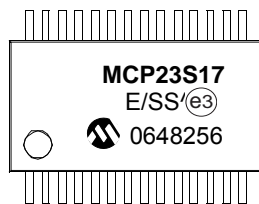
28-Lead QFN



28-Lead SOIC



28-Lead SSOP



Legend:	XX...X	Customer-specific information
	Y	Year code (last digit of calendar year)
	YY	Year code (last 2 digits of calendar year)
	WW	Week code (week of January 1 is week '01')
	NNN	Alphanumeric traceability code
	(e3)	Pb-free JEDEC designator for Matte Tin (Sn)
	*	This package is Pb-free. The Pb-free JEDEC designator (e3) can be found on the outer packaging for this package.

Note: In the event the full Microchip part number cannot be marked on one line, it will be carried over to the next line, thus limiting the number of available characters for customer-specific information.

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NOTES:

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