CECC Ceramic Chips

FEATURES

High Reliability CECC Ceramic Chips Capacitors for Military & Avionics applications

HOW TO ORDER

AN	13	Z	E	0104	J	T3
AVX Style	**Size**	**Class**	**Voltage**	**Capacitance**	**Tolerance**	**Packaging**
AN = Nickel Barrier + SnPb finish AC = Silver Palladium	12 = 0805 20 = 1206 13 = 1210 14 = 1812 15 = 2220	C = NP0 Z = X7R	D = 50/63 E = 100 F = 200	EIA code on 3 or 4 digits	NP0 F = ±1% G = ±2% J = ±5% K = ±10% X7R J = ±5% K = ±10% M = ±20%	SUFFIX Burn-in 100% 168H = T5 Burn-in 100% 48H = T3 No burn-in = --

QUALIFIED VS CECC 32101-801

Class: NP0 + X7R (2C1/BX available on request)

Sizes: 0805, 1206, 1210, 1812, 2220 (0603 qualification pending)

Voltages: 50, 100, 200 (500V on request)

Terminations: Silver Palladium or Nickel barrier + tin lead finish

CAPACITANCE vs VOLTAGE TABLE

Size	NP0*			X7R**		
	50V	100V	200V	50V	100V	200V
0805	4.7 → 1500pF	4.7 → 1500pF	10 → 470pF	0.47 → 68nF	0.47 → 39nF	0.33 → 18nF
1206	10 → 4700pF	10 → 4700pF	10 → 1500pF	1 → 180nF	1 → 100nF	0.1 → 39nF
1210	10 → 8200pF	10 → 8200pF	22 → 2700pF	10 → 330nF	4.7 → 220nF	0.47 → 100nF
1812	0.1 → 18nF	0.1 → 18nF	0.47 → 5.6nF	47 → 680nF	10 → 470nF	1 → 180nF
2220	0.47 → 39nF	0.47 → 39nF	0.1 → 12nF	0.1 → 1.5µF	0.047 → 1µF	4.7 → 390nF

* NP0 Class (range available with tolerance: 1, 2, 5, 10%)

** X7R Class (range available with tolerance: 5, 10, 20%)

Available Reliability Levels:

Suffix: -- = qualified following CECC 32101-801 [no burn-in]
Suffix: T3 = according to CECC 32100-002 or 003; Established reliability level (Equivalent to MIL-R) [100% burn-in: 48H @ 2 x Ur]
Suffix: T5 = according to CECC 32100-002 or 003; Established reliability level (Equivalent to MIL-S) [100% burn-in: 168H @ 2 x Ur]