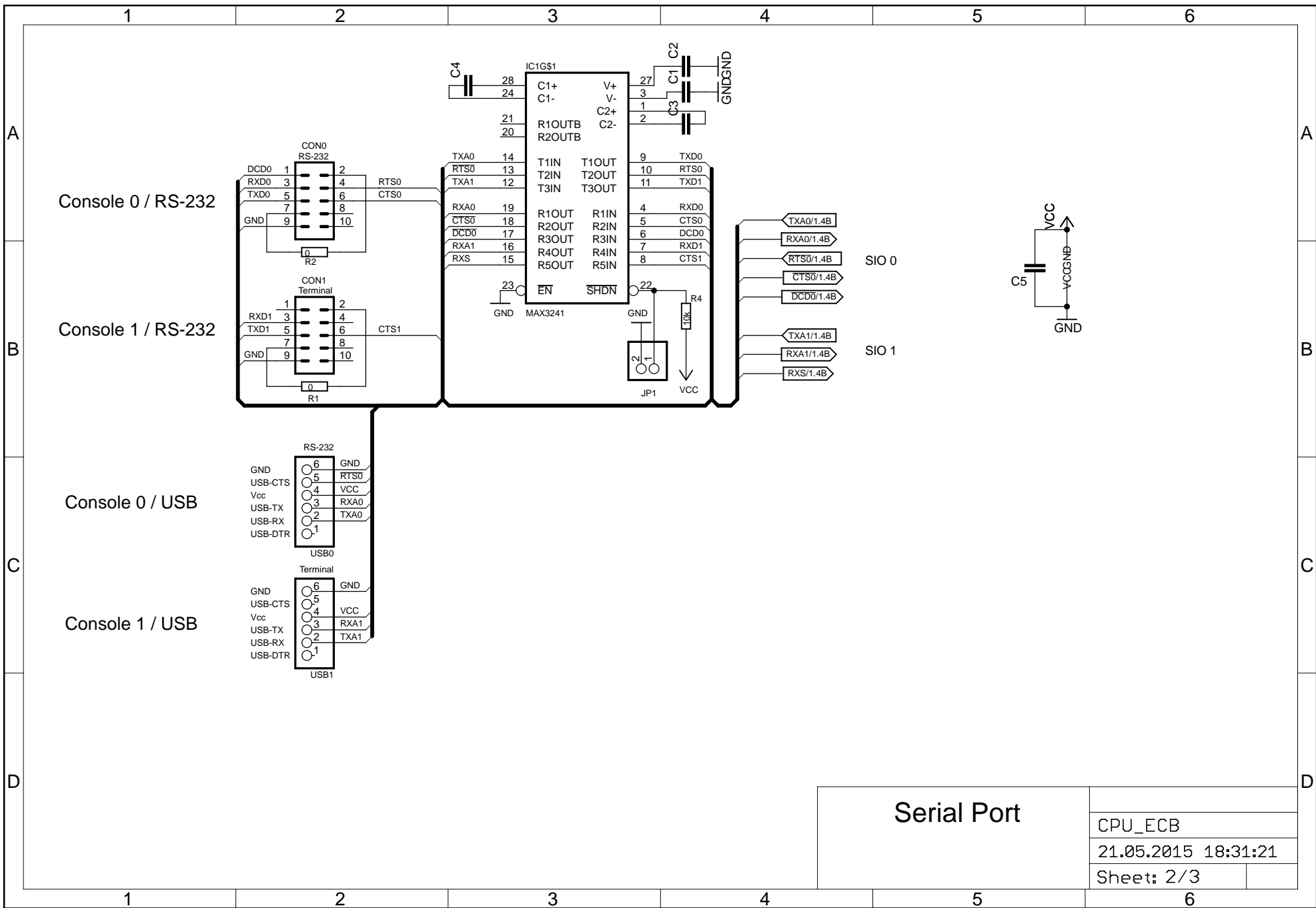
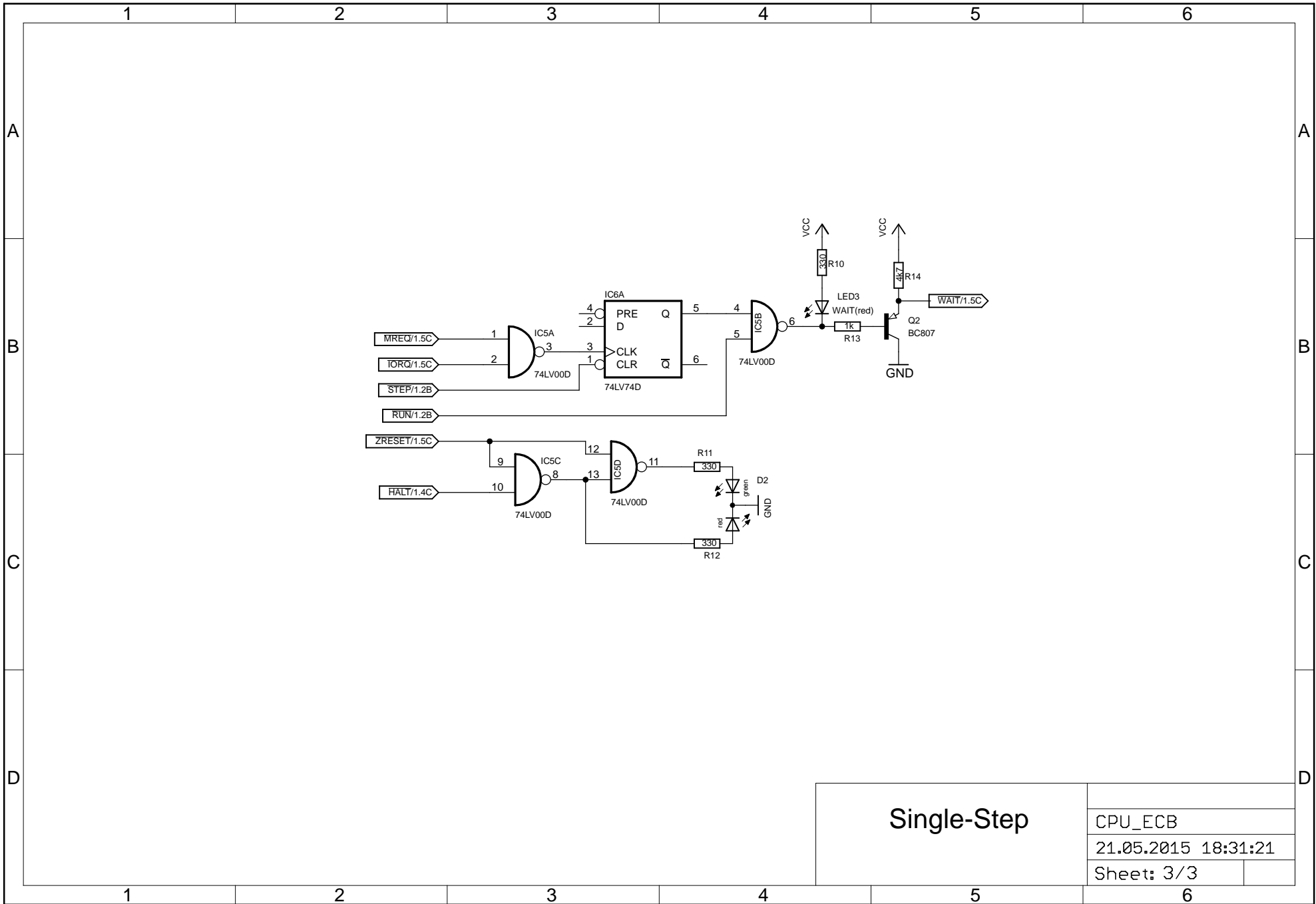


A1	C1
A2	C2
A3	C3
A4	C4
A5	C5
A6	C6
A7	C7
A8	C8
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A10	C10
A11	C11
A12	C12
A13	C13
A14	C14
A15	C15
A16	C16
A17	C17
A18	C18
A19	C19
A20	C20
A21	C21
A22	C22
A23	C23
A24	C24
A25	C25
A26	C26
A27	C27
A28	C28
A29	C29
A30	C30
A31	C31
A32	C32





<b>Single-Step</b>	CPU_ECB
	21.05.2015 18:31:21
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