

2 January 1980

ERRATA

to

FET Design Catalog

November 1979

Pages 4-8, 4-9, 4-10, 4-13, 4-14, 4-16, 4-38, 4-39, 4-43

The pin-out for suffix -18 part numbers, N-channel JFETs with offset leads to form TO-18/TO-106 pin circle should be amended to read D-S-G, as follows:



Bottom View
(Standard)



Bottom View
(-18)



Pages 4-12, 4-17

The pin-out for suffix -18 part numbers P-channel JFETs with offset leads to form TO-18/TO-106 pin circle should be amended to read D-G-S, as follows:



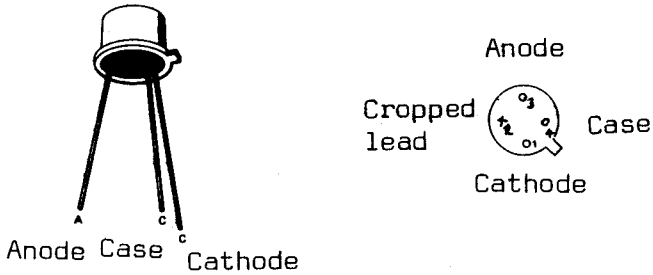
Bottom View
(Standard)



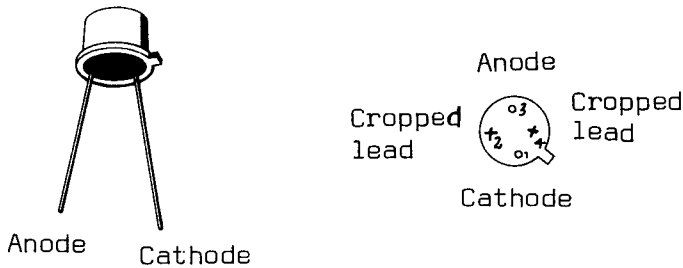
Bottom View
(-18)



Pin-out for PAD 1, 2 and 5.
Amend illustration to read as follows:



Pin-out for PAD 10, 20, 50 and 100.
Insert the following:



- X Pin 2 cropped on TO-18 package
- Pins 2 and 4 cropped on TO-72 package

