

PSpice Schematics - [PI Stellglied 01 p.1 (current)]

File Edit Draw Navigate View Options Analysis Tools Markers Window Help

vdc

None

2 1

B B

A A

11.19, 5.06

Start PS... PS... PS... PS... PS... PI... EI... M... DE Arbeitsplatz 22:51

The image shows a PSpice Schematics window titled "PSpice Schematics - [PI Stellglied 01 p.1 (current)]". The interface includes a menu bar (File, Edit, Draw, Navigate, View, Options, Analysis, Tools, Markers, Window, Help) and a toolbar with various icons. The main workspace displays a circuit diagram on a grid. The circuit consists of two operational amplifiers (op-amps) configured as integrators. The top op-amp has a feedback capacitor labeled "C1K" and a resistor labeled "R1K". The bottom op-amp has a feedback resistor labeled "R1K" and a resistor labeled "R1K". The circuit is connected to a voltage source labeled "V1" and a current source labeled "I1". The schematic is labeled with "2" and "1" at the top, and "A" and "B" on the sides. A command line at the bottom shows "Cmd:". The Windows taskbar at the bottom displays the Start button, several instances of "PS...", "PI...", "EI...", "M...", "DE Arbeitsplatz", and the time "22:51".