



**Venable Instruments** introduces a new family of "High Frequency" test instrumentations. The Venable **Model 3215** Frequency Response Analyzer combines the latest analog and digital technology with advanced digital signal processing to provide versatile test and analysis functions. This single comprehensive unit performs many sophisticated test functions. Boasting features as a bandwidth of .01Hz to 15MHz, 2-channels, 10 Vpk input and using Venable's renowned v4.0 software, the 3215 is your most complete, accurate and easy to use system for phase/gain and impedance measurements. Operating through IEEE-488 bus, the Venable system imports/exports to MATLAB<sup>TM</sup> and Excel<sup>TM</sup> and saves Bode/Impedance Plots in .jpeg or .ven file format.

**Venable Instruments** incorporates the latest FPGA technology to unleash the power of a dedicated DSP, performing all data acquisition and analysis functions. A separate processor handles all the interface functions. Optimum performance derives from the use of distributed RAM within the FPGA, which enables asynchronous buffering between the processors and the analog hardware. The 3215 performs simultaneous analysis on both input channels, reliably capturing all data. This truly versatile instrument, complete with its wide range of applications is available to you packaged in a tough, yet portable case, weighing just 10 pounds. Engineers and scientists now have the speed and technology for production, R&D Labs, academia, or field operations bundled into one compact and affordable system, the Venable **Model 3215**.

Venable, a pioneer in stability analysis for 26 years, continues to support the test and measurement customers with cutting edge instruments and analysis software. The **Model 3215** brings an economical option to Venable's lineup of top quality instrumentation.



## Description

## Venable 3215

System Frequency Range:	.01Hz to 15MHz
Generator Amplitude	1mVac to 10Vac
DC Bias	±10Vpk
Generator Isolation	Referenced to chassis ground
Generator Modes	Single frequency, sine sweep, sweep with manual step control, amplitude servo
Output Amplitude	Dynamically adjust output to maintain a constant input level
Compression	2, Ch. 1: Differential or Single-ended (Ground referenced), Ch. 2: Single-ended (Ground referenced)
Input Channels	Narrowband DFT
Meas. Technique	4 Selectable Bandwidths 100 mHz, 400 mHz, 3 Hz, 20 Hz
Bandwidth Resolution	AC and DC
Input coupling	1mV to 10V pk in 9 ranges
Input Range	±.05dB, ±.25° typical @ 10kHz
Input Accuracy	±10Vpk
Max. Input	±30Vpk
Max Input Withstand Voltage	LED indicator
Overrange alarms	Implements IEEE-488 standard interface for Windows in PCMCIA, PCI, USB
PC Interface	±12Vdc/50mA for accessories
Auxiliary Power	90 to 264Vac, 48 to 62Hz, 30VA
Power Requirements	Venable v4.0 Software Interface
Display	Each point is plotted as acquired
Real time display update	Gain, phase, angle, real, imaginary, R, L, C, Z
Data Analysis	Venable v4.0 software for Win: 95/98/NT/2000/ME/XP
Operating System	

# Model 3225



Venable Instruments introduces a new family of "High Frequency" test instrumentations. The Venable **Model 3225** Frequency Response Analyzer combines the latest analog and digital technology with advanced digital signal processing to provide versatile test and analysis functions. This single comprehensive unit performs many sophisticated test functions. Boasting features as a bandwidth of .01Hz to 25MHz, 2-channels, 10 Vpk input and using Venable's renowned v4.0 software, the 3225 is your most complete, accurate and easy to use system for phase/gain and impedance measurements. Operating through IEEE-488 bus, the Venable system imports/exports to MATLAB™ and Excel™ and saves Bode/Impedance Plots in .jpeg or .ven file format.

Venable Instruments incorporates the latest FPGA technology to unleash the power of a dedicated DSP, performing all data acquisition and analysis functions. A separate processor handles all the interface functions. Optimum performance derives from the use of distributed RAM within the FPGA, which enables asynchronous buffering between the processors and the analog hardware. The 3225 performs simultaneous analysis on both input channels, reliably capturing all data. This truly versatile instrument, complete with its wide range of applications is available to you packaged in a tough, yet portable case, weighing just 10 pounds. Engineers and scientists now have the speed and technology for production, R&D Labs, academia, or field operations bundled into one compact and affordable system, the Venable **Model 3225**.

Venable, a pioneer in stability analysis for 26 years, continues to support the test and measurement customers with cutting edge instruments and analysis software. The **Model 3225** brings an economical option to Venable's lineup of top quality instrumentation.



## Description

System Frequency Range:  
Generator Amplitude  
DC Bias  
Generator Isolation  
Generator Modes

Output Amplitude  
Compression  
Input Channels

Meas. Technique  
Bandwidth Resolution

Input coupling  
Input Range  
Input Accuracy  
Max. Input  
Max Input Withstand Voltage  
Overrange alarms  
PC Interface

Auxiliary Power  
Power Requirements  
Display  
Real time display update  
Data Analysis

Operating System

## Venable 3225

.01Hz to 25MHz  
1mVac to 10Vac  
±10Vpk  
Referenced to chassis ground  
Single frequency, sine sweep, sweep with manual step control, amplitude servo  
Dynamically adjust output to maintain a constant input level  
2, Ch. 1: Differential or Single-ended (Ground referenced) , Ch. 2: Single-ended (Ground referenced)  
Narrowband DFT  
4 Selectable Bandwidths and DC  
100 mHz, 400 mHz, 3 Hz, 20 Hz  
AC and DC  
1mV to 10V pk in 9 ranges  
±.05dB, ±.25° typical @ 10kHz  
±10Vpk  
±30Vpk  
LED indicator  
Implements IEEE-488 standard interface for Windows in PCMCIA, PCI, USB  
±12Vdc/50mA for accessories  
90 to 264Vac, 48 to 62Hz, 30VA  
Venable v4.0 Software Interface  
Each point is plotted as acquired  
Gain, phase, angle, real, imaginary, R, L, C, Z  
Venable v4.0 software for Win: 95/98/NT/2000/ME/XP