

Unbenannt

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#include <p18f2550.h>

#pragma config PLLDIV = 5           // need 5 for 20MHz xtal
#pragma config CPUDIV = OSC2_PLL3  // divide by 3 to make 40KHz possible
#pragma config FOSC = INTOSCIO_EC  // internal oscillator + RA6 enable
#pragma config WDT = OFF           // no watchdog
#pragma config LVP = OFF           // LVP MUST be off
#pragma config BOR = OFF           // no brown out reset
#pragma config MCLRE = ON          // MCLR in use (so 10k to +5v essential)
#pragma config PWRT = ON           // power up timer on
#pragma config PBADEN = OFF        // disable PORTB a/d converter

void main(void)
{
    UCONbits.USBEN = 0; //Disable USB => RC4 + RC5 output
    UCFGbits.UTRDIS = 1; //Disable USB => RC4 + RC5 output

    TRISA = 0x00;
    do{
        PORTA = (PORTCbits.RC5) ? 0xFF : 0x00;
    }while(1);
}
```