

# BF246, BF247 N-CHANNEL EPITAXIAL PLANAR SILICON FIELD EFFECT TRANSISTOR

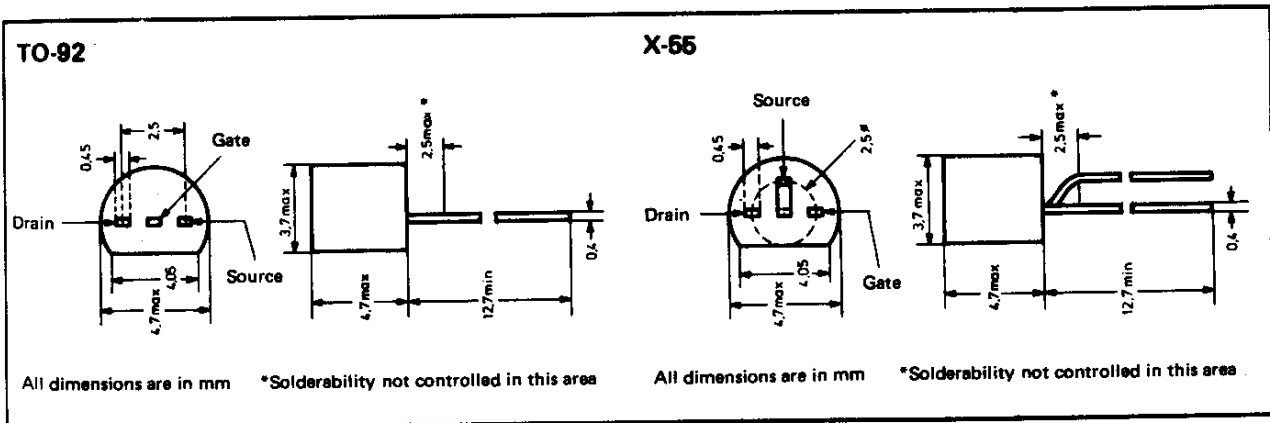
VLB n° 88 - June 1973

- VHF-Amplifiers and Mixer
- Gate-Circuits for Wide-Band Amplifiers with Low Input Resistance and Small Feedback
- $f_g = 450$  MHz typ
- $|Y_{21s}| = 17$  mS typ
- $C_{12s} = 3.5$  pF typ

## description

These components are tested according to the appropriate test method of MIL-STD-750. By special agreement, they can also be tested additionally to MIL or DIN specifications.

## mechanical data



## absolute maximum ratings at 25°C free air temperature (unless otherwise noted)

Drain-Gate Voltage	25 V
Drain-Source Voltage	±25 V
Gate Current	10 mA
Continuous Device Dissipation at 25°C Free Air Temperature (See Note 1)	250 mW
Storage Temperature Range	-55°C to 150°C
Lead Temperature 1,6 mm from Case for 10 Seconds	260°C

NOTE: 1. Derate linearly to 150°C free air temperature at the rate of 2.0 mW/°C.

# BF246, BF247

## N-CHANNEL EPITAXIAL PLANAR SILICON

### FIELD EFFECT TRANSISTOR

electrical characteristics at 25° C free air temperature

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
$-V_{(BR)GSS}$ Gate Source Breakdown Voltage	$-I_G = 1 \mu A, V_{DS} = 0$	25			V
$-I_{GSS}$ Gate Reverse Current	$-V_{GS} = 15 V, V_{DS} = 0 V$			5	nA
$I_{DSS}$ Zero-Gate-Voltage Drain Current	$V_{DS} = 15 V, -V_{GS} = 0 V$ See Note 2	10		300	mA
$-V_{GS}$ Gate-Source Voltage	$V_{DS} = 15 V, I_D = 200 \mu A$	0.5		14.0	V
$-V_{GS(off)}$ Gate-Source Cutoff Voltage	$V_{DS} = 15 V, I_D = 10 nA$	0.6		14.5	V
$ Y_{21s} $ Small-Signal Common-Source Forward Transfer Admittance	$V_{DS} = 15 V, -V_{GS} = 0 V,$ $f = 1 KHz$	8	17		mS
$f_g$ Common-Source Bandwidth	$V_{DS} = 15 V, V_{GS} = 0 V$ See Note 3		450		MHz
$C_{12s}$ Common-Source Short-Circuit Reverse Transfer Capacitance	$V_{DS} = 15 V, I_D = 10 mA$ $f = 1 MHz$		3.5		pF
$C_{11s}$ Common-Source Short-Circuit Input Capacitance	$V_{DS} = 15 V, I_D = 10 mA$ $f = 1 MHz$		11		pF
$C_{22s}$ Common-Source Short-Circuit Output Capacitance	$V_{DS} = 15 V, I_D = 10 mA,$ $f = 1 MHz$		5		pF

On request following  $I_{DSS}/V_{GS}$ -Groups can be delivered

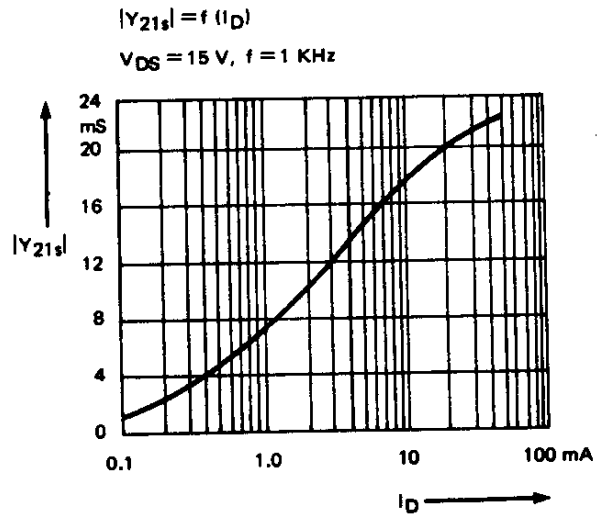
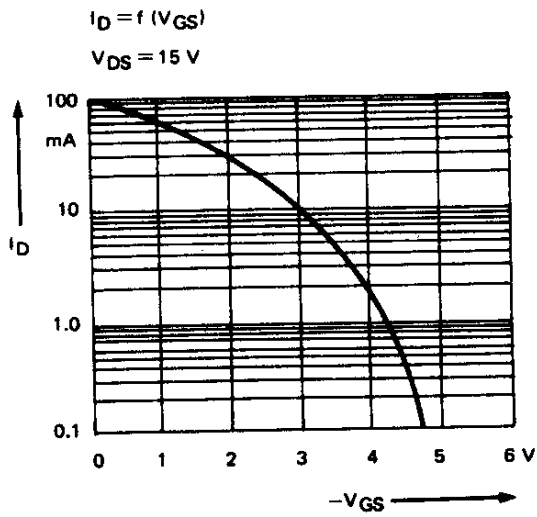
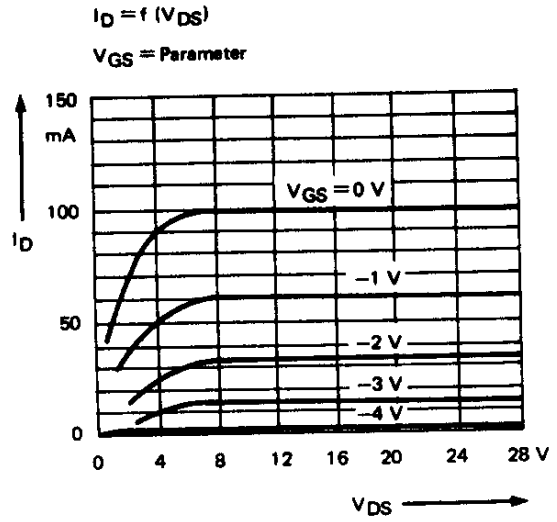
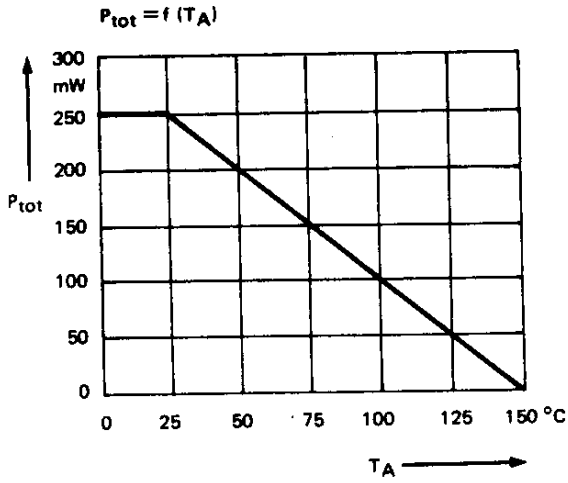
PARAMETER	TEST CONDITIONS	MIN	MAX	UNIT	
$I_{DSS}$ Zero-Gate-Voltage Drain Current	$V_{DS} = 15 V, V_{GS} = 0$ See Note 2	Group A	30	80	mA
		Group B	60	140	mA
		Group C	110	260	mA
$V_{GS}$ Gate Source Voltage	$V_{DS} = 15 V, I_D = 200 \mu A$	Group A	1.5	4.0	V
		Group B	3.0	7.0	V
		Group C	5.5	12.0	V

NOTES : 2. This value must be measured using pulse techniques ;  $t_p \leq 300 \mu s$ , duty cycle  $\leq 2\%$ .

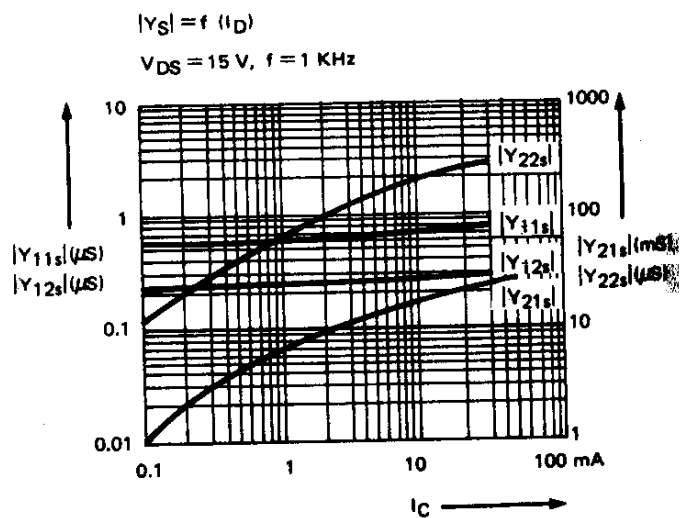
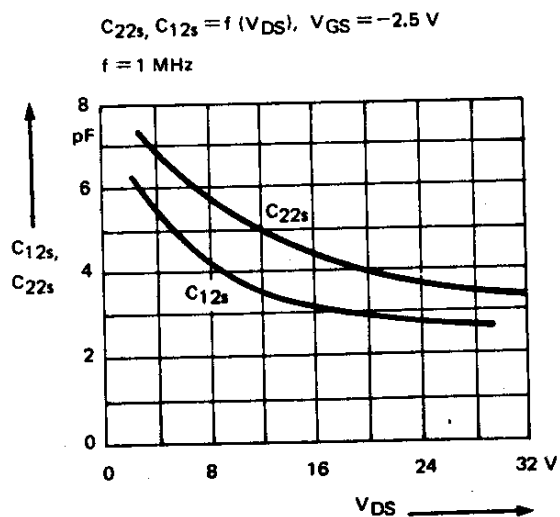
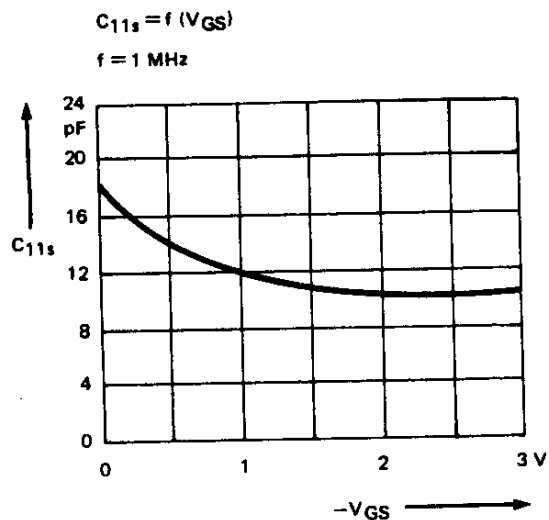
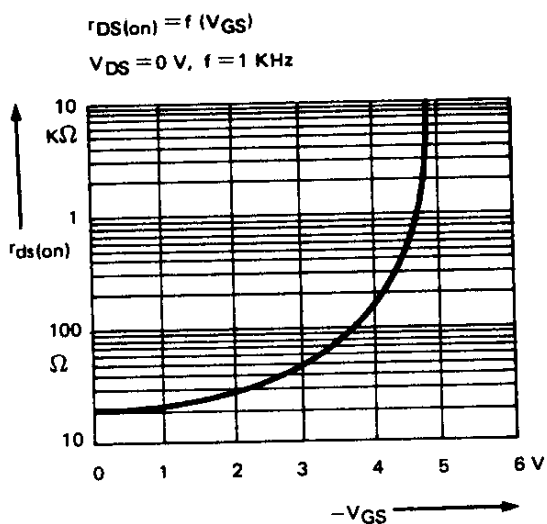
3. Frequency at which the real part of the Forward Transfer Admittance has fallen by 3 dB referred to the value at 1 KHz.

TEXAS INSTRUMENTS

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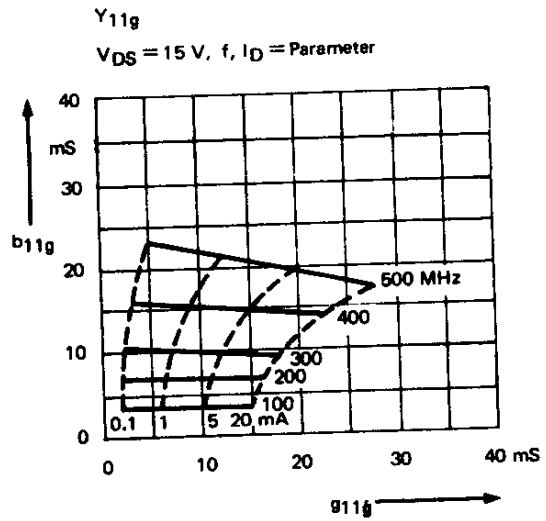
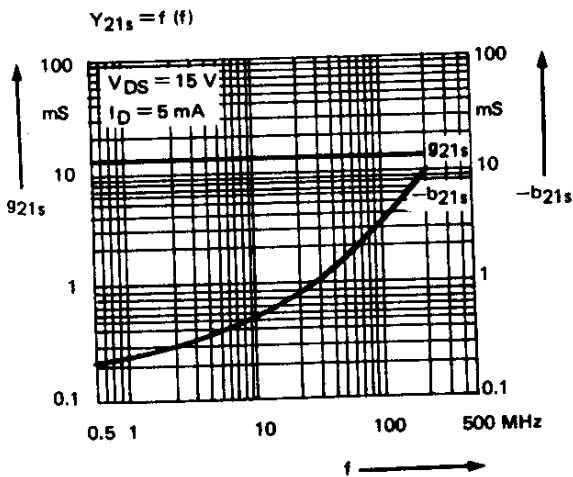
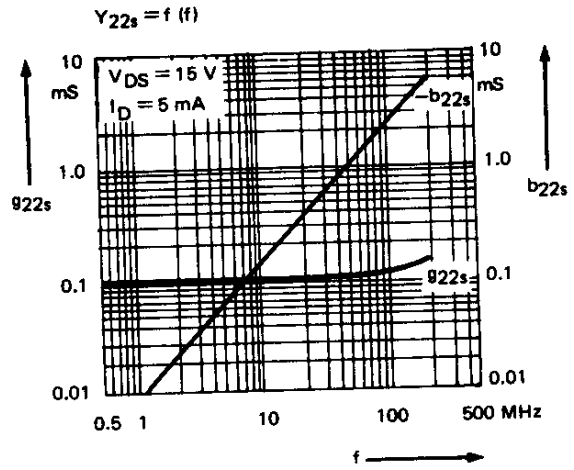
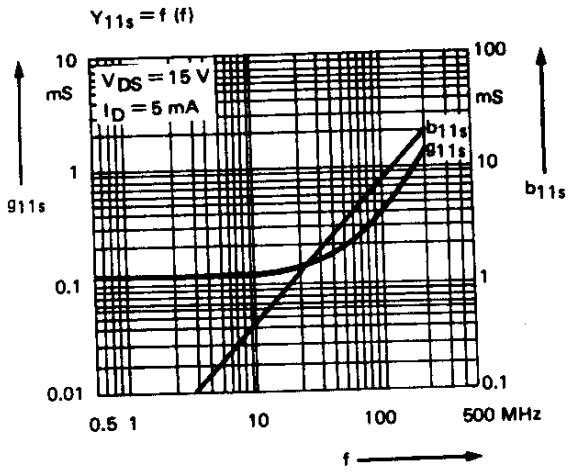
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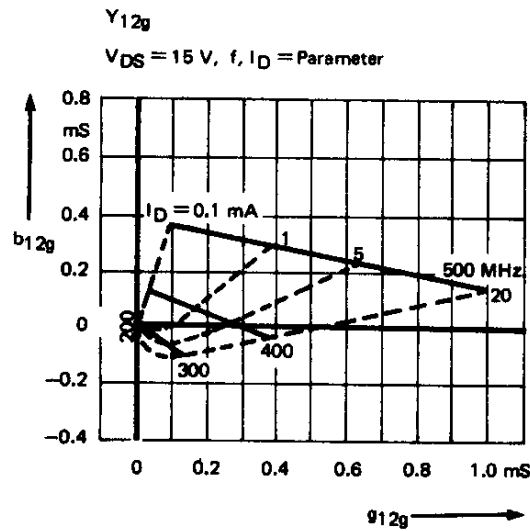
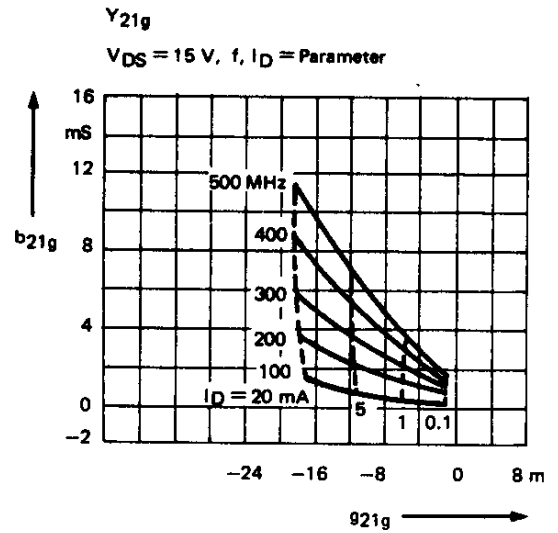
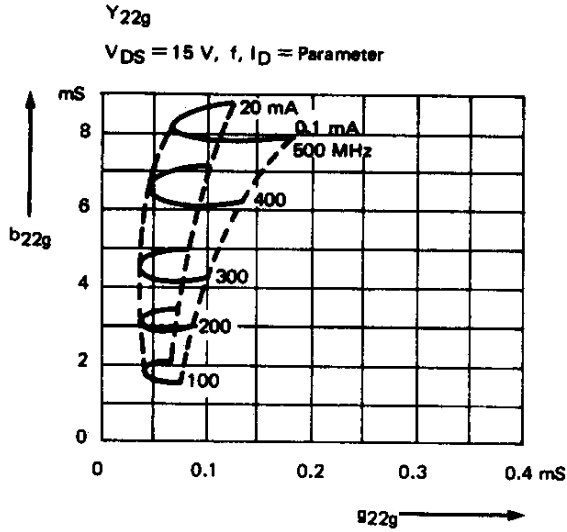
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# BF246, BF247

## N-CANNEL EPITAXIAL PLANAR SILICON FIELD EFFECT TRANSISTOR



# BF246, BF247 N-CANNEL EPITAXIAL PLANAR SILICON FIELD EFFECT TRANSISTOR



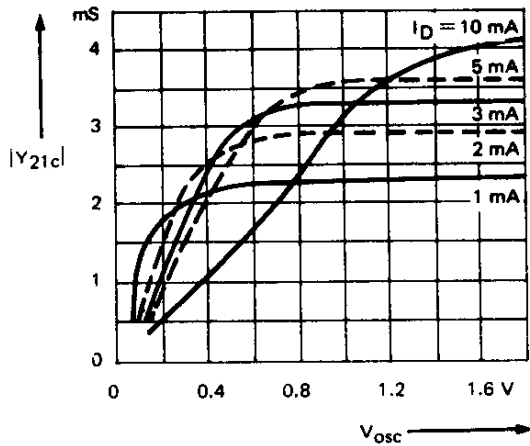
TEXAS INSTRUMENTS

# BF246, BF247 N-CHANNEL EPITAXIAL PLANAR SILICON FIELD EFFECT TRANSISTOR

CONVERSION CONDUCTANCE  $|Y_{21c}|$  AS FUNCTION OF THE OSCILLATOR VOLTAGE

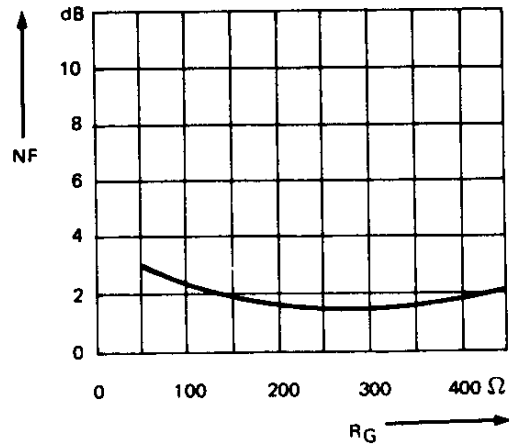
$V_E = \text{const.} = 100 \text{ mV}$ ,  $f_g = 1 \text{ MHz}$

$f_{\text{OSC}} = 1.5 \text{ MHz}$ ,  $I_D = \text{Parameter}$ ,  $V_{DS} = 10 \text{ V}$



$NF = f(R_G)$

$f = 100 \text{ MHz}$ ,  $V_{DS} = 15 \text{ V}$ ,  $I_D = 12 \text{ mA}$



CONVERSION CONDUCTANCE  $|Y_{21c}|$

