

VL-FS-MDLS16265BHD-02 REV.B (MDLS16265BHD-SS-G-LV-LED01 (DOTS)) APR/2006 PAGE 1 OF 15

## DOCUMENT NUMBER AND REVISION VL-FS-MDLS16265BHD-02 REV.B (MDLS16265BHD-SS-G-LV-LED01 (DOTS))

# DOCUMENT TITLE: SPECIFICATION OF LCD MODULE TYPE MODEL NO.: MDLS16265BHD-02

DEPARTMENT	NAME	SIGNATURE	DATE
PREPARED BY	ZHANG YAN FANG	Zhang Yan Fang	2006.4.13
CHECKED BY	XIE SHENG	Nie Areng	2006.04.13.
APPROVED BY	WONG MAN CHARM	from por com	m614/13

DISTRIBUTION LIST: MARKETING



VL-FS-MDLS16265BHD-02 REV.B (MDLS16265BHD-SS-G-LV-LED01 (DOTS)) APR/2006 PAGE 2 OF 15

### **DOCUMENT REVISION HISTORY**

DOCUMENT DATE		DESCRIPTION	CHANGED	CHECKED		
REVISION			BY	BY		
FROM TO			1			
A	2005.08.10	First Release. Based on: a) Test Specification VL-TS-MDLS16265BHD-XX REV.D 2003.12.01 b) VL-QUA-012A, REV. R, 2004.03.20. According to VL-QUA-012A, LCD size is small because Unit Per Laminate=35 which is greater than 6 pcs/Laminate.)	ZHANG XIAO LAN	FRANK WANG		
A B	2006.04.13	Items 1 to 3 were updated.  1.) (Page4, Point1) "RoHS" compliance was added.  2.) (Page8, Table4) Humidity condition was updated.  3.) (Page9, Table5) Number of LED dies was revised from 4 to 2 and forward current of backlight was updated.	ZHANG YAN FANG	XIE SHENG		



VL-FS-MDLS16265BHD-02 REV.B (MDLS16265BHD-SS-G-LV-LED01 (DOTS)) APR/2006 PAGE 3 OF 15

## **CONTENTS**

		<u>Page No.</u>
1.	GENERAL DESCRIPTION	4
2.	MECHANICAL SPECIFICATIONS	4
3.	INTERFACE SIGNALS	7
4. 4.1 4.2	ABSOLUTE MAXIMUM RATINGS ELECTRICAL MAXIMUM RATINGS – FOR IC ONLY ENVIRONMENTAL CONDITIONS	8 8 8
5. 5.1 5.2 5.3	ELECTRICAL SPECIFICATIONS TYPICAL ELECTRICAL CHARACTERISTICS TIMING SPECIFICATIONS TIMING DIAGRAM OF VDD AGAINST V0	9 9 10 12
6.	CGROM CHARACETR CODE TABLE	13
7.	REMARK	14
8.	LCD COSMETIC CONDITIONS	15



VL-FS-MDLS16265BHD-02 REV.B (MDLS16265BHD-SS-G-LV-LED01 (DOTS)) APR/2006 PAGE 4 OF 15

## **VARITRONIX LIMITED**

## Specification of LCD Module Type Model No.: MDLS16265BHD-02

### 1. General Description

- 16 characters (5 x 8 dots) x 2 lines STN Positive Transflective Yellow LCD Character Module.
- Viewing angle: 6 O'clock.
- Driving scheme: 1/16 duty, 1/5 bias.
- 'SAMSUNG' KS0070BP-00CC (die form) LCD Controller & Driver or equivalent.
- Yellow-green LED01 backlight.
- "RoHS" compliance.

### 2. Mechanical Specifications

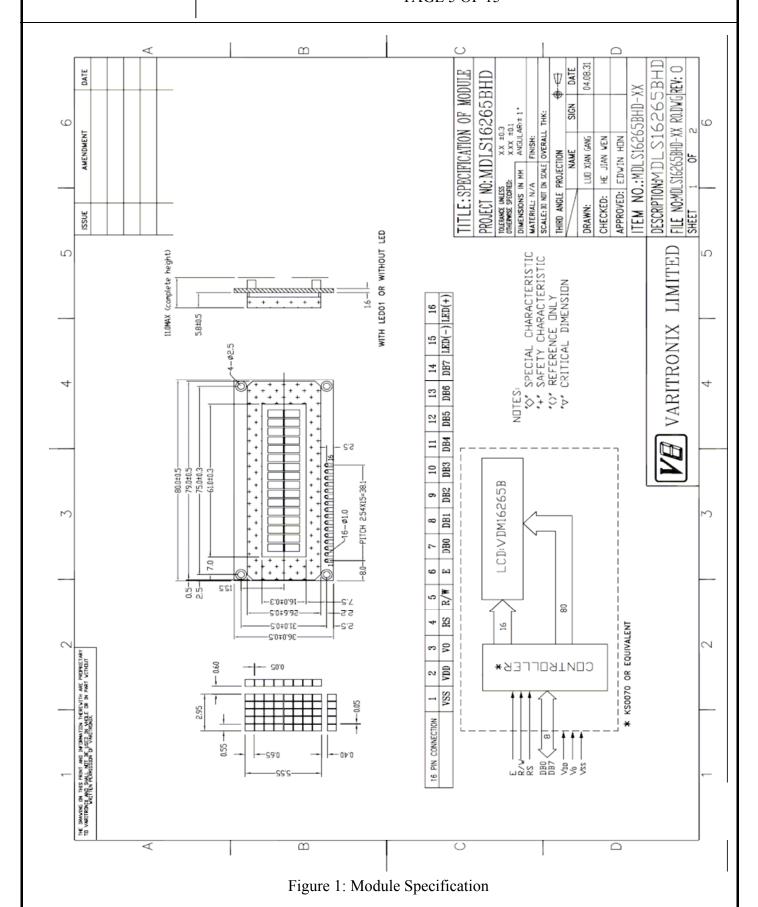
The mechanical detail is shown in Fig. 1 and summarized in Table 1 below.

Table 1

Parameter	Specifications	Unit
Outline dimensions	80.0(W) x 36.0(H) x 11.0 MAX.(D)	mm
Viewing area	61.0(W) x 16.0(H)	mm
Display format	16 characters x 2 lines	-
Character size	2.95(W) x 5.55(H) (5 x 8 dots)	mm
Character spacing	0.60(W) x 0.40(H)	mm
Character pitch	3.55(W) x 5.95(H)	mm
Dot size	0.55(W) x 0.65(H)	mm
Dot spacing	$0.05(W) \times 0.05(H)$	mm
Dot pitch	0.60(W) x 0.70(H)	mm
Weight	Approx.: 34	gram



VL-FS-MDLS16265BHD-02 REV.B (MDLS16265BHD-SS-G-LV-LED01 (DOTS)) APR/2006 PAGE 5 OF 15





VL-FS-MDLS16265BHD-02 REV.B (MDLS16265BHD-SS-G-LV-LED01 (DOTS)) APR/2006 PAGE 6 OF 15

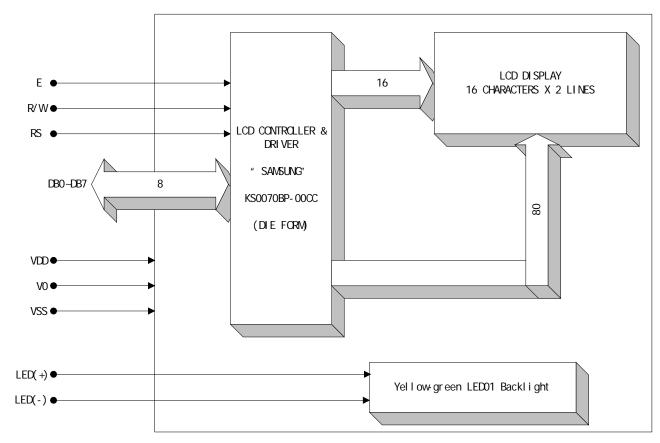


Figure 2: Block diagram



VL-FS-MDLS16265BHD-02 REV.B (MDLS16265BHD-SS-G-LV-LED01 (DOTS)) APR/2006 PAGE 7 OF 15

## 3. Interface signals

## Table 2

Pin No.	Symbol	Description
1	VSS	Ground (0V).
2	VDD	Power supply for logic.
3	V0	Power supply for LCD driver.
4	RS	Register Select Input:
		"High" for Data register (for read and write).
		"Low" for Instruction register (for write),
		Busy flag, address counter (for read).
5	R/W	Read/Write signal:
		"High" for Read mode.
		"Low" for Write mode.
6	E	Enable.
		Start signal for data read /write.
7	DB0	Data input/output (LSB).
8	DB1	Data input/output.
9	DB2	Data input/output.
10	DB3	Data input/output.
11	DB4	Data input/output.
12	DB5	Data input/output.
13	DB6	Data input/output.
14	DB7	Data input/output (MSB).
15	LED (-)	Cathode of LED Backlight.
16	LED (+)	Anode of LED Backlight.



VL-FS-MDLS16265BHD-02 REV.B (MDLS16265BHD-SS-G-LV-LED01 (DOTS)) APR/2006 PAGE 8 OF 15

## **4 Absolute Maximum Ratings**

## **4.1 Electrical Maximum Ratings – For IC Only**

Table 3

Parameter	Symbol	Min.	Max.	Unit
Power Supply voltage (Logic)	VDD-VSS	-0.3	+7.0	V
Power Supply voltage	VLCD=VDD-V0	-0.3	+15.0	V
(LCD drive)				
Input voltage	Vin	-0.3	VDD+0.3	V

Note (1): The module may be destroyed if they are used beyond the absolute maximum ratings.

(2): All voltage values are referenced to VSS = 0V.

#### **4.2 Environmental Conditions**

Table 4

	Oper	ating	Stor	age	
Item	Tempe	erature	Tempe	erature	Remark
	(To	pr)	(Ts	tg)	
			(Not	te 1)	
	Min.	Max.	Min.	Max.	
Ambient Temperature	0°C	+50°C	-10°C	+60°C	Dry
Humidity (Note1)	90% max	. RH for T	$a \le 40^{\circ}C$	No condensation	
	<50%RH	for 40°C	<Ta $<=$ Ma		
	operating	temperatu	ıre		
Vibration (IEC 68-2-6)	Frequenc	y: 10 ~ 5	5 Hz		3 directions
cells must be mounted	Amplitud	le: 0.75 mr	n		
on a suitable connector	Duration	20 cycles	in each di	rection.	
Shock (IEC 68-2-27)	Pulse dur	ation: 11 r	ns	3 directions	
Half-sine pulse shape	Peak acco	eleration: 9	$981 \text{ m/s}^2 =$		
	Number of	of shocks:	3 shocks i		
	mutually	perpendic	ular axes.		

Note (1): Product cannot sustain at extreme storage conditions a long time.



VL-FS-MDLS16265BHD-02 REV.B (MDLS16265BHD-SS-G-LV-LED01 (DOTS)) APR/2006 PAGE 9 OF 15

## **5. Electrical Specifications**

## **5.1 Typical Electrical Characteristics**

At  $Ta = 25 \, ^{\circ}\text{C}$ ,  $VDD = 5V \pm 5\%$ , VSS = 0V.

### Table 5

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Supply voltage (Logic)	VDD -VSS		4.85	5.0	5.15	V
Supply voltage (LCD)	VLCD =VDD -V0	VDD = 5V, Note (1)	4.3	4.6	4.9	V
Input signal voltage for E,DB0-DB7,R/W,RS.	V <sub>IH</sub>	"H" level	2.2	-	VDD	V
, , ,	$V_{ m IL}$	"L" level	-0.3	1	0.6	V
Supply Current (Logic & LCD)	IDD	Character mode, VDD=5V, Note 1	-	1.3	2.0	mA
		Checker board mode, VDD=5V, Note 1	-	1.5	2.3	mA
Supply Current (LCD)	10	Character mode, VDD=5V, Note 1	-	0.5	0.8	mA
		Checker board mode, VDD=5V, Note 1	-	0.5	0.8	mA
Supply voltage of yellow-green LED01 backlight	VLED	Forward current = 20mA	4.0	4.1	4.2	V
Wavelength of yellow-green LED01 backlight	λ	Number of LED dies =2	-	568	-	nm

Note (1): There is tolerance in optimum LCD driving voltage during production and it will be within the specified range.



VL-FS-MDLS16265BHD-02 REV.B (MDLS16265BHD-SS-G-LV-LED01 (DOTS)) APR/2006 PAGE 10 OF 15

## **5.2 Timing Specifications**

At Ta = 0 °C To +50 °C, VDD = +5V $\pm$ 5%, VSS = 0V. Refer to Fig. 3, the bus timing diagram for write mode.

Table 6

Parameter	Symbol	Min.	Max.	Unit
E cycle time	$t_{c}$	500	-	ns
E rise time	$t_{\rm r}$	-	25	ns
E fall time	${ m t_f}$	-	25	ns
E pulse width (High, Low)	$t_{ m W}$	220	-	ns
R/W and RS set-up time	$t_{\mathrm{SU1}}$	40	-	ns
R/W and RS hold time	$t_{h1}$	10	-	ns
Data set-up time	$t_{ m SU2}$	60	_	ns
Data hold time	$t_{h2}$	10	_	ns

Refer to Fig. 4, the bus timing diagram for read mode.

Table 7

Parameter	Symbol	Min.	Max.	Unit
E cycle time	$t_{\mathrm{C}}$	500	-	ns
E rise time	$t_{\rm r}$	-	25	ns
E fall time	$t_{\mathrm{f}}$	-	25	ns
E pulse width (High,Low)	$t_{ m W}$	220	-	ns
R/W and RS set-up time	$t_{ m SU}$	40	-	ns
R/W and RS hold time	$t_{\rm h}$	10	-	ns
Data output delay time	$t_{\mathrm{D}}$	-	120	ns
Data hold time	$t_{ m DH}$	20	-	ns



VL-FS-MDLS16265BHD-02 REV.B (MDLS16265BHD-SS-G-LV-LED01 (DOTS)) APR/2006 PAGE 11 OF 15

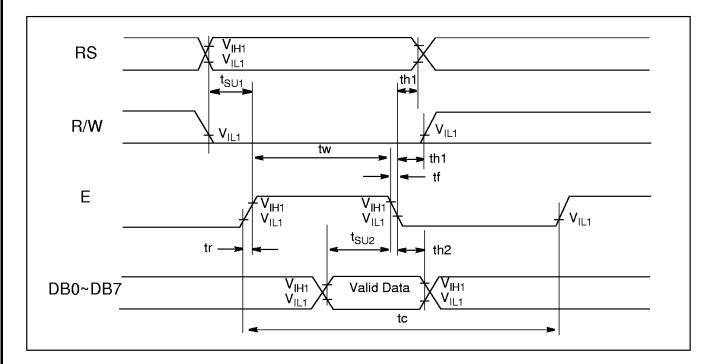


Figure 3: The bus timing diagram for write mode.

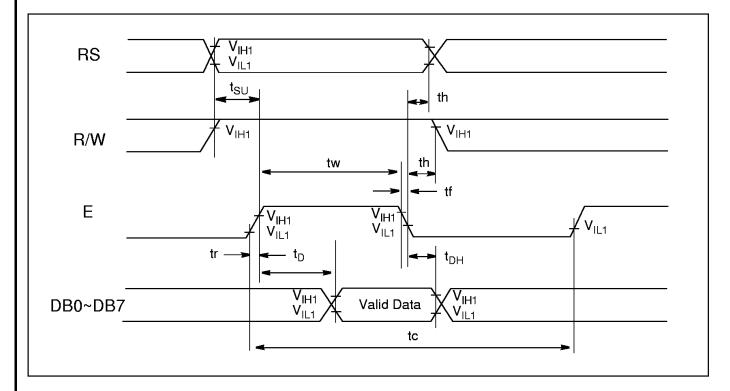


Figure 4: The bus timing diagram for read mode.



VL-FS-MDLS16265BHD-02 REV.B (MDLS16265BHD-SS-G-LV-LED01 (DOTS)) APR/2006 PAGE 12 OF 15

## **5.3** Timing Diagram of VDD Against V0

Power on sequence shall meet the requirement of Figure 5, the timing diagram of VDD against V0.

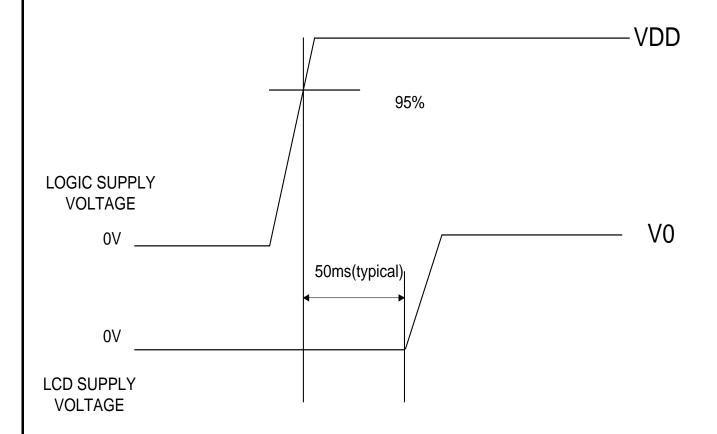


Figure 5: Timing Diagram of VDD Against V0.



VL-FS-MDLS16265BHD-02 REV.B (MDLS16265BHD-SS-G-LV-LED01 (DOTS)) APR/2006 PAGE 13 OF 15

## 6. CGROM Character Code Table

KS00	70B-	00			•										
Upper 4bit Lower 4bit	LLLL	LLHL	LLHH	LHLL	LHLH	LHHL	ІННН	HLLL	HLLH	HLHL	нілн	HHLL	ннін	HHHL	нннн
LLLL	CG RAM (1)														
LLLH	(2)														
LLHL	(3)														
LLHH	(4)														
LHLL	(5)														
THTH	6														
LHHL	0														
LHHH	(8)														
HLLL	(I)														
ніін	(2)														
HLHL	(3)														
нілн	(4)								В						
HHLL	(5)														
ннін	(6)														
нннг	თ														
ннн	(8)														



## VL-FS-MDLS16265BHD-02 REV.B (MDLS16265BHD-SS-G-LV-LED01 (DOTS)) APR/2006

PAGE 14 OF 15

#### 7. Remark:

#### HANDLING LCD AND LCD MODULES

#### 1. Liquid Crystal Display (LCD)

LCD is made up of glass, organic sealant, organic fluid and polymer based polarizers. The following precautions should be taken when handling:

- Keep the temperature within range for use and storage. Excessive temperature and humidity could cause polarization degredation, polarizer peel-off or bubble generation. When storage for a long period over 40° C is required, the relative humidity should be kept below 60%.
- (2) Do not contact the exposed polarizers with anything harder than an HB pencil lead. To clean dust off the display surface, wipe gently with cotton, chamois or other soft material soaked in petroleum benzin. Never scrub hard. Varitronix does not responsible for any polarizer
- defect after the protective film has been removed from the display
- Wipe off saliva or water drops immediately. Contact with water over a long period of time may cause polarizer deformation or color fading, while an active LCD with water condensation on its surface will e corrosion of ITO electrodes.
- PETROLEUM BENZIN is recommended to remove adhesives used to attach front/rear polarizers and reflectors, while chemicals like acetone, toluene, ethanol and isopropyl alcohol will cause damage to the polarizer. Avoid oil and fats, Avoid lacquer and epoxies which might contain solvents and hardeners to cause electrode errosion. Some solvents will also soften the epoxy covering the DIL pins and thereby weakening the adhesion of the epoxy on glass. This will cause the exposed electrodes to erode electrochemically when operating in high humidity and condensing environment.
- Glass can be easily chipped or cracked from rough handling, especially at corners and edges.
- Do not drive LCD with DC voltage.
- When soldering DIL pins, avoid excessive heat and keep soldering temperature between 260°C to 300°C for no more than 5 seconds. Never use wave or reflow soldering.

#### 2. Liquid Crystal Display Modules (MDL)

#### 2.1 Mechanical Considerations

MDL's are assembled and adjusted with a high degree of precision. Avoid excessive shocks and do not make any alterations or modifications. The following should be noted.

- Do not tamper in any way with the tabs on the metal frame.
- Do not modify the PCB by drilling extra holes changing its outline, moving its components or modifying its pattern.
- (3) Do not touch the elastomer connector (conductive rubber), especially when inserting an EL panel.

When mounting a MDL make sure that the PCB is not under any stress such as bending or twisting. Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.

- Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose ontact, resulting in missing pixels.
- If FPCA need to be bent, please refer the suggested bending area on the specification. The stiffener and component area on FPC/FFC/COF must not be bent during or after assembly (Note: for those models with FPC/FFC/COF +stiffener).
- Sharp bending should be avoided on FPC to prevent track cracking.

#### 2.2 Static Electricity

MDL contains CMOS LSI's and the same precaution for such devices should apply, namely:

- The operator should be grounded whenever he comes into contact with the module. Never touch any of the conductive parts such as the LSI pads, the copper leads on the PCB and the interface terminals with any part of the human body.
- The modules should be kept in antistatic bags or other containers resistant to static for storage
- (3) Only properly grounded soldering irons should be
- (4) If an electric screwdriver is used it should be well grounded and shielded from commutator sparks.
- The normal static prevention measures should be observed for work clothes and working benches; for the latter conductive (rubber) mat is recommended.
- Since dry air is inducive to statics, a relative humidity of 50 - 60% is recommended.

#### 2.3 Soldering

- Solder only to the I/O terminals.
- Use only soldering irons with proper grounding and (2)no leakage.
- Soldering temperature is  $280^{\circ}C \pm 10^{\circ}C$ . (3)
- Soldering time: 3 to 4 seconds.
  Use eutectic solder with resin flux fill. (5)
- If flux is used, the LCD surface should be covered to avoid flux spatters. Flux residue should be removed
- Use proper de-soldering methods (e.g. suction type desoldering irons) to remove lead wires from the I/O terminals when necessary. Do not repeat the soldering/ desoldering process more than three times as the pads and plated through holes may be damaged

Identification labels will be stuck on the module without

obstructing the viewing area of display.

- The viewing angle can be adjusted by varying the LCD driving voltage Vo.
- Driving voltage should be kept within specified range excess voltage shortens display life. Response time increases with
- temperature.
- Display may turn black or dark Blue at temperature above its operational range; this is however not destructive and the display will return to normal once the temperature falls back to range
- Mechanical disturbance during operation (such as pressing on the viewing area) may cause the segments to appear "fractured". They will recover
- once the display is turned off.

  Condensation at terminals will cause malfunction and possible electrochemical reaction. Relative humidity of the environment should therefore be kept below
- Display performance may vary out of viewing area If there is any special requirement on performance out of viewing area, please consult Varitronix.

#### 4. Storage and Reliability

- LCD's should be kept in sealed polyethylene bags while MDL's should use antistatic ones. If properly sealed, there is no need for desiccant.
- Store in dark places and do not expose to sunlight or fluorescent light. Keep the temperature between 0°C and 35°C and the relative humidity low. Please consult VARITRONIX for other storage requirements.
- Water condensation will affect performance of the display and is not allowed.
- Semi-conductor device on the display is sensitive to light and should be protected properly.
- Power up/down sequence.
  - a) Power Up: in general, LCD supply voltage, Vo must be supplied after logic voltage, VDD becomes steady. Please refer to related IC data
  - Power Down: in general, LCD supply voltage Vo must be removed before logic voltage VDD turns off. Please refer to related IC data sheet for details.

#### 5. Safety

If any fluid leaks out of a damaged glass cell, wash off any human part that comes into contact with soap and water Never swallow the fluid. The toxicity is extremely low but caution should be exercised at all times.

#### LIMITED WARRANTY

VARITRONIX LCDs and modules are not consumer products, but may be incorporated by VARITRONIX's customers into consumer products or components thereof. VARITRONIX does not warrant that its LCDs and components are fit for any such particular purpose.

> The liability of VARITRONIX is limited to repair or replacement on the terms set forth below. VARITRONIX will not be responsible for any subsequent or consequential events or injury or damage to any personnel or user including third party personnel and/or user.

Unless otherwise agreed in writing between VARITRONIX and the customer, VARITRONIX will only replace or repair any of its LCD which is found defective electrically or visually when inspected in accordance with VARITRONIX LCD Acceptance Standards (copies available on request), for a period of one year from the date of shipment. Confirmation of such date shall be based on freight

- No warranty can be granted if any of the precautions stated in HANDLING LCD and LCD Modules above have been disregarded Broken glass, scratches on polarizers, mechanical damages as well as defects that are caused by accelerated environmental tests are excluded from warranty.
- In returning the LCD and Modules, they must be properly packaged and there should be detailed description of the failures or defects.

#### IMPORTANT NOTICE

The information presented in this document has been carefully checked and is believed to be accurate, however, no responsibility is assumed for inaccuracies. VARITRONIX reserves the right to make changes to any specifications without further notice for performance, reliability, production technique and other considerations, VARITRONIX does not assume any liability arising out of the application or use of products herein. Please see Limited Warranty in the previous



VL-FS-MDLS16265BHD-02 REV.B (MDLS16265BHD-SS-G-LV-LED01 (DOTS)) APR/2006 PAGE 15 OF 15

#### 8. LCD Cosmetic Conditions

- a.) Reference document follow VL-QUA-012A.
- b.) LCD size of the product is small.

"Varitronix Limited reserves the right to change this specification."

FAX:(852) 2343-9555.

URL:http://www.varitronix.com