

SCHEMATICS ARE CURRENT AS OF PSR PLUS THE FOLLOWING MODS:
 TRACKING # CORPORATE MOD #
 TTL-84 B4566
 TTL-80

CPU SHEETS: 10, 11, 12, 13

ATTN SHEETS: 2, 3, 4, 5

SHEET: 7
 ACQUISITION

SHEETS: 8, 9
 DMM

SHEET: 14
 DISPLAY

SHEETS: 15, 16, 17
 PWRSPLY

ROOTSHEET A1

1

DRAWING
 TITLE=ROOT
 ABBREV=ROOTSHEET
 LAST_MODIFIED=Tue Dec 3 13:06:44 1996

PROJECT=RHOMBUS
 ENGINEER=DOUG HUARD

PHONE: 627-2946

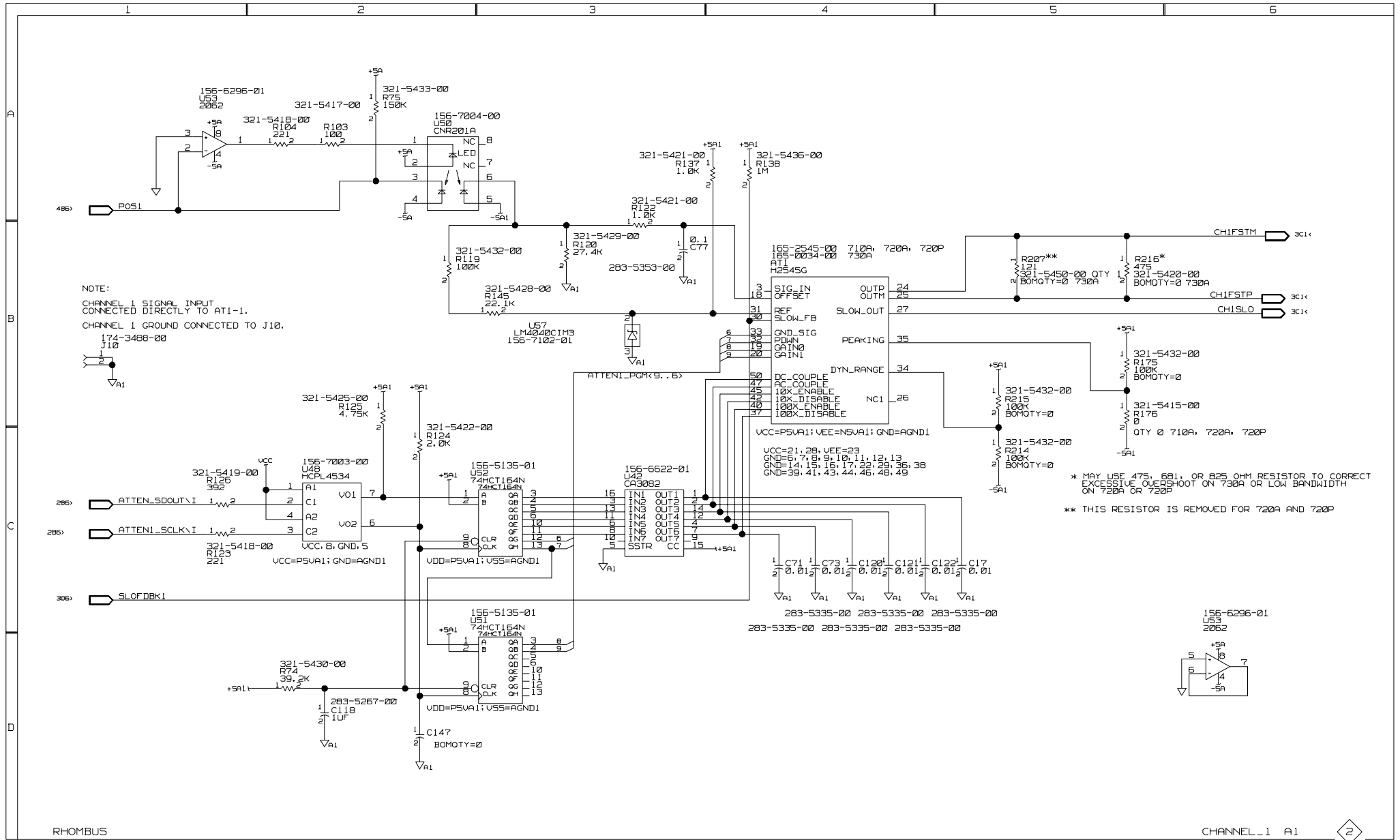
DATE: 3/11/96

SHEET: 1

OF: 17

SHEET_NAME=ROOTSHEET

ASSEMBLY NUMBER:



NOTE:
 CHANNEL 1 SIGNAL INPUT
 CONNECTED DIRECTLY TO AT1-1.
 CHANNEL 1 GROUND CONNECTED TO J10.
 174-3488-00
 J10

* MAY USE 475, 681, OR 825 OHM RESISTOR TO CORRECT
 EXCESSIVE OVERTSHOOT ON 730A OR LOW BANDWIDTH
 ON 720A OR 720P
 ** THIS RESISTOR IS REMOVED FOR 720A AND 720P

RHOMBUS

PROJECT=RHOMBUS
 ENGINEER=DOUG HUARD

PHONE: 627-2946

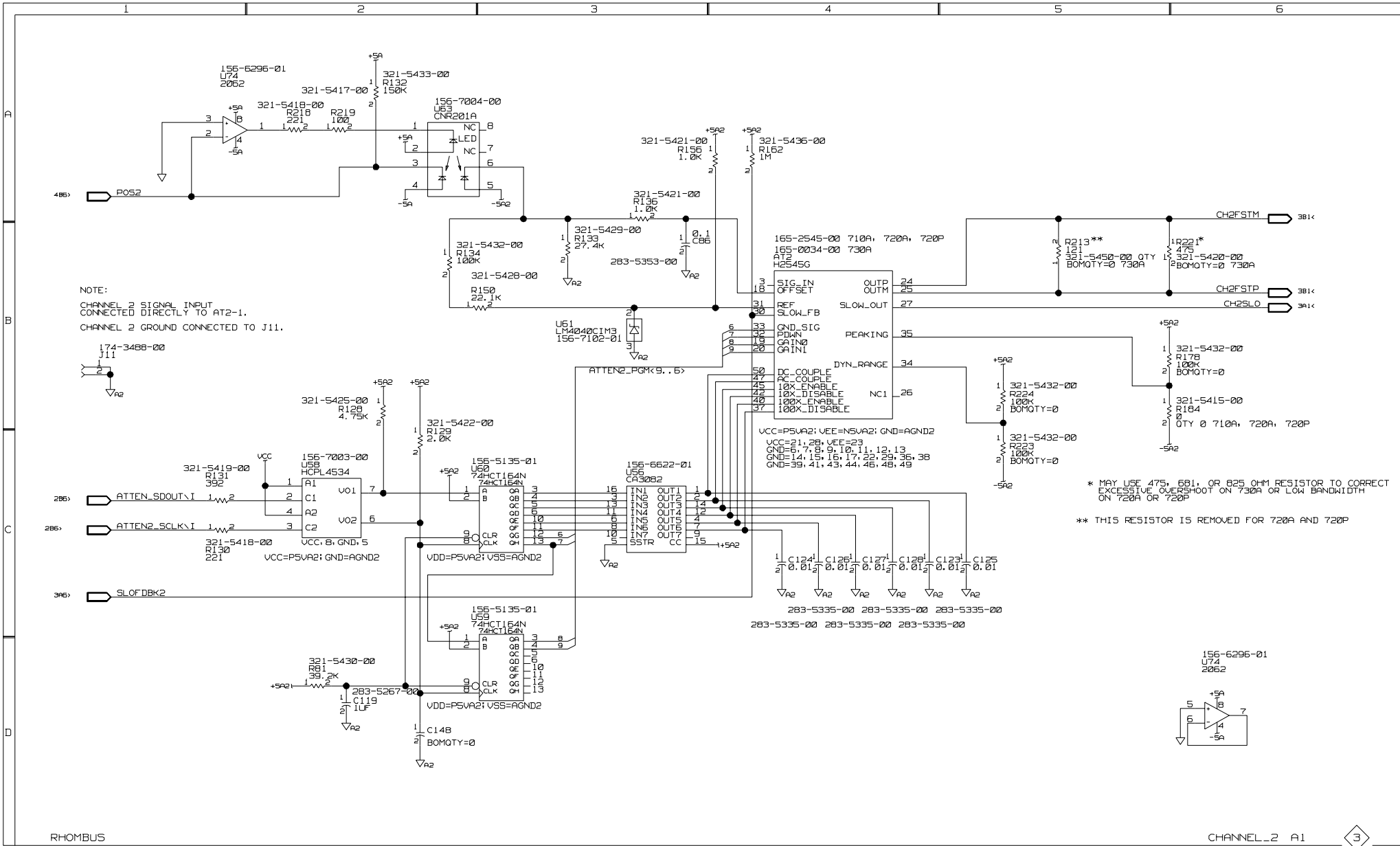
DATE: 3/11/96

SHEET: 2 OF: 17 SHEET_NAME: CHANNEL_1

ASSEMBLY NUMBER:

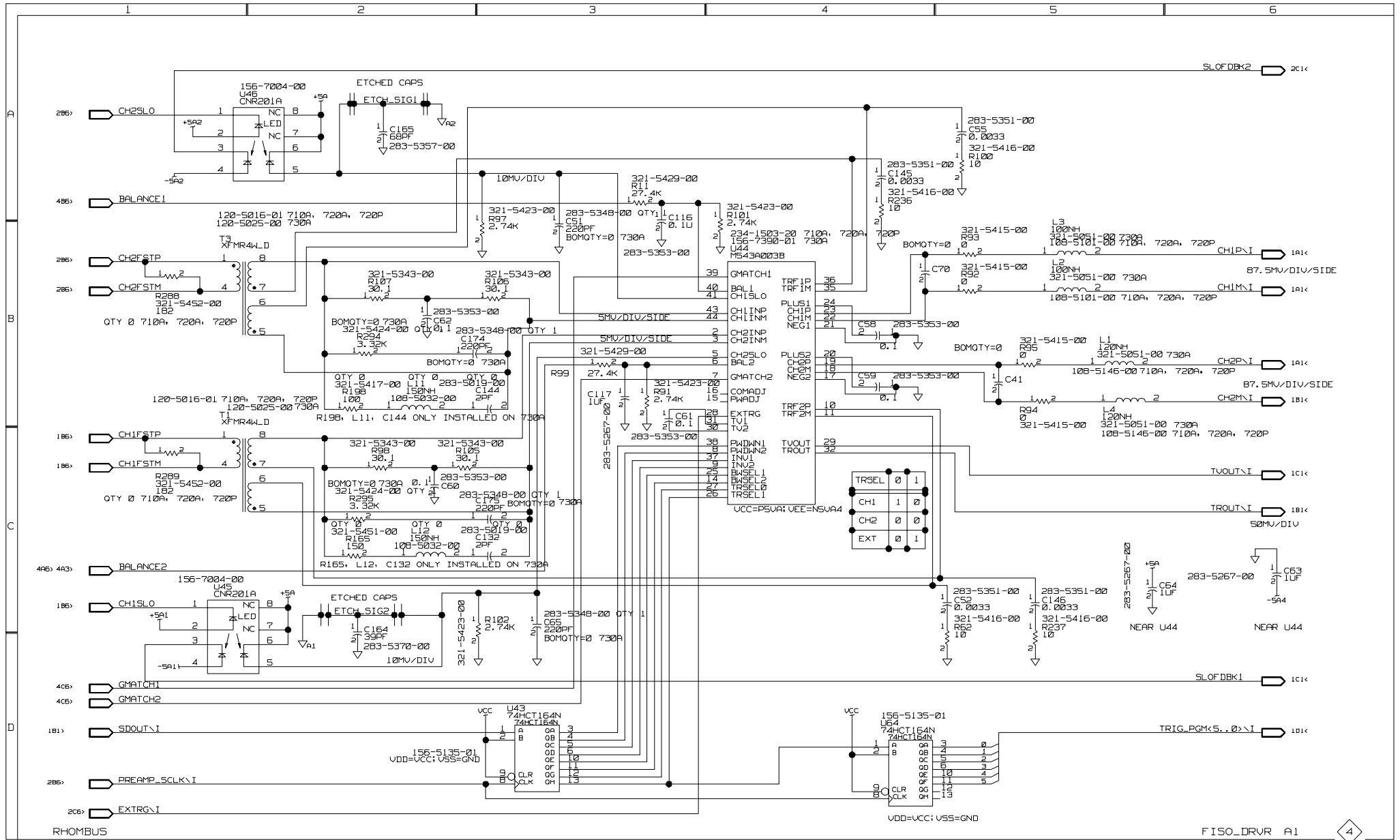
TITLE=ATTEN
 ABBREV=ATTEN
 LAST_MODIFIED=Tue Dec 3 13:06:56 1996

CHANNEL_1 A1



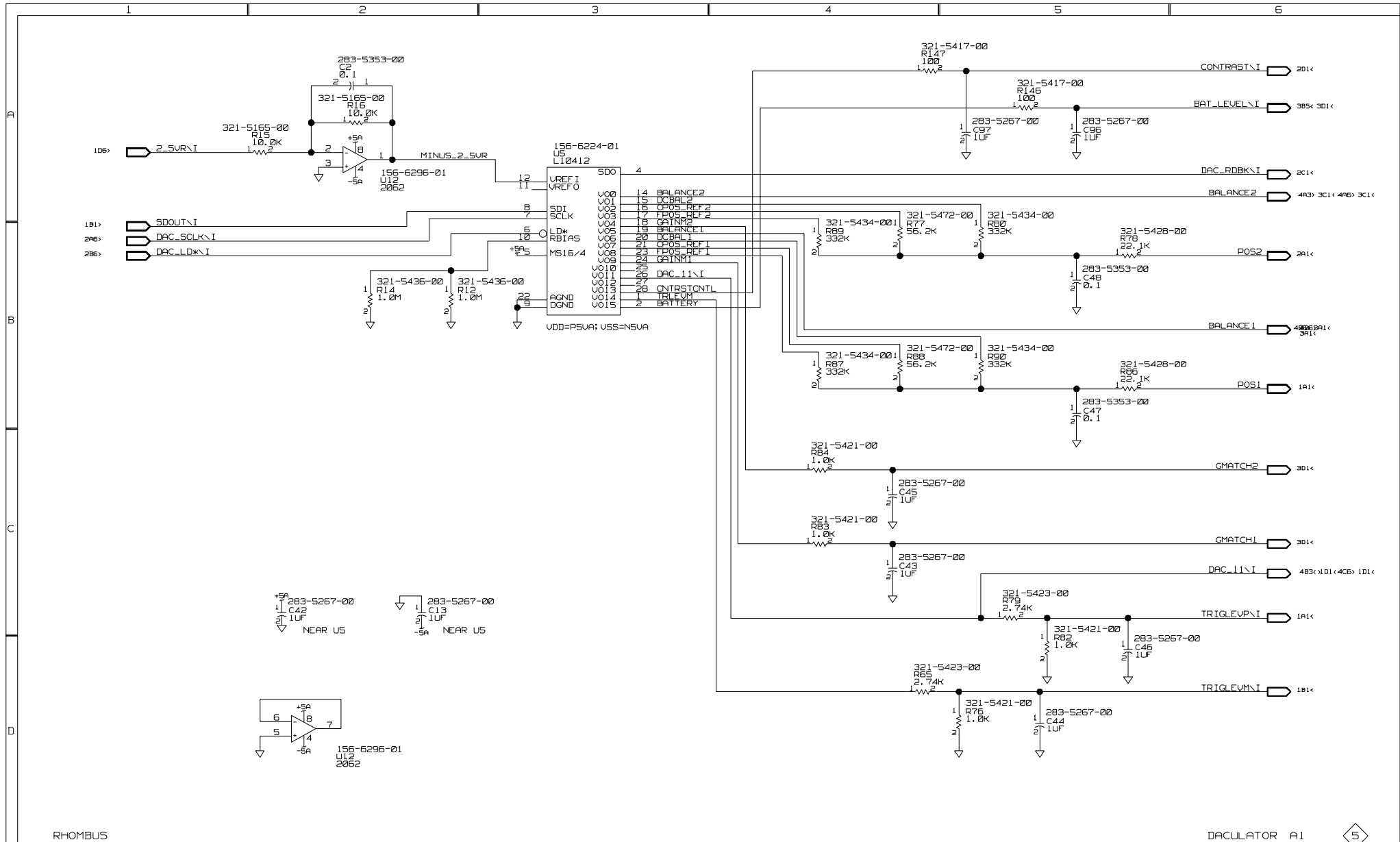
NOTE:
 CHANNEL 2 SIGNAL INPUT
 CONNECTED DIRECTLY TO AT2-1.
 CHANNEL 2 GROUND CONNECTED TO J11.

* MAY USE 475, 681, OR 825 OHM RESISTOR TO CORRECT
 EXCESSIVE OVERSHOOT ON 730A OR LOW BANDWIDTH
 ON 720A OR 720P
 ** THIS RESISTOR IS REMOVED FOR 720A AND 720P



PROJECT=RHOMBUS
 ENGINEER=DOUG HUARD
 PHONE: 627-2946
 DATE: 3/11/96
 SHEET: 4 OF: 17
 SHEET_NAME: FISO_DRV1
 ASSEMBLY_NUMBER:
 DRAWING
 TITLE=A11EN
 ABBREV=A11EN
 LAST_MODIFIED=Tue Dec 3 13:07:10 1995

FISO_DRV1 A1



RHOMBUS

DACULATOR A1

5

DRAWING

PROJECT=RHOMBUS
ENGINEER=DOUG HUARD

PHONE: 627-2946

DATE: 3/11/96

SHEET: 5

OF: 17

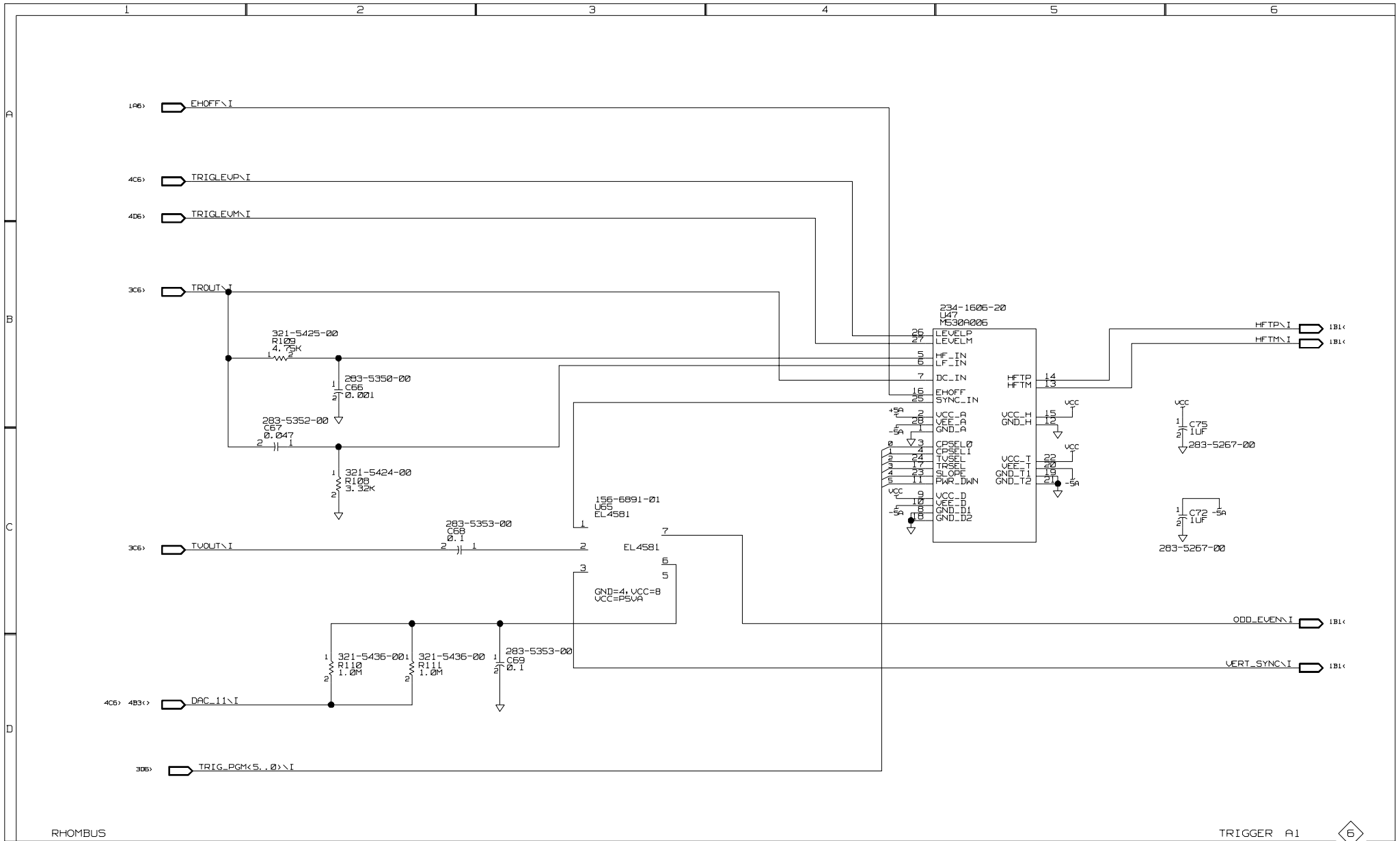
SHEET_NAME: DACULATOR

ASSEMBLY NUMBER:

TITLE=ATTEN

ABBREV=ATTEN

LAST_MODIFIED= Tue Dec 3 13:07:15 1996



RHOMBUS

TRIGGER A1

6

DRAWING

PROJECT=RHOMBUS
ENGINEER=DOUG HUARD

PHONE: 627-2946

DATE: 3/11/96

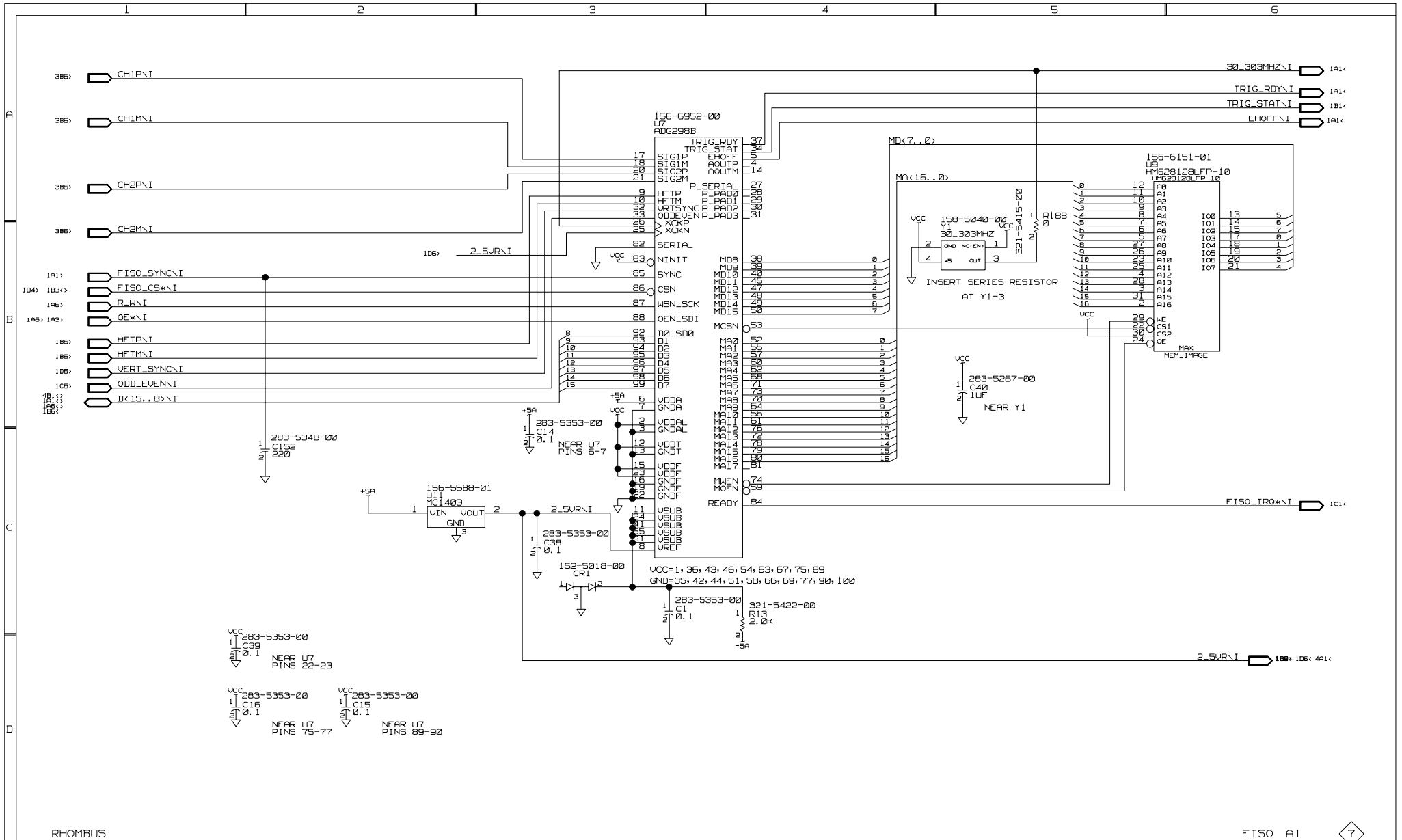
SHEET: 6 OF: 17

SHEET_NAME: TRIGGER

ASSEMBLY NUMBER:

TITLE=TRIGGER

ABBREV=TRIGGER
LAST_MODIFIED= Tue Dec 3 13:06:49 1996



RHOMBUS

FISO A1

7

DRAWING
 TITLE=ACQUISITION
 ABBREV=ACQUISITION
 LAST_MODIFIED=Tue Dec 3 13:07:23 1996

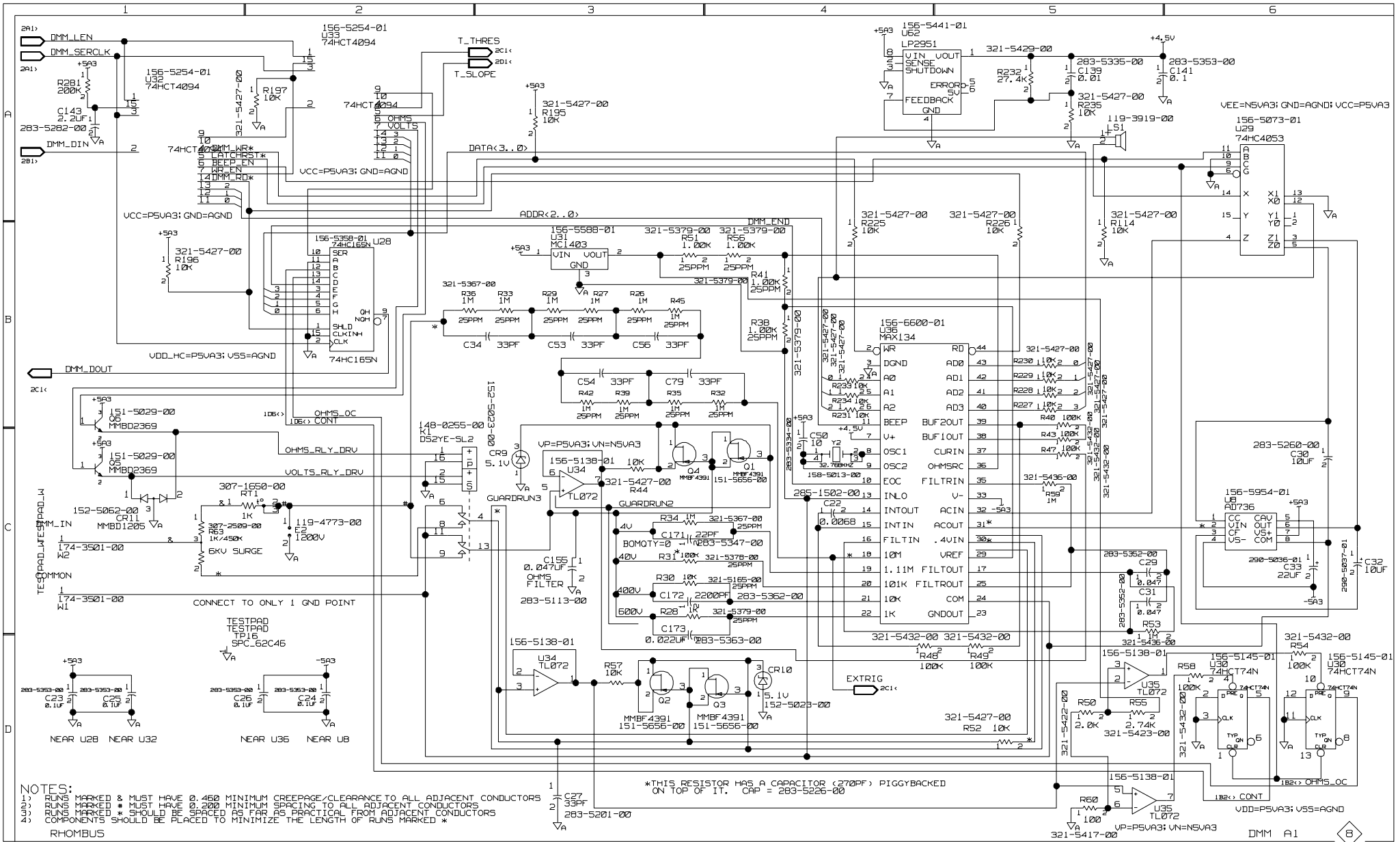
PROJECT=RHOMBUS
 ENGINEER=DOUG HUARD

PHONE: 627-2946

DATE: 3/11/96

SHEET: 7 OF: 17 SHEET_NAME: FISO

ASSEMBLY_NUMBER:



NOTES:
 1) RUNS MARKED & MUST HAVE 0.460 MINIMUM CREEPAGE/CLEARANCE TO ALL ADJACENT CONDUCTORS
 2) RUNS MARKED * MUST HAVE 0.200 MINIMUM SPACING TO ALL ADJACENT CONDUCTORS
 3) RUNS MARKED * SHOULD BE SPACED AS FAR AS PRACTICAL FROM ADJACENT CONDUCTORS
 4) COMPONENTS SHOULD BE PLACED TO MINIMIZE THE LENGTH OF RUNS MARKED *

*THIS RESISTOR HAS A CAPACITOR (270PF) PIGGYBACKED ON TOP OF IT. CAP = 283-5226-00

DRAWING

TITLE=DMM

ABREV=DMM

LAST_MODIFIED=Tue Dec 3 13:07:38 1996

PROJECT=RHOMBUS
 ENGINEER=DOUG HUARD/BOB SIMPSON

PHONE:627-2946

DATE:3/11/96

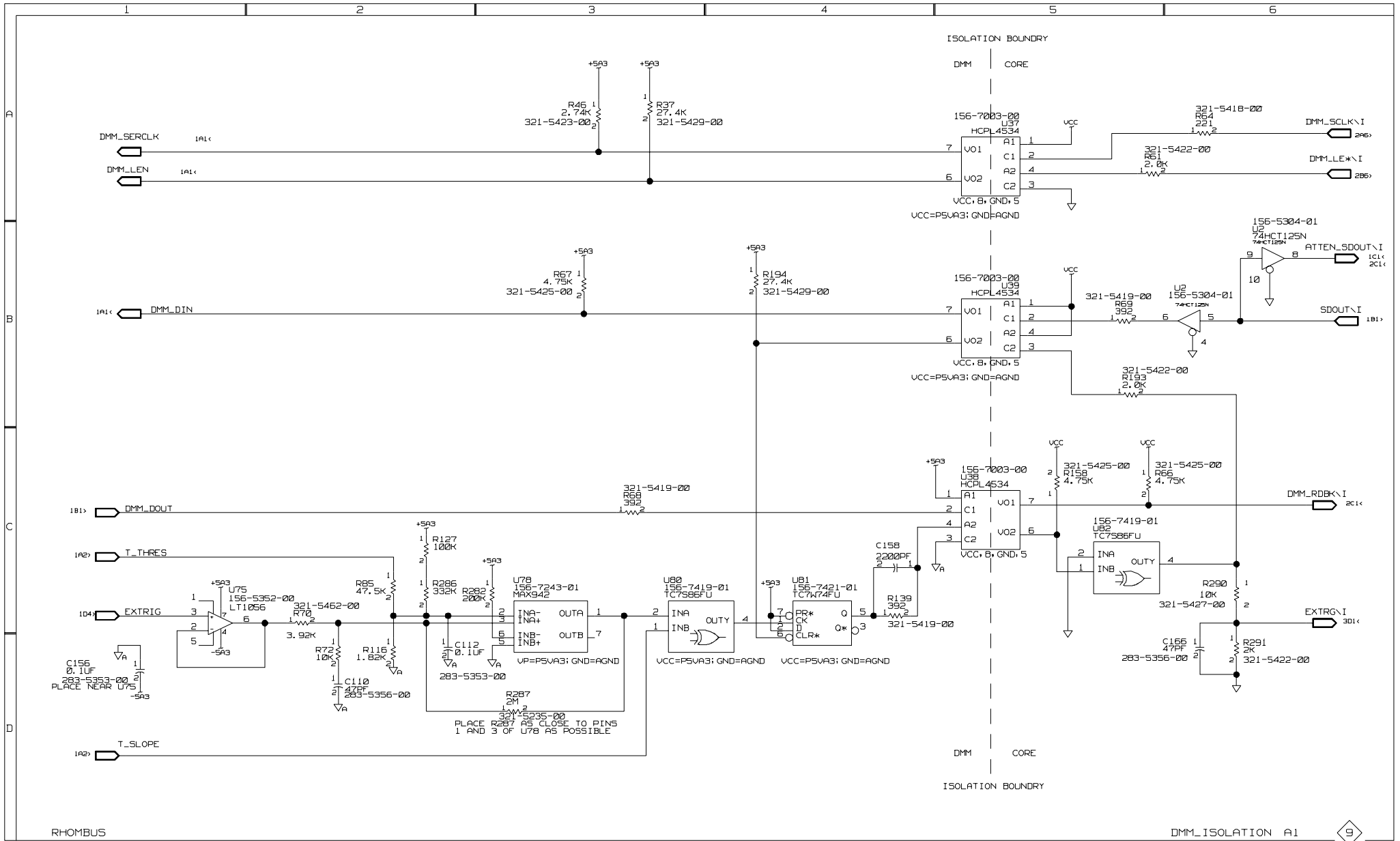
SHEET: 8

OF: 17

SHEET_NAME: DMM

ASSEMBLY NUMBER:

8



RHOMBUS

DMM_ISOLATION A1

9

DRAWING

PROJECT=RHOMBUS
ENGINEER=DOUG HUARD/BOB SIMPSON

PHONE: 627-2946

DATE: 3/11/96

SHEET: 9

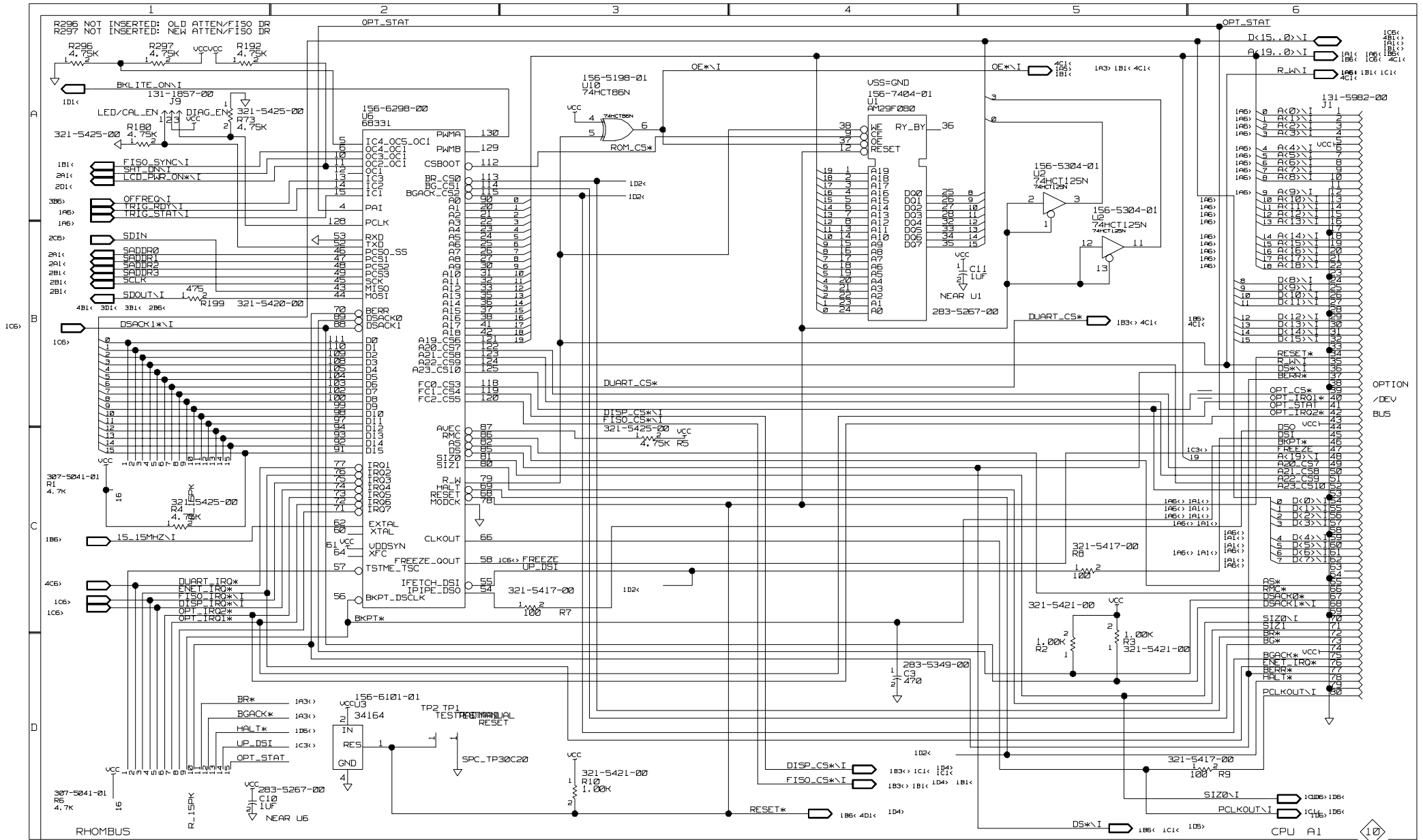
OF: 17

SHEET_NAME: DMM_ISOLATION

ASSEMBLY_NUMBER:

ABBREV=DMM

LAST_MODIFIED=Tue Dec 3 13:07:45 1996



DRAWING
 TITLE=CPU
 ABBREV=CPU
 LAST_MODIFIED=Tue Dec 3 13:08:36 1996

PROJECT=RHOMBUS
 ENGINEER=DOUG HUARD

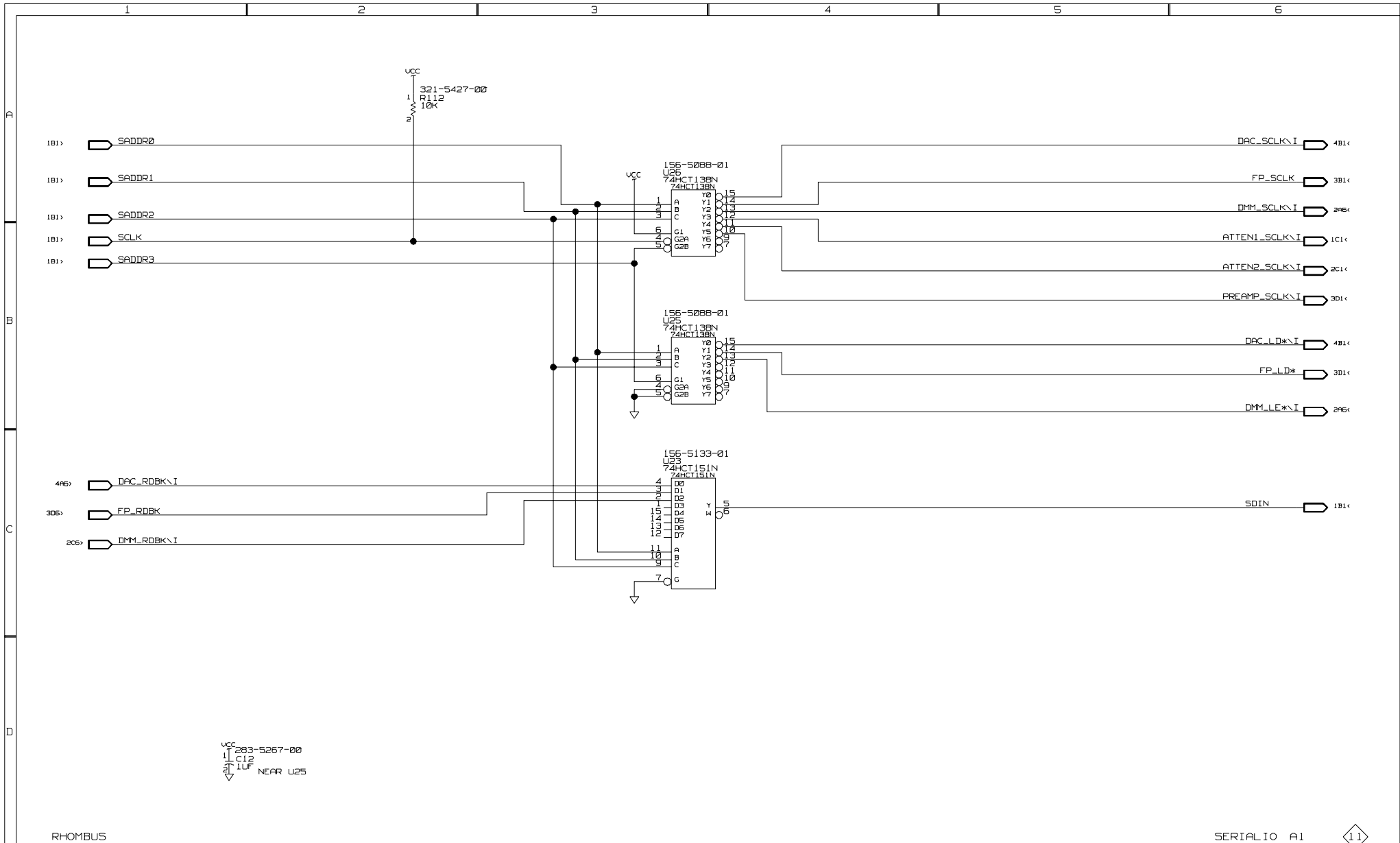
PHONE: 627-2946

DATE: 3/11/96

SHEET: 10 OF: 17

SHEET_NAME: CPU

ASSEMBLY_NUMBER:



RHOMBUS

SERIALIO A1

11

DRAWING

PROJECT=RHOMBUS
ENGINEER=DOUG HUARD

PHONE: 627-2946

DATE: 3/11/96

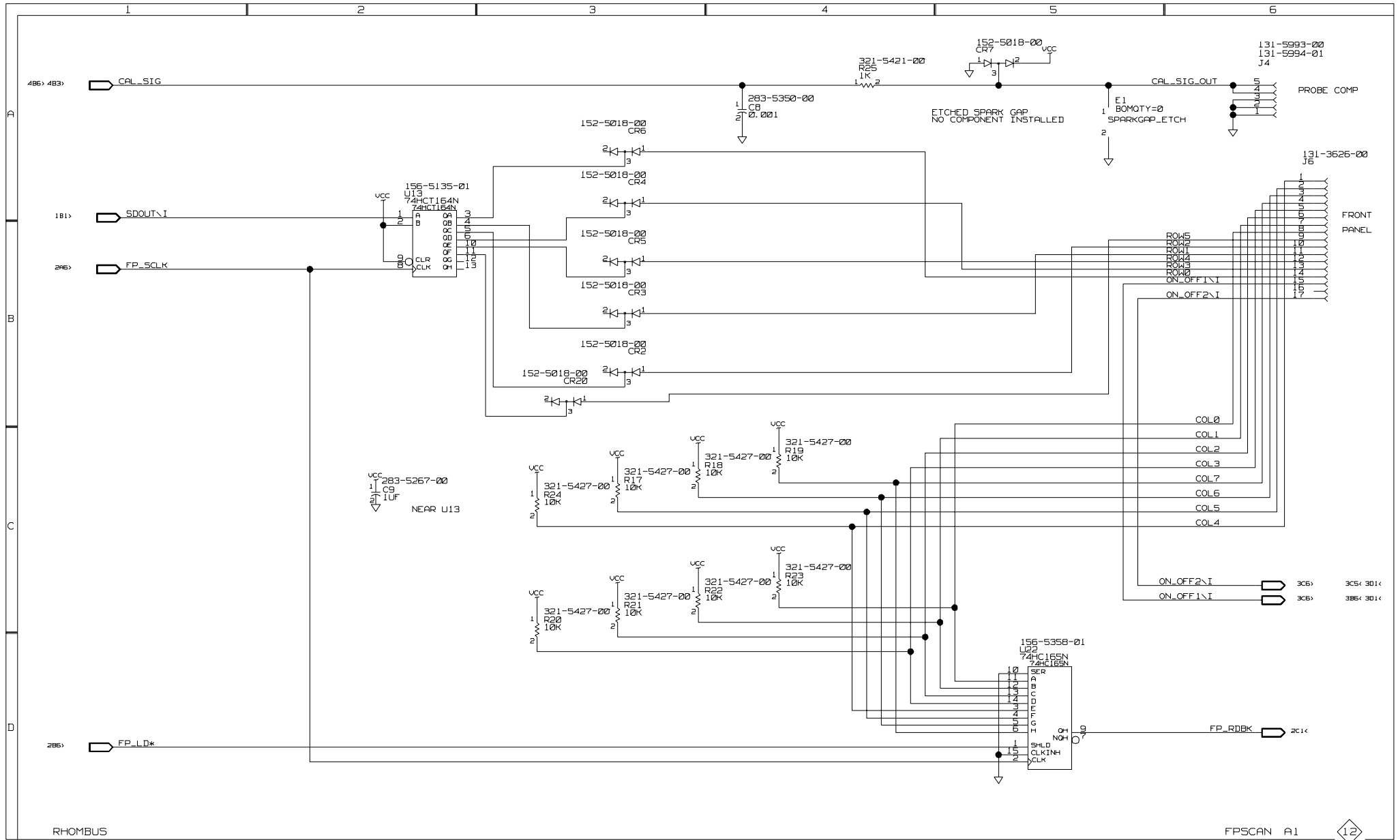
SHEET: 11 OF: 17

SHEET_NAME: SERIALIO

ASSEMBLY_NUMBER:

ABBREV=CPU

LAST_MODIFIED=Tue Dec 3 13:08:41 1996



RHOMBUS

PROJECT=RHOMBUS
ENGINEER=DOUG HUARD

PHONE: 627-2946

DATE: 3/11/96

SHEET: 12 OF: 17 SHEET_NAME: FPSCAN

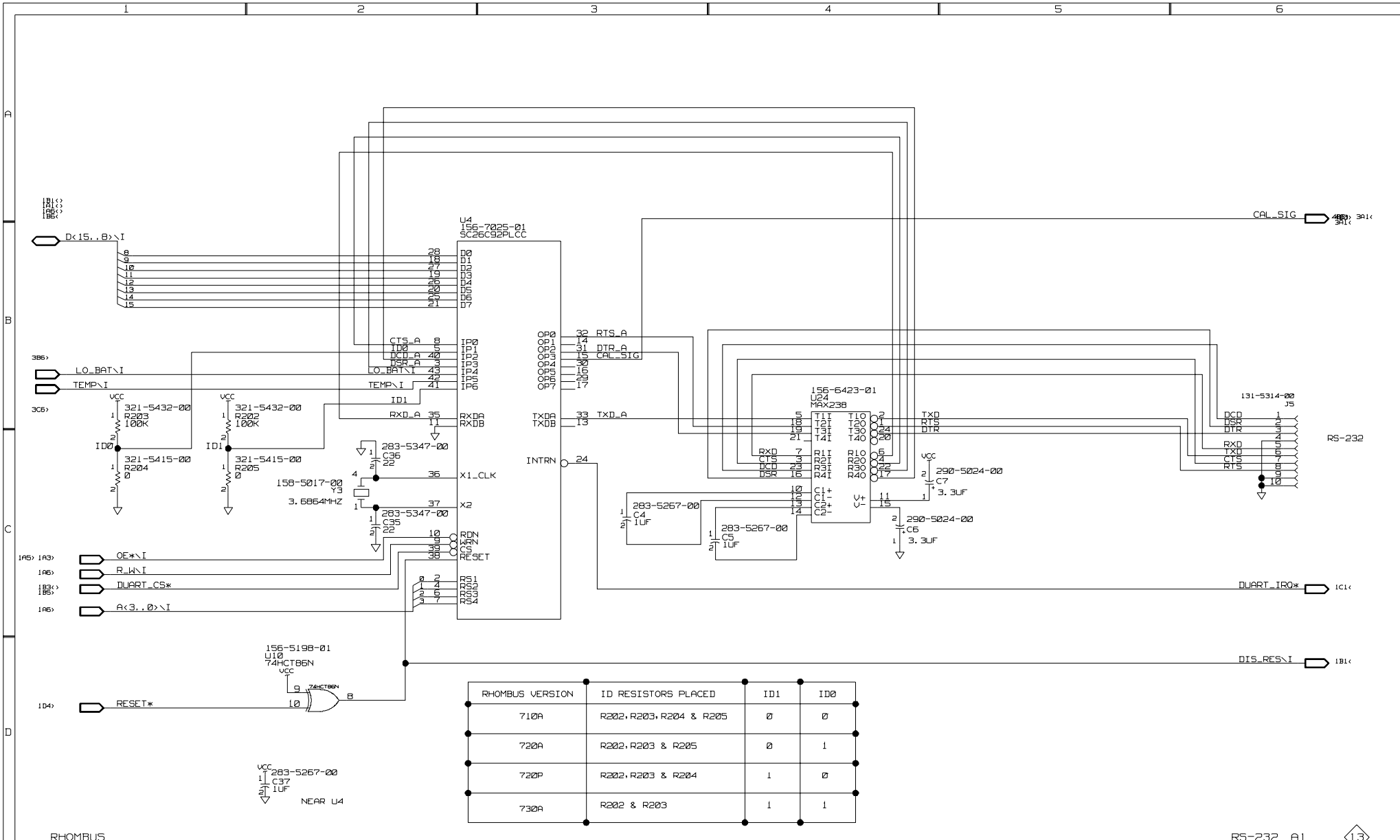
FPSCAN A1

12

DRAWING

TITLE=CPU
ABBREV=CPU
LAST_MODIFIED=Tue Dec 3 13:08:46 1996

ASSEMBLY NUMBER:



RHOMBUS VERSION	ID RESISTORS PLACED	ID1	ID0
710A	R202, R203, R204 & R205	0	0
720A	R202, R203 & R205	0	1
720P	R202, R203 & R204	1	0
730A	R202 & R203	1	1

RHOMBUS

DRAWING
 TITLE=CPU
 ABBREV=CPU
 LAST_MODIFIED= Tue Dec 3 13:08:50 1996

PROJECT=RHOMBUS
 ENGINEER=DOUG HUARD

PHONE: 627-2946

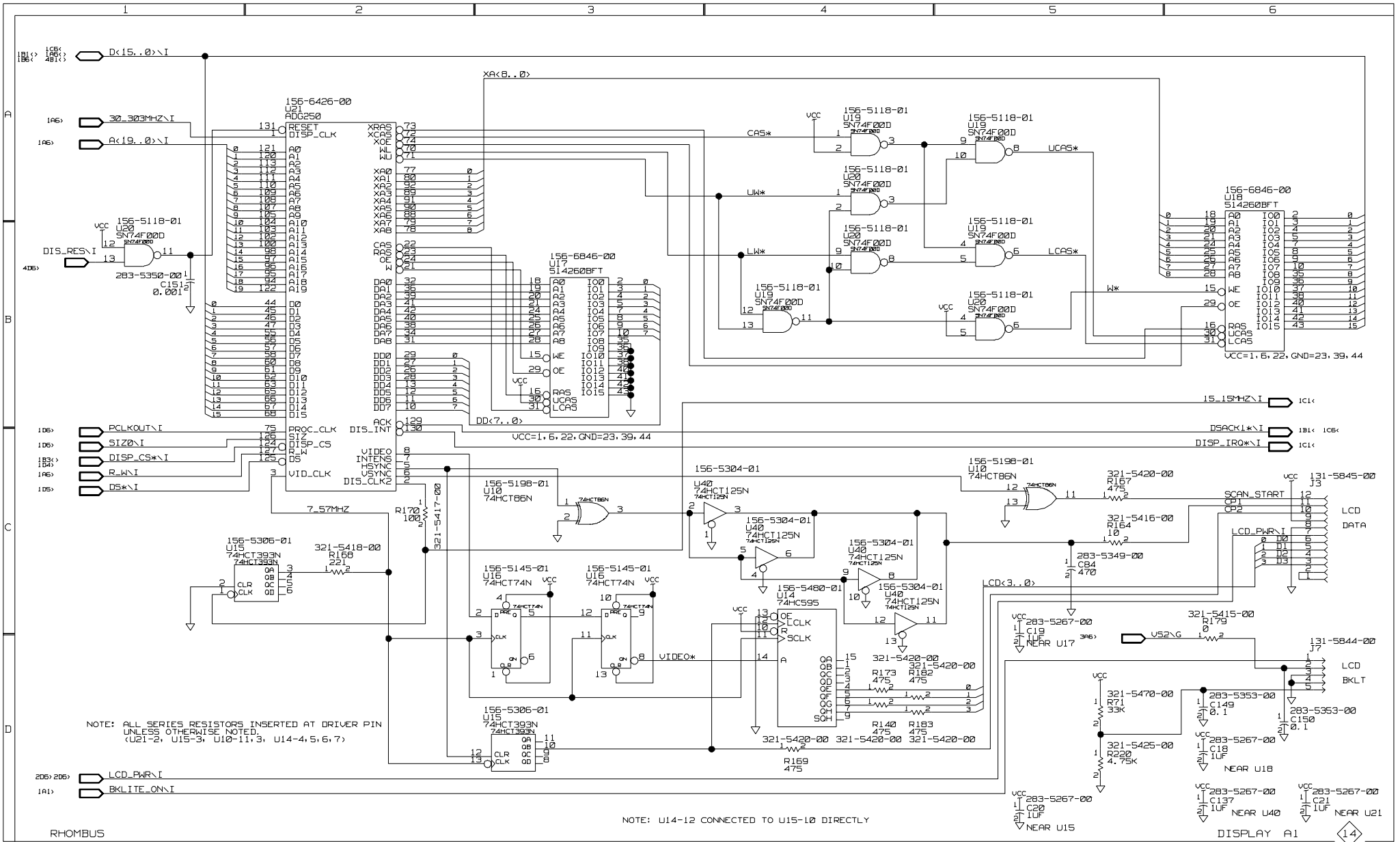
DATE: 3/11/96

SHEET: 13 OF: 17

SHEET_NAME: RS-232

RS-232 A1

ASSEMBLY NUMBER:



NOTE: ALL SERIES RESISTORS INSERTED AT DRIVER PIN UNLESS OTHERWISE NOTED.
(U21-2, U15-3, U10-11,3, U14-4,5,6,7)

NOTE: U14-12 CONNECTED TO U15-10 DIRECTLY

RHOMBUS

PROJECT=RHOMBUS
ENGINEER=DOUG HUARD

PHONE: 627-2946

DATE: 3/11/96

SHEET: 14 OF: 17 SHEET_NAME: DISPLAY

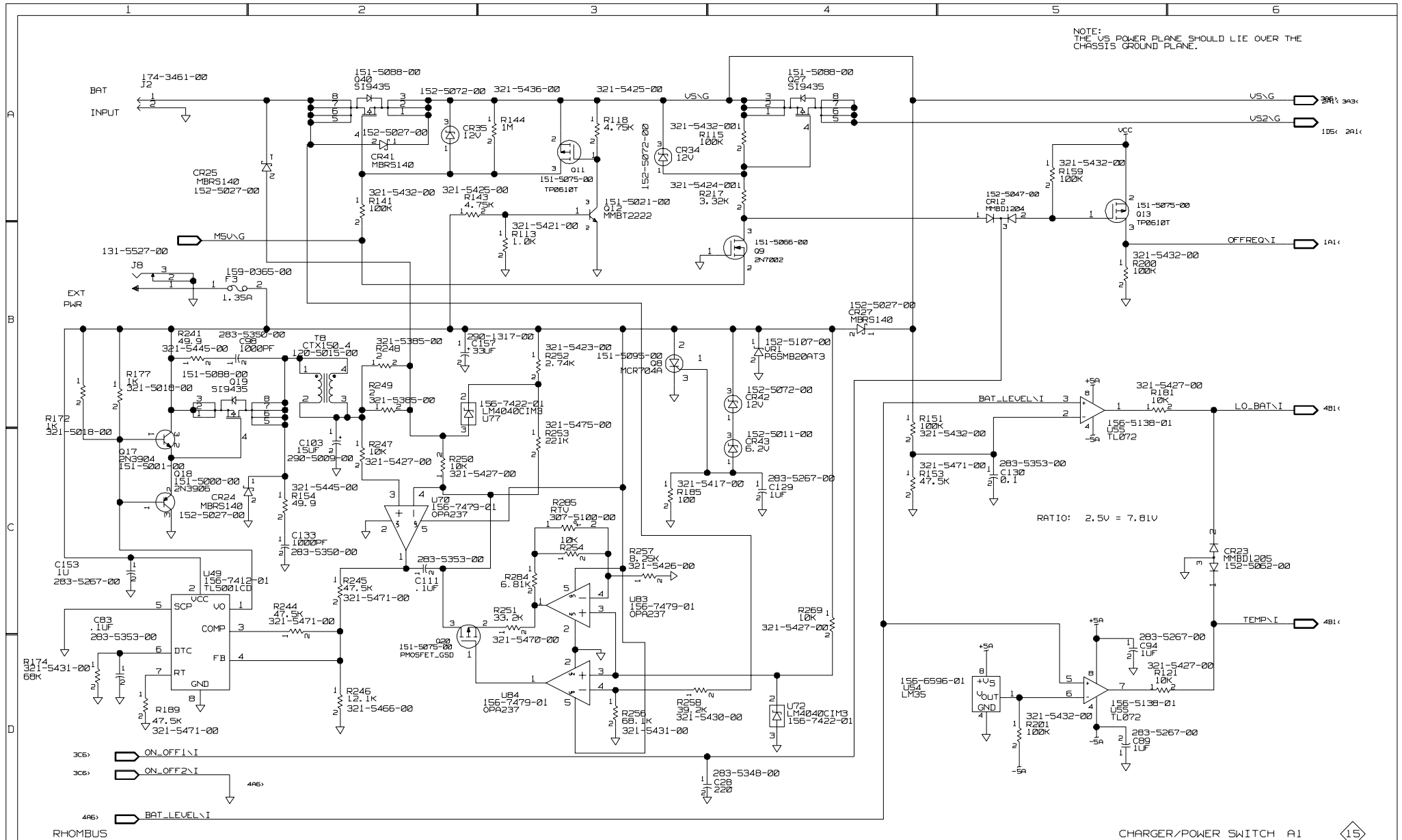
ASSEMBLY NUMBER:

DRAWING

TITLE=DISPLAY

ABBREV=DISPLAY

LAST_MODIFIED= Tue Dec 3 13:28:23 1996



NOTE:
THE US POWER PLANE SHOULD LIE OVER THE
CHASSIS GROUND PLANE.

DRAWING
TITLE=PWRSPLY
ABBREV=PWRSU
LAST_MODIFIED=Tue Dec 3 13:08:11 1996

PROJECT=RHOMBUS
ENGINEER=DOUG HUARD/BERNARD RATE

PHONE: 627-5779

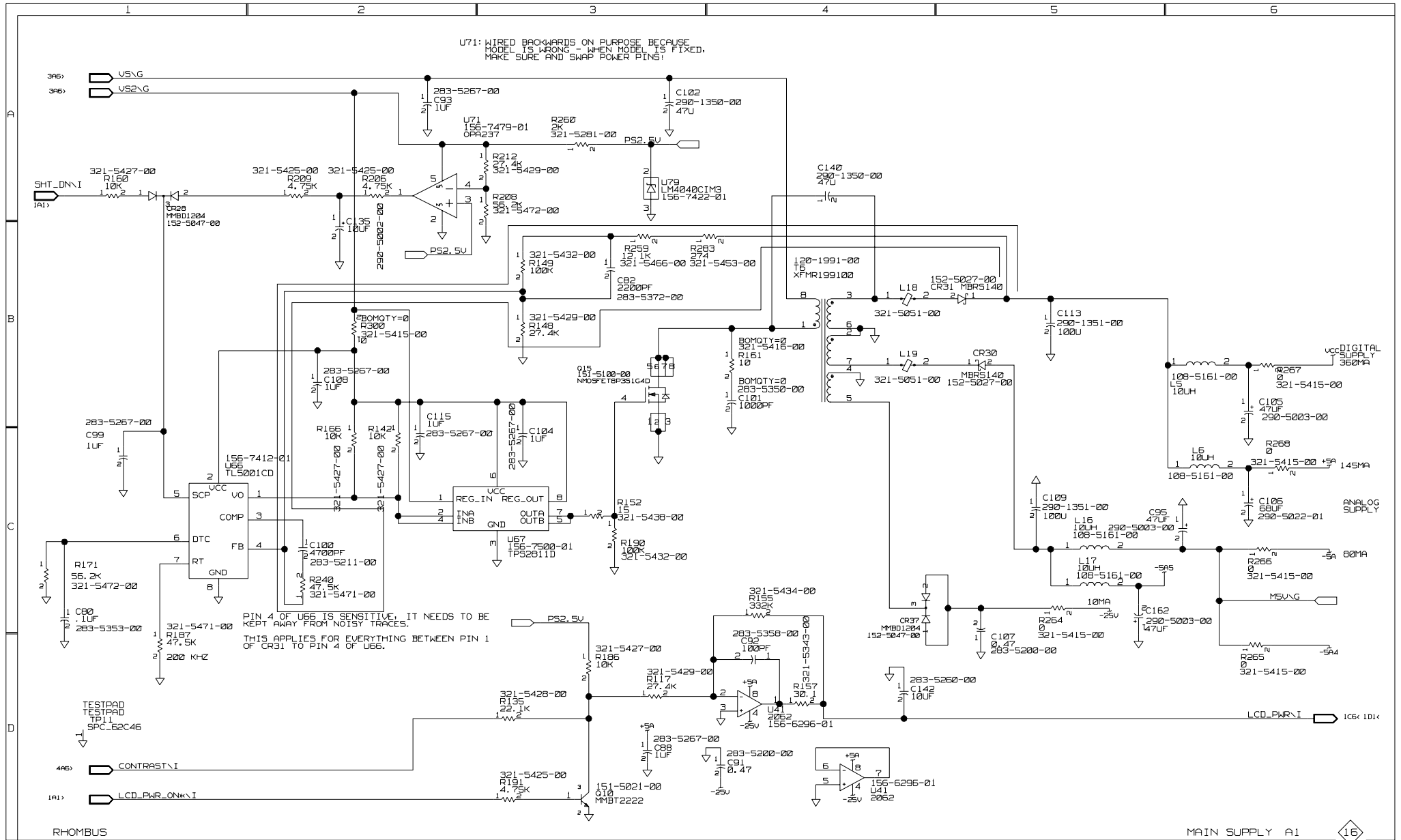
DATE: 3/11/96

SHEET: 15 OF: 17

SHEET_NAME: POWER SWITCH

CHARGER/POWER SWITCH A1

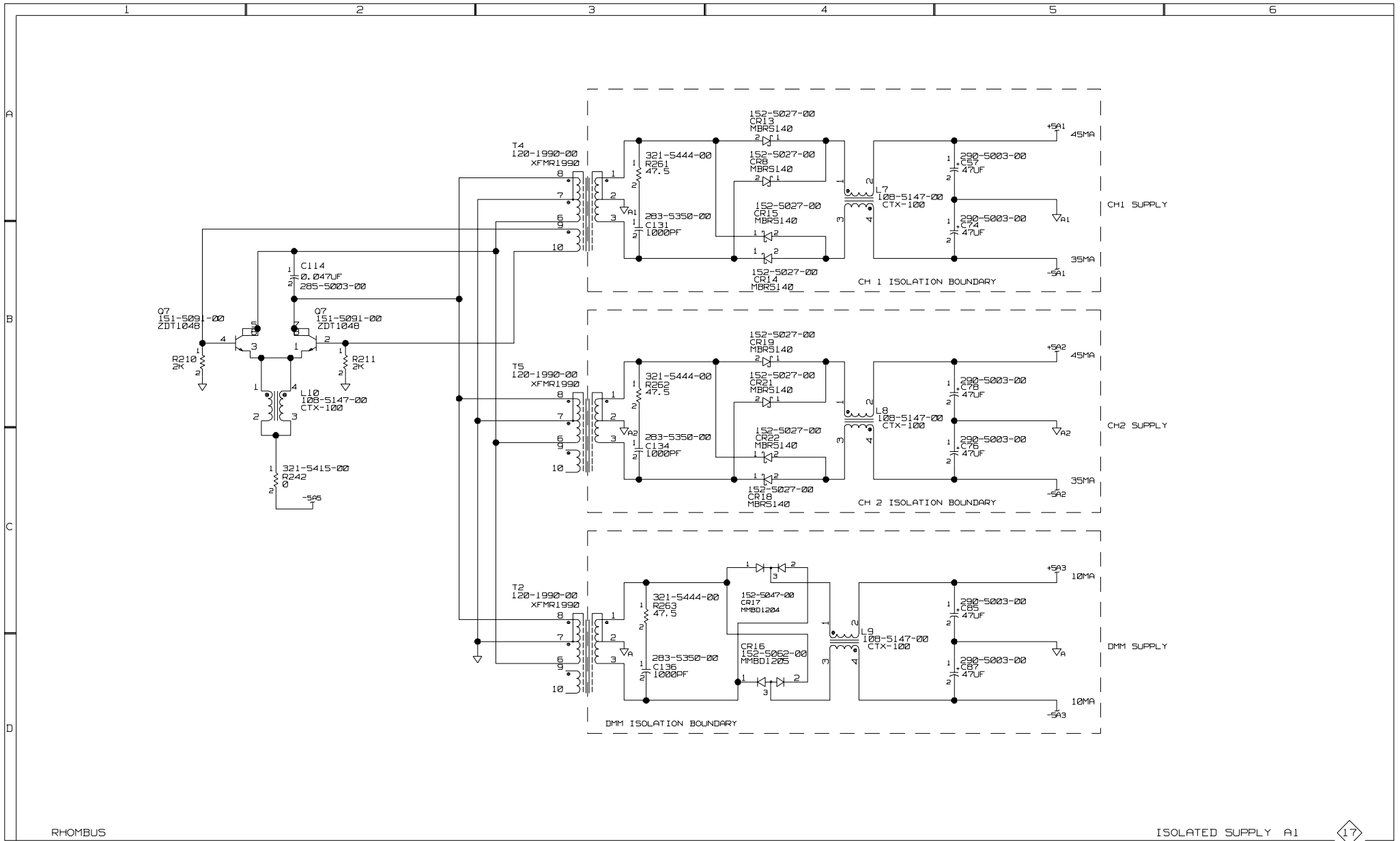
ASSEMBLY NUMBER:



DRAWING
TITLE=PWRSPLY
ABBREV=PWRSPLY
LAST_MODIFIED=Tue Dec 3 13:08:00 1996

MAIN SUPPLY A1

16



RHOMBUS

ISOLATED SUPPLY A1

17

DRAWING

PROJECT=RHOMBUS
ENGINEER=DOUG HUARD/BERNARD RATE

PHONE: 627-2946

DATE: 3/11/96

SHEET: 17 OF: 17

SHEET_NAME: ISOLATED SUPPLY

ASSEMBLY NUMBER:

TITLE=PWRSPLY

APPROV=PWRSJ
LAST_MODIFIED=Tue Dec 3 13:07:52 1996