

Dejan Sajić M.Sc.Eng.

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Personal Details

Date of Birth 01/09/1980
Nationality Serbian
Marital Status Single
Driving License B1, B, C1, C, BE

Profile

I'm a computer engineer with 2 (two) years of genuine and wide experience in SW and HW engineering. During this period I took a part in a considerable number of different projects varying in types and extent.

Key Skills:

- C/C++, Java, VHDL, HTML
- Experience with: Microsoft Visual Studio .NET C/C++, C#
- Experience with: Windows 95/98/2000/XP, Linux
- Embedded systems programming (based on DSP MAS, *Cirrus Logic* CS4952X),
- PC platform hardware and software knowledge,
- Ability to solve problems working alone or as a member of team,
- Fluent in English and Serbian. Basic knowledge of the German language.

Achievements

- DTS Legacy and NEO6
- DTS-HD Master Audio
- DTS-HD High Resolution Audio
- DTS-HD Low Bit Rate
- DTS-HD Audio Decoder implementation for Cirrus Logic CS4953X system.
- All these projects involved speed and memory requirements optimization in C/assembler, usage of simulator and profiling tools and integration into and testing of Real-Time system.

Education and Qualifications

Education:

- University of Novi Sad, Faculty of Technical Sciences in Novi Sad, Department of Computer Science and System Control. Graduated in June 2006, GPA 8.2 out of 10.00.
 - Five years program consisted of thirty-five courses.
 - Main Subjects: Applied Programming (90%), System Programming Support (100%), Software Engineering (100%), Information Structures and Databases (90%), Computer Networks (100%)
 - The courses provided an opportunity to gain knowledge and practical experience in the areas of Embedded Systems and Real-Time Systems.
 - Master Thesis: *One solution of frequent and sample resolution expansion of high quality audio decoder based on DSP processor(DTS-HD X96 and XBR component)*

Personal Qualification

- Have significant experience in signal processing design and implementation for audio devices
- Strong Embedded C/C++ and Assembler programming skills
- Embedded DSP software development
- DSP assembler / devices
- Real-time and embedded system design
- Experience with various embedded platforms (MAS, CS4952X, CS4953X)
- Experience in International teams
- Experience in working abroad
- Experience in handling multiple projects under tight schedule
- Willing to learn new things, open to progress and new ideas
- Can be easily adapted to new environment

Professional Experience:

Software Engineer
present Micronasnit LLC (Institute of Information Technology)
Novi Sad, Serbia

As a student:

- Design of MDCT transformation on *DSP*.
- GUI designs and implementation in C++.

As a member of development team:

- Implementation of X96 and XBR components for DTS-HD audio decoder on Cirrus Logic CS4952X processor.
- DTS Legacy and NEO6
- DTS-HD Low Bit Rate
- Implementation of 64Bands and 32Bands perfect reconstruction filter banks (24 Bits) for DTS-HD Lossless audio decoder on Cirrus Logic CS4953X processor.
- Testing framework development (C/C++) for DTS-HD based on DTS-HD test vectors.

As a front man I was working on following projects:

- DTS-HD Master Audio
- DTS-HD High Resolution Audio

Interests

My personal concentration was primarily on designing of Embedded and Real-Time system software.

Publications:

- National conference ETRAN, "One realization of the expanding number of channels for the specific audio decoder class Milan Ačanski, Robert Pečkai-Kovač, Dejan Sajić, Herceg Novi, 2007. <http://etran.etf.bg.ac.yu/>
- National conference ETRAN, One solution of frequent and sample resolution expansion of high quality audio decoder based on DSP processor, Dejan Sajić, Marko Gajić, Herceg Novi 2007. <http://etran.etf.bg.ac.yu/>
- National conference TELFOR, "One solution for DTSHD Master Audio decoder optimization on dual core digital signal processor| Dejan Sajić, Nenad Četić, Marija Tadić, Jelena Kovačević, Miroslav Popović, Vladimir Kovačević, Belgrade 2007 www.telfor.org.yu