

A.7.3.3 Emulation Single-Chip Mode (Without Wait States)

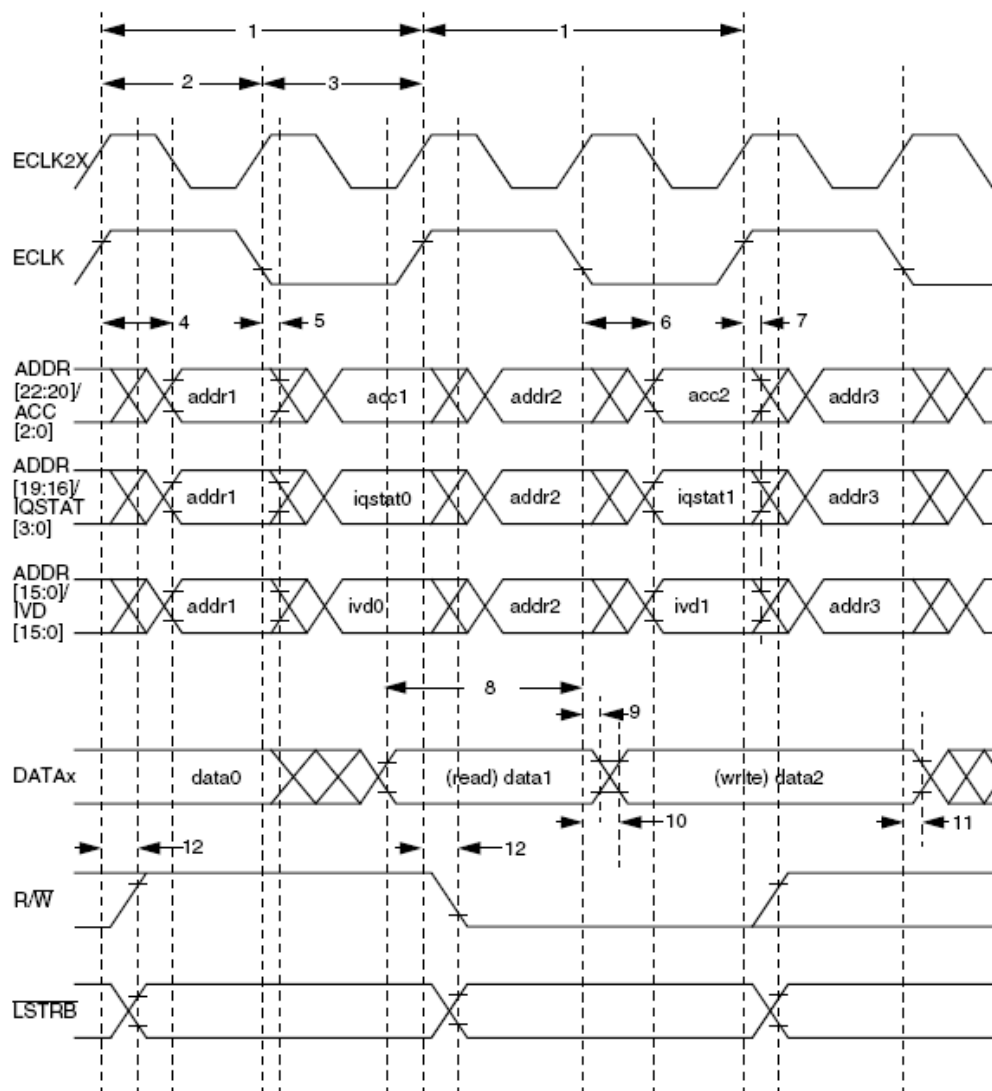


Figure A-15. Example 2a: Emulation Single-Chip Mode — Read Followed by Write

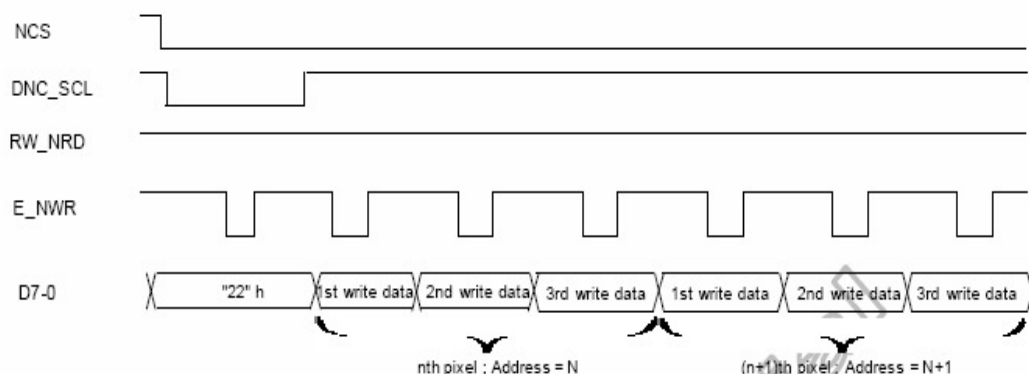
>> HX8346-A

240RGB x 320 dot, 262K color, TFT Mobile Single Chip Driver



DATA SHEET Preliminary V00

Write to the graphic RAM (8-bit + 8 bit + 8-bit)



Read the graphic RAM (8-bit + 8 bit + 8-bit)

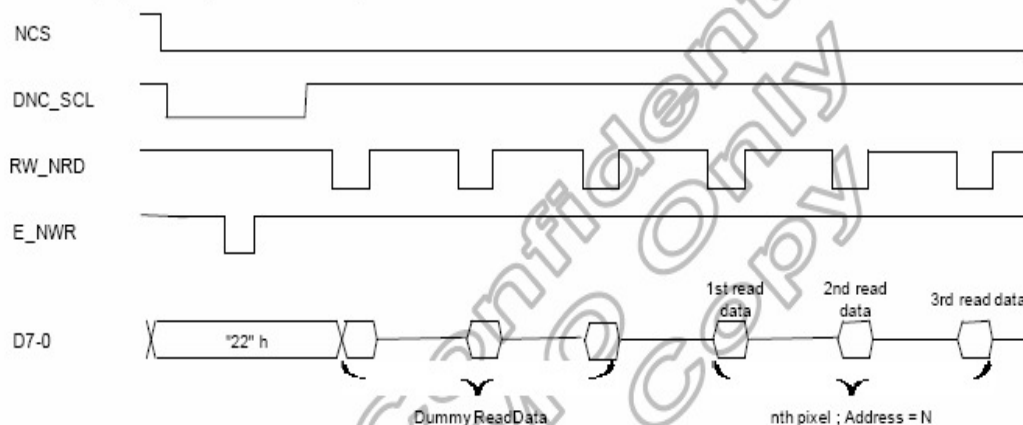


Figure 5. 3 GRAM read/write Timing in Parallel Bus System Interface (for I80 series MPU)