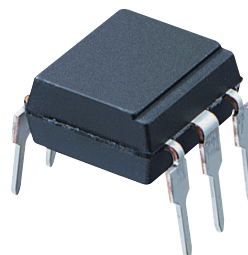


# PC715V0NSZXF Series

DIP 6 pin Darlington Phototransistor  
Output Photocoupler



## ■ Description

**PC715V0NSZXF Series** contains an IRED optically coupled to a phototransistor.

It is packaged in a 6 pin DIP.

Input-output isolation voltage(rms) is 5.0kV.

CTR is MIN. 600% at input current of 1mA.

## ■ Features

1. 6 pin DIP package
2. Double transfer mold package (Ideal for Flow Soldering)
3. Darlington phototransistor output (CTR : MIN. 600% at  $I_F=1\text{mA}$ ,  $V_{CE}=2\text{V}$ )
4. High isolation voltage between input and output ( $V_{\text{iso(rms)}} : 5.0\text{kV}$ )
5. RoHS directive compliant

## ■ Agency approvals/Compliance

1. Recognized by UL1577 (Double protection isolation), file No. E64380 (as model No. **PC715V**)
2. Approved by VDE, DIN EN60747-5-2<sup>(\*)</sup> (as an option), file No. 40008565 (as model No. **PC715V**)
3. Package resin : UL flammability grade (94V-0)

<sup>(\*)</sup>DIN EN60747-5-2 : successor standard of DIN VDE0884

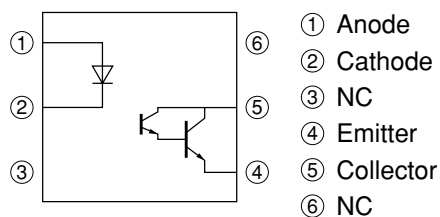
## ■ Applications

1. Home appliances
2. Programmable controllers
3. Personal computer peripherals

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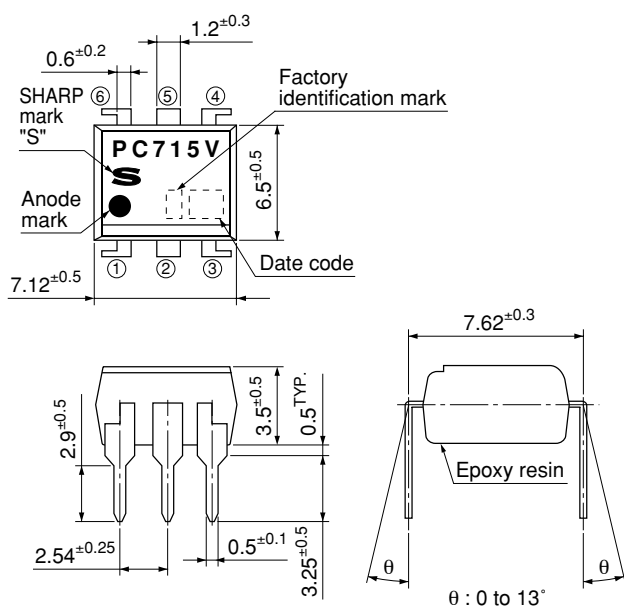
## Internal Connection Diagram



## Outline Dimensions

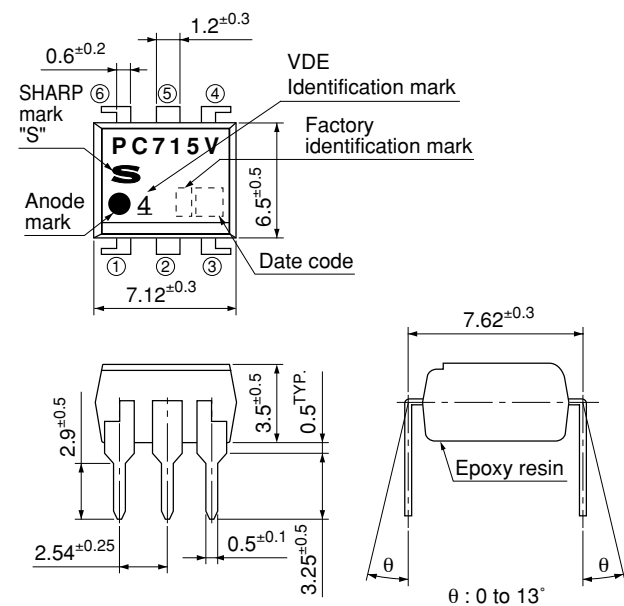
(Unit : mm)

### 1. Through-Hole [ex. PC715V0NSZXF]



Product mass : approx. 0.36g

### 2. Through-Hole (VDE option) [ex. PC715V0YSZXF]



Product mass : approx. 0.36g




Plating material : SnCu (Cu : TYP. 2%)

### Date code (2 digit)

1st digit				2nd digit	
Year of production				Month of production	
A.D.	Mark	A.D.	Mark	Month	Mark
1990	A	2002	P	January	1
1991	B	2003	R	February	2
1992	C	2004	S	March	3
1993	D	2005	T	April	4
1994	E	2006	U	May	5
1995	F	2007	V	June	6
1996	H	2008	W	July	7
1997	J	2009	X	August	8
1998	K	2010	A	September	9
1999	L	2011	B	October	O
2000	M	2012	C	November	N
2001	N	∴	∴	December	D

repeats in a 20 year cycle

### Factory identification mark

Factory identification Mark	Country of origin
no mark	Japan
	
	Indonesia
	China

\* This factory marking is for identification purpose only.  
Please Contact the local SHARP sales representative to see the actual status of the production.

### Rank mark

There is no rank mark indicator.

## ■ Absolute Maximum Ratings (T<sub>a</sub>=25°C)

	Parameter	Symbol	Rating	Unit
Input	Forward current	I <sub>F</sub>	50	mA
	*1 Peak forward current	I <sub>FM</sub>	1	A
	Reverse voltage	V <sub>R</sub>	6	V
	Power dissipation	P	70	mW
Output	Collector-emitter voltage	V <sub>CEO</sub>	35	V
	Emitter-collector voltage	V <sub>ECO</sub>	6	V
	Collector current	I <sub>C</sub>	80	mA
	Collector power dissipation	P <sub>C</sub>	150	mW
	Total power dissipation	P <sub>tot</sub>	170	mW
	Operating temperature	T <sub>opr</sub>	−25 to +100	°C
	Storage temperature	T <sub>stg</sub>	−40 to +125	°C
	*2 Isolation voltage	V <sub>iso (rms)</sub>	5	kV
	*3 Soldering temperature	T <sub>sol</sub>	260	°C

\*1 Pulse width ≤ 100μs, Duty ratio : 0.001

\*2 40 to 60%RH, AC for 1minute, f=60Hz

\*3 For 10s

## ■ Electro-optical Characteristics (T<sub>a</sub>=25°C)

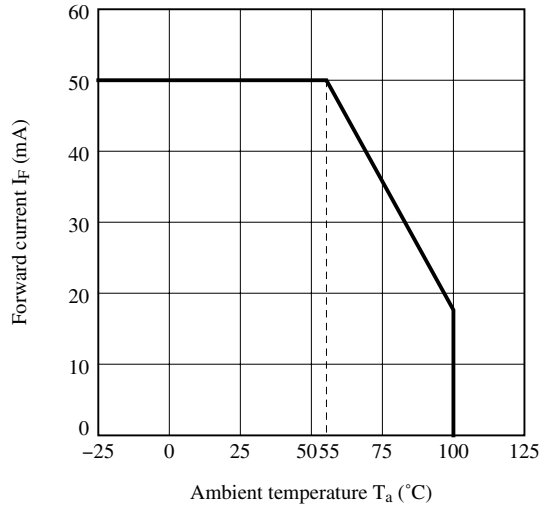
	Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Input	Forward voltage	V <sub>F</sub>	I <sub>F</sub> =10mA	–	1.2	1.4	V
	Peak forward voltage	V <sub>FM</sub>	I <sub>FM</sub> =0.5V	–	–	3.0	V
	Reverse current	I <sub>R</sub>	V <sub>R</sub> =4V	–	–	10	μA
	Terminal capacitance	C <sub>t</sub>	V=0, f=1kHz	–	30	250	pF
Output	Collector dark current	I <sub>CEO</sub>	V <sub>CE</sub> =10V, I <sub>F</sub> =0	–	–	1 000	nA
	Collector-emitter breakdown voltage	BV <sub>CEO</sub>	I <sub>C</sub> =0.1mA, I <sub>F</sub> =0	35	–	–	V
	Emitter-collector breakdown voltage	BV <sub>ECO</sub>	I <sub>E</sub> =10μA, I <sub>F</sub> =0	6	–	–	V
Transfer characteristics	Current transfer ratio	I <sub>C</sub>	I <sub>F</sub> =1mA, V <sub>CE</sub> =2V	6.0	16.0	75.0	mA
	Collector-emitter saturation voltage	V <sub>CE (sat)</sub>	I <sub>F</sub> =20mA, I <sub>C</sub> =5mA	–	–	1.0	V
	Isolation resistance	R <sub>ISO</sub>	DC500V, 40 to 60%RH	5×10 <sup>10</sup>	1×10 <sup>11</sup>	–	Ω
	Floating capacitance	C <sub>f</sub>	V=0, f=1MHz	–	0.6	1.0	pF
	Cut-off frequency	f <sub>C</sub>	V <sub>CE</sub> =2V, I <sub>C</sub> =2mA, R <sub>L</sub> =100Ω –3dB	–	6	–	kHz
	Response time	Rise time	V <sub>CE</sub> =2V, I <sub>C</sub> =10mA, R <sub>L</sub> =100Ω	–	60	250	μs
		Fall time		–	53	250	μs

**■ Model Line-up**

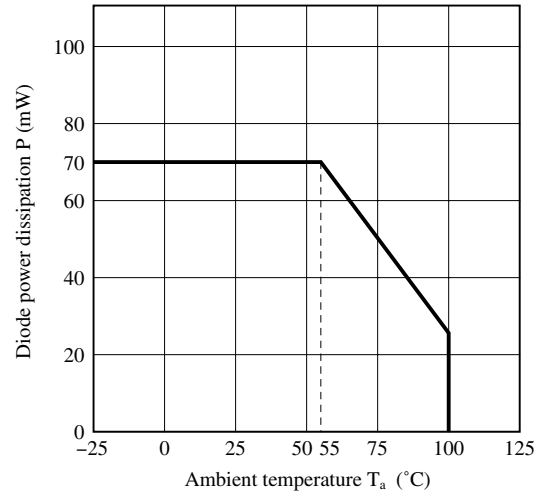
Lead Form	Through-Hole	
Package	Sleeve	
	50pcs/sleeve	
DIN EN60747-5-2	———	Approved
Model No.	<b>PC715V0NSZXF</b>	<b>PC715V0YSZXF</b>

Please contact a local SHARP sales representative to inquire about production status.

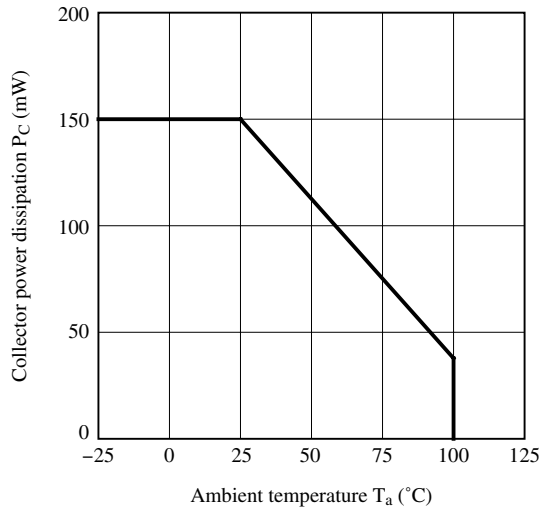
**Fig.1 Forward Current vs. Ambient Temperature**



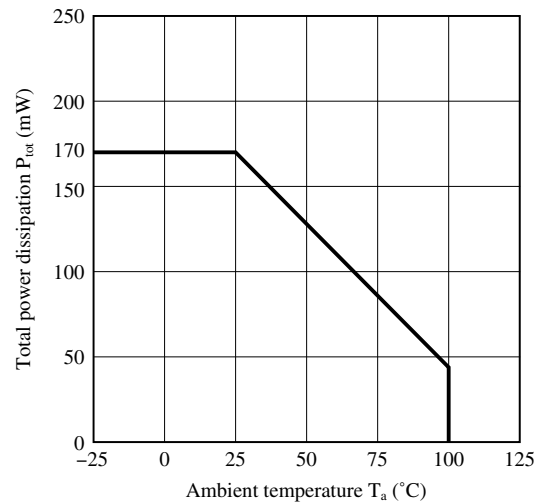
**Fig.2 Diode Power Dissipation vs. Ambient Temperature**



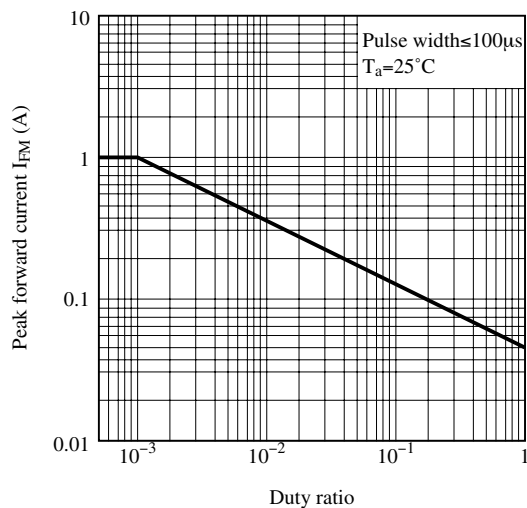
**Fig.3 Collector Power Dissipation vs. Ambient Temperature**



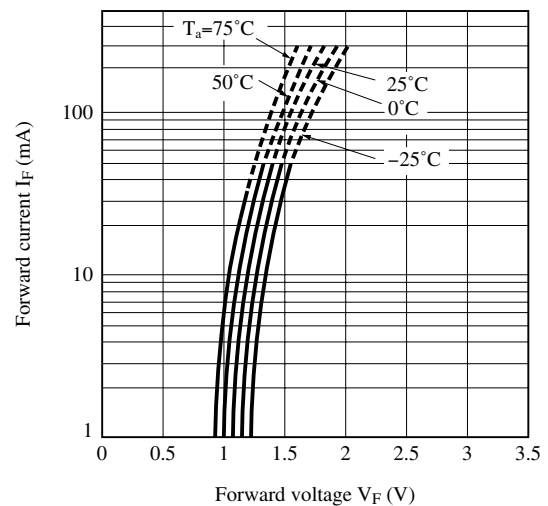
**Fig.4 Total Power Dissipation vs. Ambient Temperature**



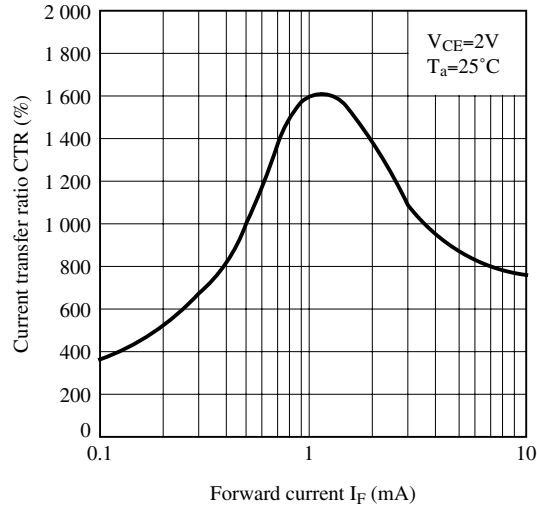
**Fig.5 Peak Forward Current vs. Duty Ratio**



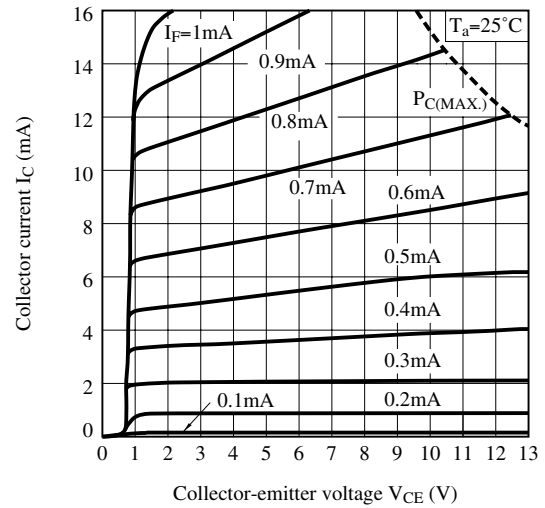
**Fig.6 Forward Current vs. Forward Voltage**



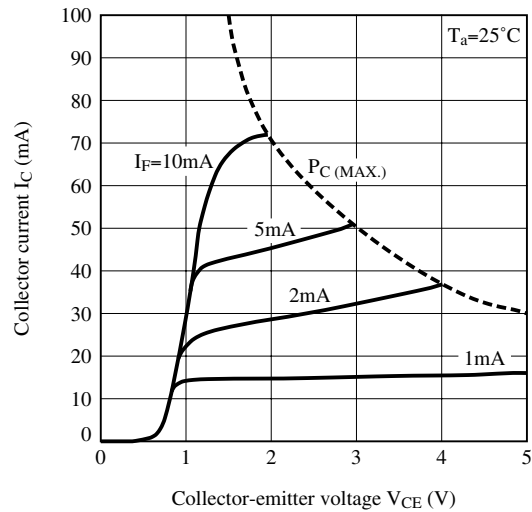
**Fig.7 Current Transfer Ratio vs. Forward Current**



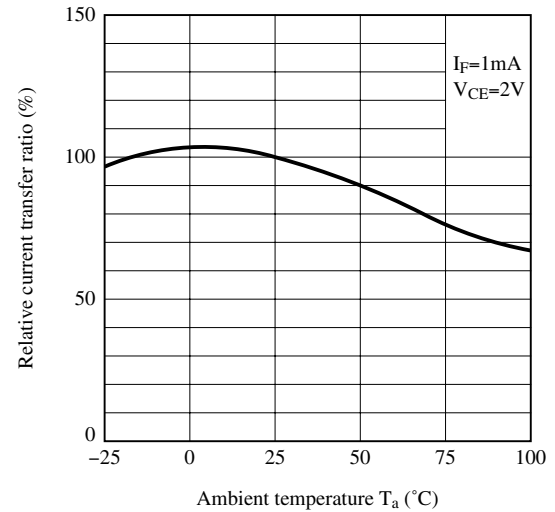
**Fig.8 Collector Current vs. Collector-emitter Voltage**



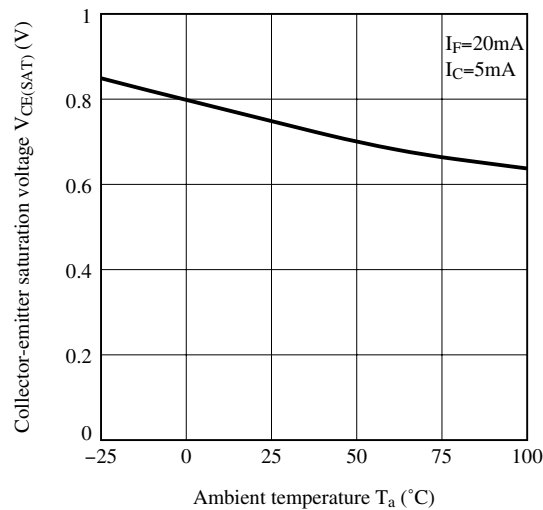
**Fig.9 Collector Current vs. Collector-emitter Voltage**



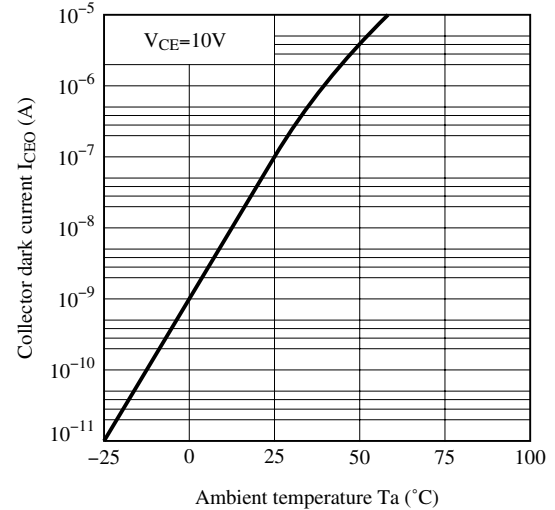
**Fig.10 Relative Current Transfer Ratio vs. Ambient Temperature**



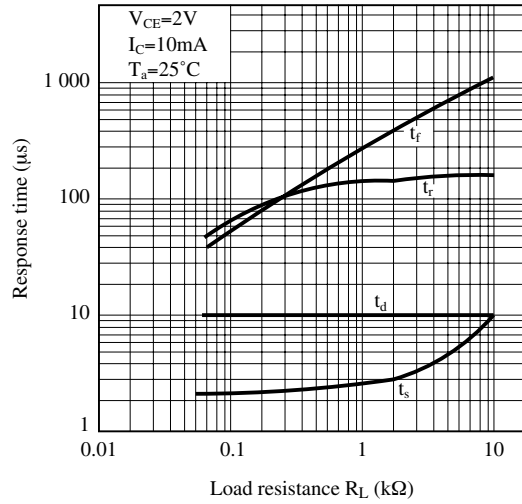
**Fig.11 Collector - emitter Saturation Voltage vs. Ambient Temperature**



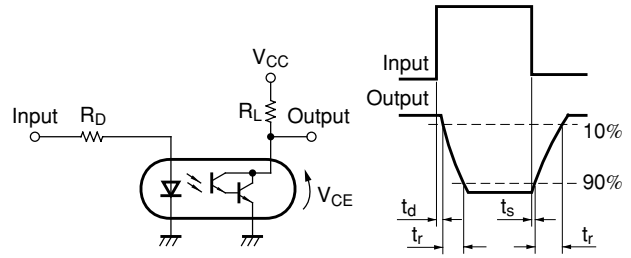
**Fig.12 Collector Dark Current vs. Ambient Temperature**



**Fig.13 Response Time vs. Load Resistance**

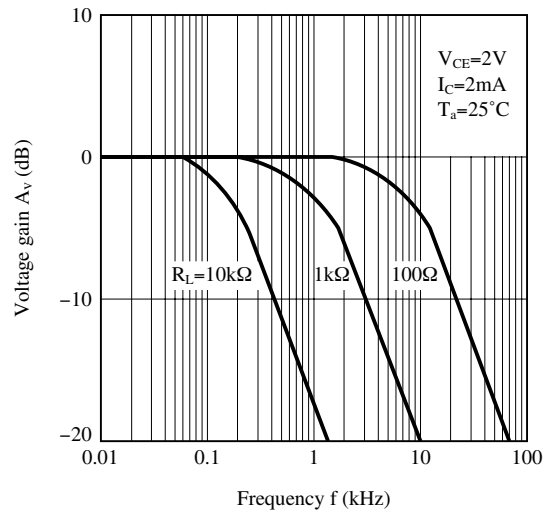


**Fig.14 Test Circuit for Response Time**

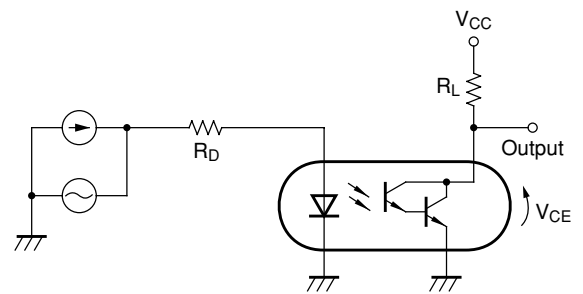


Please refer to the conditions in Fig.13

**Fig.15 Frequency Response**



**Fig.16 Test Circuit for Frequency Response**



Please refer to the conditions in Fig.15

Remarks : Please be aware that all data in the graph are just for reference and not for guarantee.



**■ Design Considerations****● Design guide**

While operating at  $I_F < 1.0\text{mA}$ , CTR variation may increase.  
Please make design considering this fact.

This product is not designed against irradiation and incorporates non-coherent IRED.

**● Degradation**

In general, the emission of the IRED used in photocouplers will degrade over time.

In the case of long term operation, please take the general IRED degradation (50% degradation over 5 years) into the design consideration.

**■ Manufacturing Guidelines****● Soldering Method****Flow Soldering :**

Due to SHARP's double transfer mold construction submersion in flow solder bath is allowed under the below listed guidelines.

Flow soldering should be completed below 270°C and within 10s.

Preheating is within the bounds of 100 to 150°C and 30 to 80s.

Please don't solder more than twice.

**Hand soldering**

Hand soldering should be completed within 3s when the point of solder iron is below 400°C.

Please don't solder more than twice.

**Other notices**

Please test the soldering method in actual condition and make sure the soldering works fine, since the impact on the junction between the device and PCB varies depending on the tooling and soldering conditions.

**● Cleaning instructions****Solvent cleaning:**

Solvent temperature should be 45°C or below Immersion time should be 3 minutes or less

**Ultrasonic cleaning:**

The impact on the device varies depending on the size of the cleaning bath, ultrasonic output, cleaning time, size of PCB and mounting method of the device.

Therefore, please make sure the device withstands the ultrasonic cleaning in actual conditions in advance of mass production.

**Recommended solvent materials:**

Ethyl alcohol, Methyl alcohol and Isopropyl alcohol

In case the other type of solvent materials are intended to be used, please make sure they work fine in actual using conditions since some materials may erode the packaging resin.

**● Presence of ODC**

This product shall not contain the following materials.

And they are not used in the production process for this product.

Regulation substances : CFCs, Halon, Carbon tetrachloride, 1.1.1-Trichloroethane (Methylchloroform)

Specific brominated flame retardants such as the PBBOs and PBBs are not used in this product at all.

This product shall not contain the following materials banned in the RoHS Directive (2002/95/EC).

- Lead, Mercury, Cadmium, Hexavalent chromium, Polybrominated biphenyls (PBB), Polybrominated diphenyl ethers (PBDE).

## ■ Package specification

### ● Sleeve package

#### Package materials

Sleeve : HIPS (with anti-static material)

Stopper : Styrene-Elastomer

#### Package method

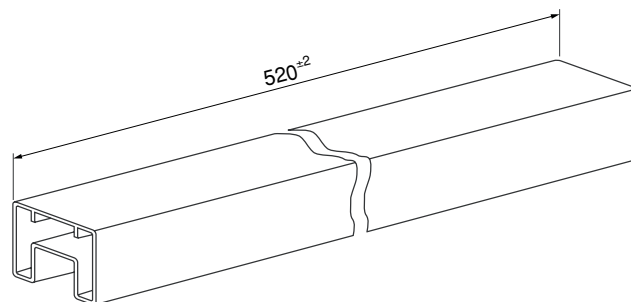
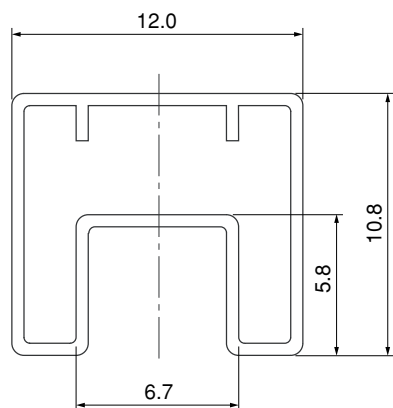
MAX. 50 pcs. of products shall be packaged in a sleeve.

Both ends shall be closed by tabbed and tabless stoppers.

The product shall be arranged in the sleeve with its anode mark on the tabless stopper side.

MAX. 20 sleeves in one case.

#### Sleeve outline dimensions



(Unit : mm)

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- Personal computers
- Office automation equipment
- Telecommunication equipment [terminal]
- Test and measurement equipment
- Industrial control
- Audio visual equipment
- Consumer electronics

(ii) Measures such as fail-safe function and redundant design should be taken to ensure reliability and safety when SHARP devices are used for or in connection

with equipment that requires higher reliability such as:

- Transportation control and safety equipment (i.e., aircraft, trains, automobiles, etc.)
- Traffic signals
- Gas leakage sensor breakers
- Alarm equipment
- Various safety devices, etc.

(iii) SHARP devices shall not be used for or in connection with equipment that requires an extremely high level of reliability and safety such as:

- Space applications
- Telecommunication equipment [trunk lines]
- Nuclear power control equipment
- Medical and other life support equipment (e.g., scuba).

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