

1C n C	SAMD11D 20-pin SOIC/ WLCSP	SAMD11D 24-pin QFN	I/O Pin	Supply	Type	EIC	REF	ADC	AC	PTC	DAC	SERCOM ⁽¹⁾	SERCOM- ALT	TC/TCC	TCC	COM	GCLK
	18 / A1	1	PA02	VDD		EXTINT[2]		AIN[0]		Y[0]	VOUT						
	19 / A2	2	PA03	VDD		EXTINT[3]	ADC/REFA DAC/REFA	AIN[1]		Y[1]							
	20 / A3	3	PA04	VDD		EXTINT[4]	ADC/REFB	AIN[2]	AIN[0]	Y[2]		SERCOM0/ PAD[2]	SERCOM0/ PAD[0]	TC1/WO[0]	TCC0/ WO[0]		
	1 / B2	4	PA05	VDD		EXTINT[5]		AIN[3]	AIN[1]	Y[3]		SERCOM0/ PAD[3]	SERCOM0/ PAD[1]	TC1/WO[1]	TCC0/ WO[1]		
	2 / A4	5	PA06	VDD		EXTINT[6]		AIN[4]	AIN[2]	Y[4]		SERCOM0/ PAD[0]	SERCOM0/ PAD[2]	TC2/WO[0]	TCC0/ WO[2]		
	3 / B3	6	PA07	VDD		EXTINT[7]		AIN[5]	AIN[3]	Y[5]		SERCOM0/ PAD[1]	SERCOM0/ PAD[3]	TC2/WO[1]	TCC0/ WO[3]		
	4 / B4	7	PA08	VDD		EXTINT[6]						SERCOM1/ PAD[2]	SERCOM0/ PAD[2]	TCC0/WO[2]	TCC0/ WO[4]		GCLK_IO[0]
	5 / C4	8	PA09	VDD		EXTINT[7]						SERCOM1/ PAD[3]	SERCOM0/ PAD[3]	TCC0/WO[3]	TCC0/ WO[5]		GCLK_IO[1]
		9	PA10	VDD		EXTINT[2]		AIN[8]	CMP[0]	X[2]/Y[8]		SERCOM0/ PAD[2]	SERCOM2/ PAD[2]	TC2/WO[0]	TCC0/ WO[2]		GCLK_IO[4]
		10	PA11	VDD		EXTINT[3]		AIN[9]	CMP[1]	X[3]/Y[9]		SERCOM0/ PAD[3]	SERCOM2/ PAD[3]	TC2/WO[1]	TCC0/ WO[3]		GCLK_IO[5]
	6 / D4	11	PA14	VDD	I ² C	NMI		AIN[6]	CMP[0]	X[0]/Y[6]		SERCOM0/ PAD[0]	SERCOM2/ PAD[0]	TC1/WO[0]	TCC0/ WO[0]		GCLK_IO[4]
	7 / E4	12	PA15	VDD	I ² C	EXTINT[1]		AIN[7]	CMP[1]	X[1]/Y[7]		SERCOM0/ PAD[1]	SERCOM2/ PAD[1]	TC1/WO[1]	TCC0/ WO[1]		GCLK_IO[5]
	8 / C3	13	PA16	VDD		EXTINT[0]				X[4]/Y[10]		SERCOM1/ PAD[2]	SERCOM2/ PAD[2]	TC1/WO[0]	TCC0/ WO[6]		GCLK_IO[2]
		14	PA17	VDD		EXTINT[1]				X[5]/Y[11]		SERCOM1/ PAD[3]	SERCOM2/ PAD[3]	TC1/WO[1]	TCC0/ WO[7]		GCLK_IO[3]
	9 / E3	15	PA22	VDD	I ² C	EXTINT[6]				X[6]/Y[12]		SERCOM1/ PAD[0]	SERCOM2/ PAD[0]	TC1/WO[0]	TCC0/ WO[4]		GCLK_IO[1]