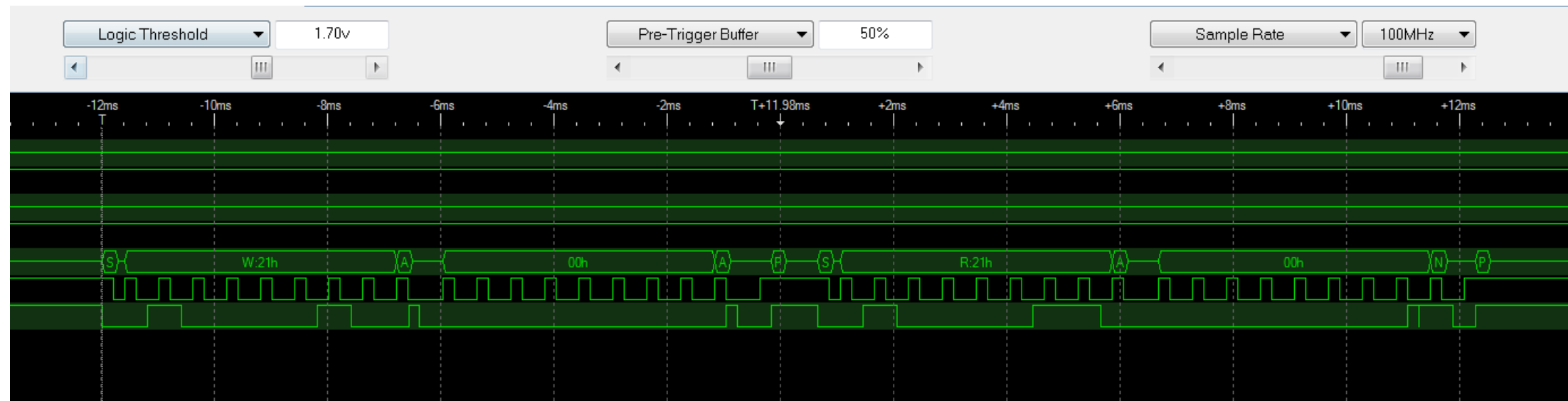
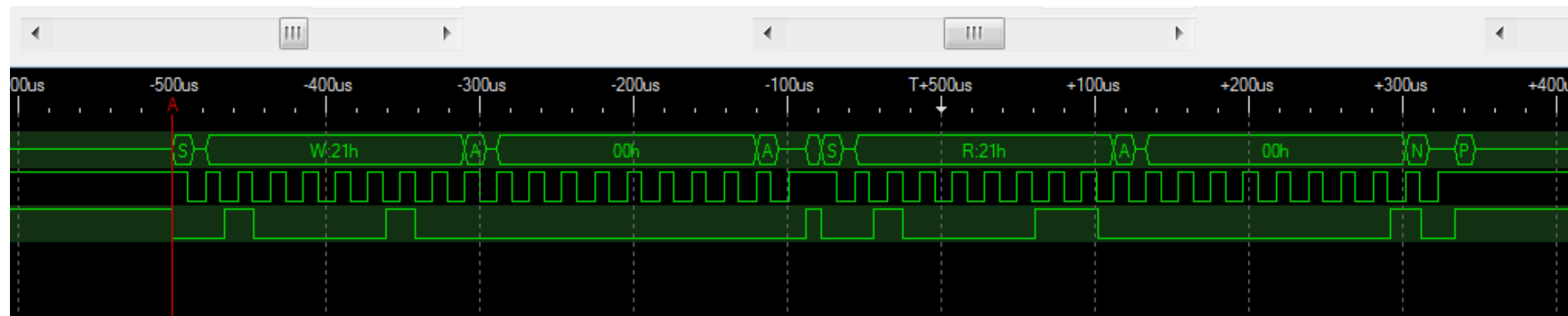


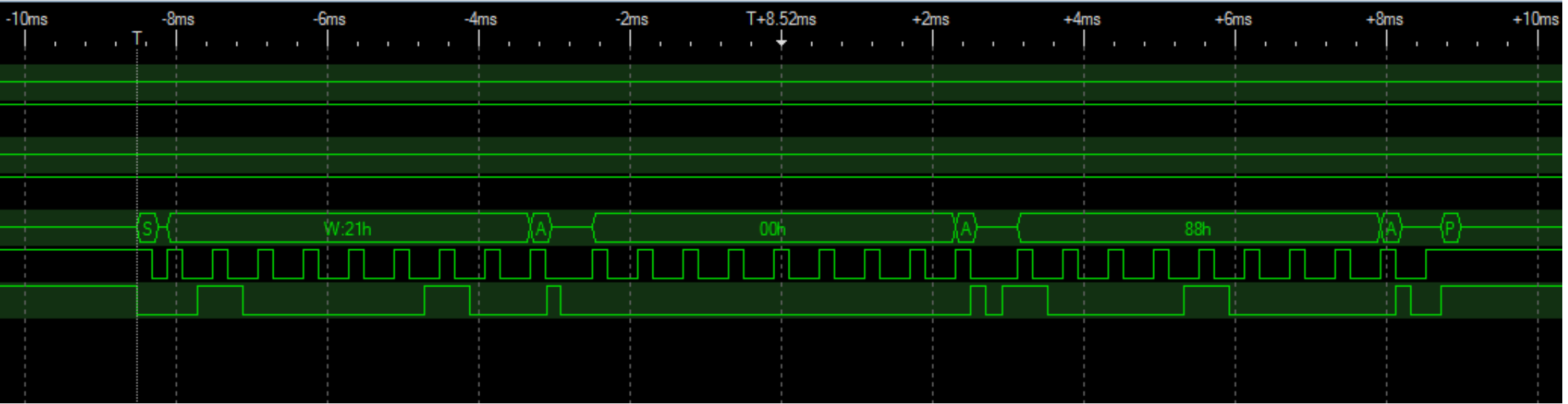
Read Register 0x00 (ATmega328P <-> OV7670_with_FiFo)



Read Register 0x00 (STM32F429 <-> OV7670_with_FiFo)



Write Register 0x00, Value 0x88 (ATmega328P <-> OV7670_with_FiFo)



Write Register 0x00, Value 0x88 (STM32F429 <-> OV7670_with_FiFo)

