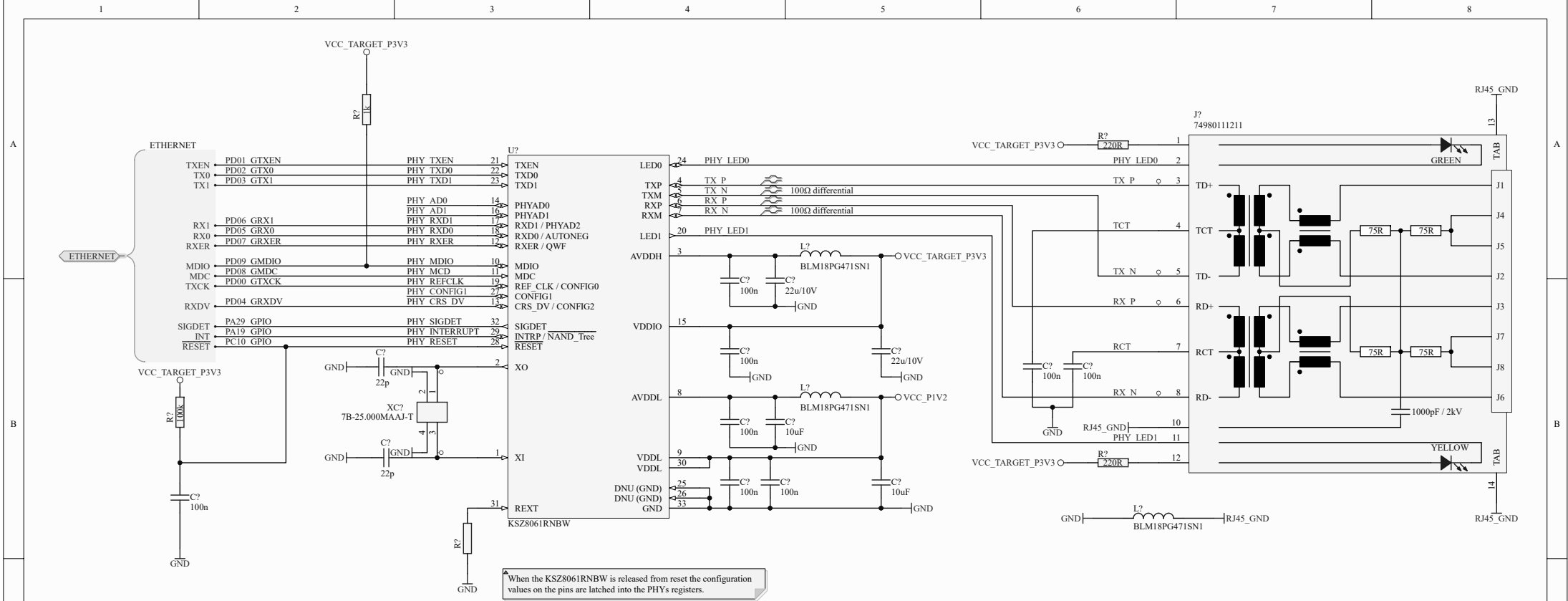




When the KSZ8061RNBW is released from reset the configuration values on the pins are latched into the PHYs registers.

PHYAD[2:0] is used to set the PHY's address:
001 (default on board)
010
....
110
111

CONFIG[2:0] is used to select operation mode.

001	= RMII normal, MDI/MDI-X disabled
101	= RMII back to back
111	= RMII normal MDI/MDI-X enabled

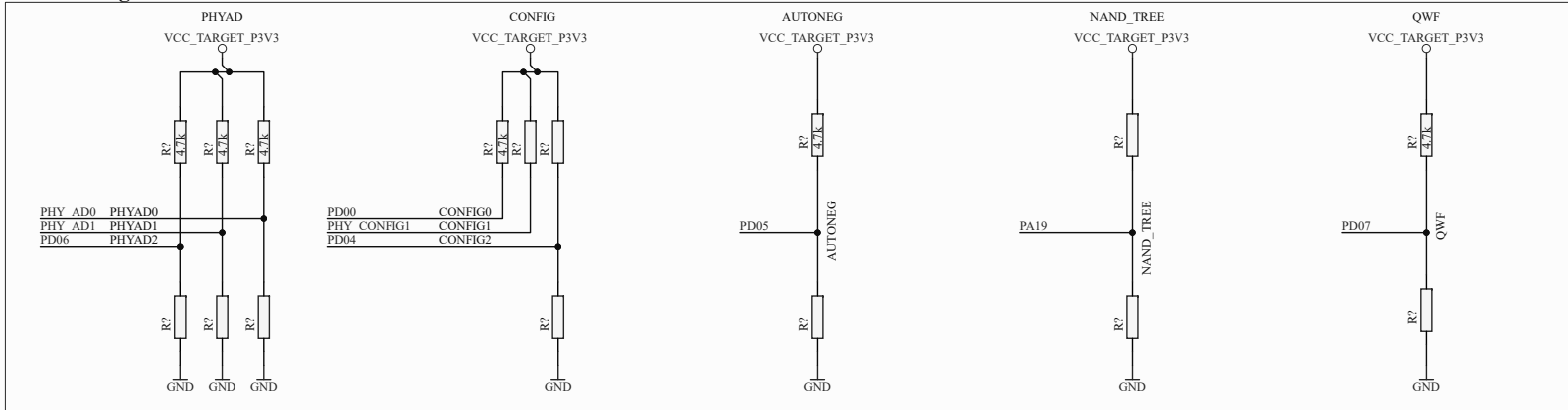
Not configurable on board as back to back

AUTONEG is used to select if auto negotiation of link speed should be enabled.
1 = disable Auto-Negotiation
0 = enable Auto-Negotiation (default on board)

NAND_TREE enables a test mode for all pins connected to the SAMV71Q21.
1 = Disable NAND_TREE (default on board)
0 = Enable NAND_TREE

▲ QWF is used to enable/disable Quiet-WIRE Filtering
1 = Disale Quiet-WIRE Filtering
0 = Enable Quiet-WIRE Filtering (default on board)

PHY Configuration



Title		
Size A3	Number	Revision
Date:	18.08.2019	Sheet of
File:	Sheet2.SchDoc	Drawn By: