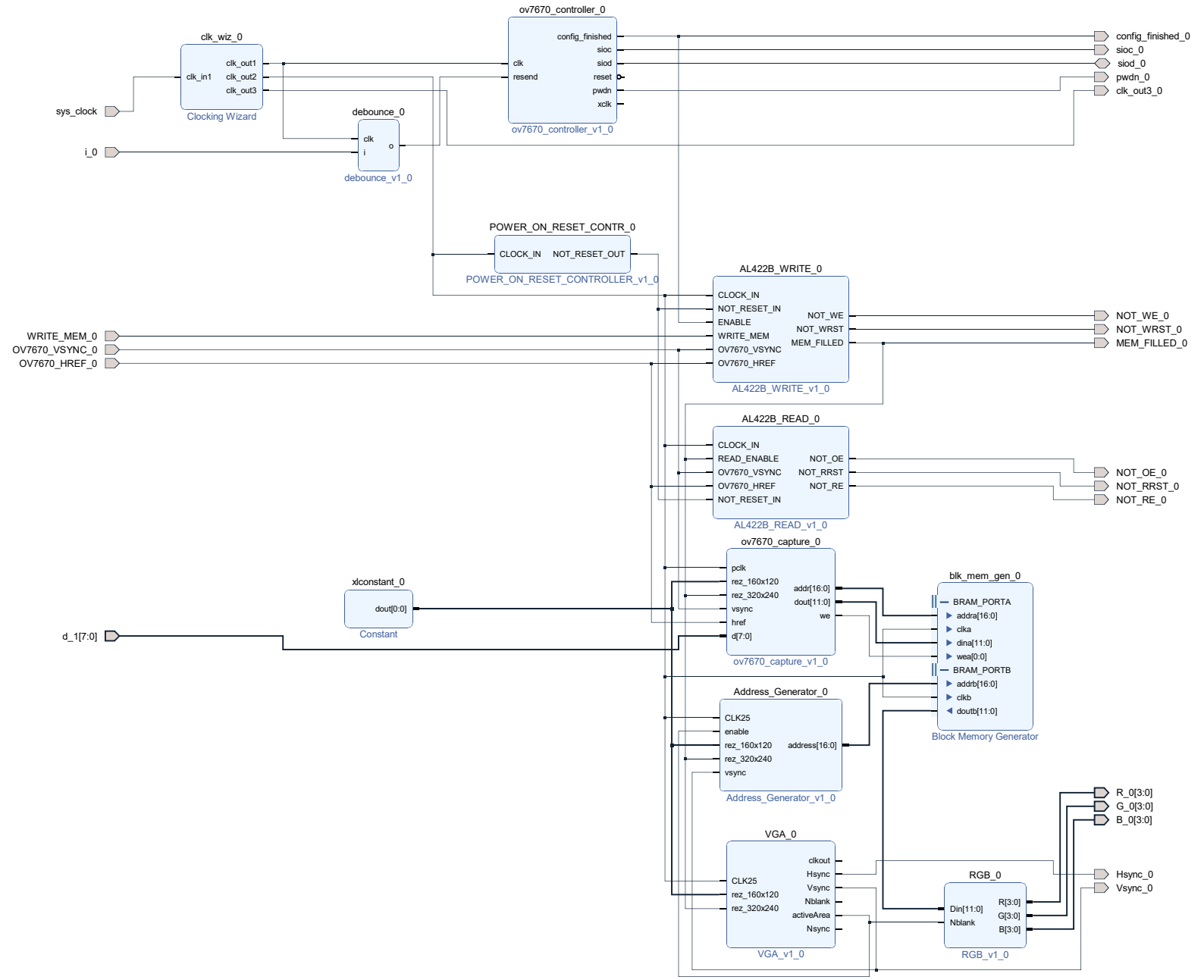
ov7670_controller_0
config_finished
sioc
siod
reset
pwdn
xclk
clk
resend
ov7670_controller_v1_0
config_finished_0
sioc_0
siod_0
pwdn_0
clk_out3_0
clk_wiz_0
clk_out1
clk_out2
clk_out3
clk_in1
Clocking Wizard
sys_clock
debounce_0
clk
i
o
debounce_v1_0
i_0
POWER_ON_RESET_CONTR_0
CLOCK_IN
NOT_RESET_OUT
POWER_ON_RESET_CONTROLLER_v1_0
AL422B_WRITE_0
CLOCK_IN
NOT_RESET_IN
ENABLE
WRITE_MEM
OV7670_VSYNC
OV7670_HREF
NOT_WE
NOT_WRST
MEM_FILLED
AL422B_WRITE_v1_0
NOT_WE_0
NOT_WRST_0
MEM_FILLED_0
WRITE_MEM_0
OV7670_VSYNC_0
OV7670_HREF_0
AL422B_READ_0
CLOCK_IN
READ_ENABLE
OV7670_VSYNC
OV7670_HREF
NOT_RESET_IN
NOT_OE
NOT_RRST
NOT_RE
AL422B_READ_v1_0
NOT_OE_0
NOT_RRST_0
NOT_RE_0
ov7670_capture_0
pclk
rez_160x120
rez_320x240
vsync
href
d[7:0]
addr[16:0]
dout[11:0]
we
ov7670_capture_v1_0
xlconstant_0
dout[0:0]
Constant
d_1[7:0]
blk_mem_gen_0
BRAM_PORTA
addra[16:0]
clka
dina[11:0]
wea[0:0]
BRAM_PORTB
addrb[16:0]
clkb
doutb[11:0]
Block Memory Generator
Address_Generator_0
CLK25
enable
rez_160x120
rez_320x240
vsync
address[16:0]
Address_Generator_v1_0
VGA_0
CLK25
rez_160x120
rez_320x240
clkout
Hsync
Vsync
Nblank
activeArea
Nsync
VGA_v1_0
RGB_0
Din[11:0]
Nblank
R[3:0]
G[3:0]
B[3:0]
RGB_v1_0
R_0[3:0]
G_0[3:0]
B_0[3:0]
Hsync_0
Vsync_0